



## UT35P06

Preliminary

POWER MOSFET

### -35A, -60V P-CHANNEL POWER MOSFET

#### DESCRIPTION

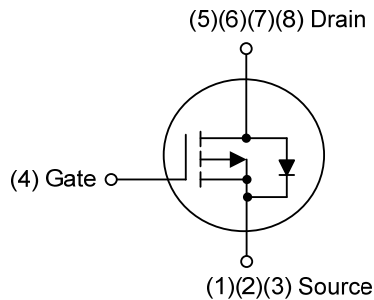
The UTC **UT35P06** is P-channel enhancement mode power MOSFET using UTC's advanced technology to provide customers with ideal for low voltage inverter applications.

The UTC **UT35P06** is suitable for high efficiency synchronous rectification in SMPS, UPS, hard switched and high frequency circuits.

#### FEATURES

- \*  $R_{DS(ON)} \leq 30 \text{ m}\Omega$  @  $V_{GS} = -10\text{V}$ ,  $I_D = -17.5\text{A}$
- $R_{DS(ON)} \leq 40 \text{ m}\Omega$  @  $V_{GS} = -4.5\text{V}$ ,  $I_D = -17.5\text{A}$
- \* High Cell Density Trench Technology
- \* High Power and Current Handling Capability

#### SYMBOL



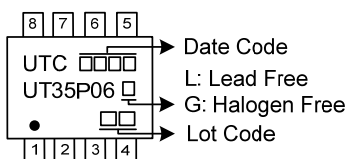
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT35P06L-S08-R	UT35P06G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<b>UT35P06G-S08-R</b> 		(1) R: Tape Reel (2) S08: SOP-8 (3) G: Halogen Free and Lead Free, L: Lead Free
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C=25^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DS}$	-60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	Continuous	$I_D$	-35	A
Pulsed Drain Current	Pulsed (Note 2)	$I_{DM}$	-70	A
Single Pulsed Avalanche Energy (Note 3)		$E_{AS}$	45	mJ
Power Dissipation		$P_D$	2.7	W
Junction Temperature		$T_J$	+150	$^{\circ}\text{C}$
Storage Temperature Range		$T_{STG}$	-55 ~ +150	$^{\circ}\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=0.1\text{mH}$ ,  $I_{AS}=-30\text{A}$ ,  $V_{DD}=-50\text{V}$ ,  $R_G=25\ \Omega$ , Starting  $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		$\theta_{JA}$	125	$^{\circ}\text{C/W}$
Junction to Case		$\theta_{JC}$	45	$^{\circ}\text{C/W}$

Note: Device mounted on FR-4 substrate  $P_C$  board, 2oz copper, with 1inch square copper plate.

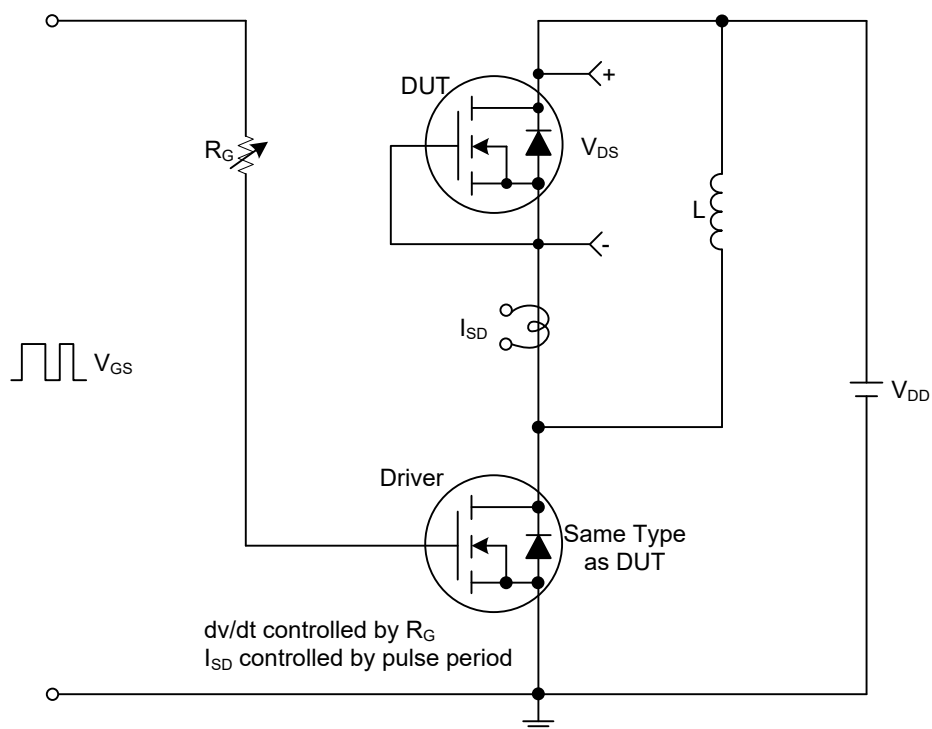
■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	-60			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C			-1	μA
Gate-Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
	Reverse		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-17.5A			30	mΩ
			V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-17.5A			40	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-25V, f=1.0MHz		2900		pF
Output Capacitance		C <sub>OSS</sub>			235		pF
Reverse Transfer Capacitance		C <sub>RSS</sub>			160		pF
SWITCHING PARAMETERS							
Total Gate Charge (Note 1)		Q <sub>G</sub>	V <sub>DS</sub> =-48V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-35A, (Note 1, 2)		60		nC
Gate to Source Charge		Q <sub>GS</sub>			9		nC
Gate to Drain Charge		Q <sub>GD</sub>			17		nC
Turn-on Delay Time (Note 1)		t <sub>D(ON)</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-35A, R <sub>G</sub> =3Ω (Note 1, 2)		8		ns
Rise Time		t <sub>R</sub>			18		ns
Turn-off Delay Time		t <sub>D(OFF)</sub>			70		ns
Fall-Time		t <sub>F</sub>			35		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current		I <sub>S</sub>				-35	A
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				-70	A
Drain-Source Diode Forward Voltage (Note 1)		V <sub>SD</sub>	I <sub>S</sub> =-35A, V <sub>GS</sub> =0V			-1.4	V
Body Diode Reverse Recovery Time		t <sub>rr</sub>	I <sub>S</sub> =-35A, V <sub>GS</sub> =0V,		60		ns
Reverse Recovery Charge		Q <sub>rr</sub>	dl <sub>F</sub> /dt=100A/μs (Note 1)		90		ns

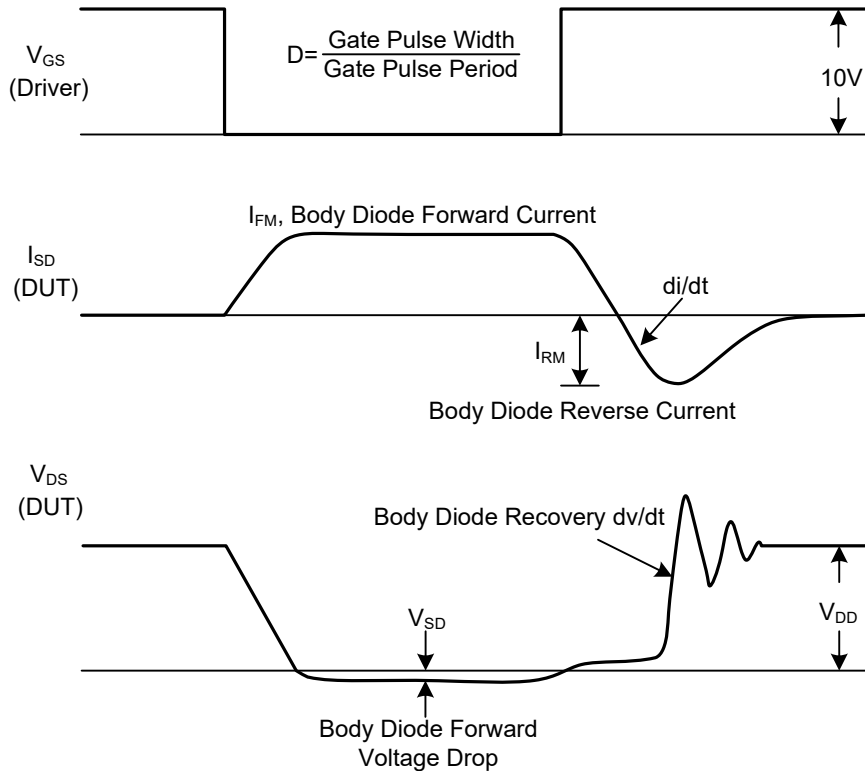
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

# ■ TEST CIRCUITS AND WAVEFORMS



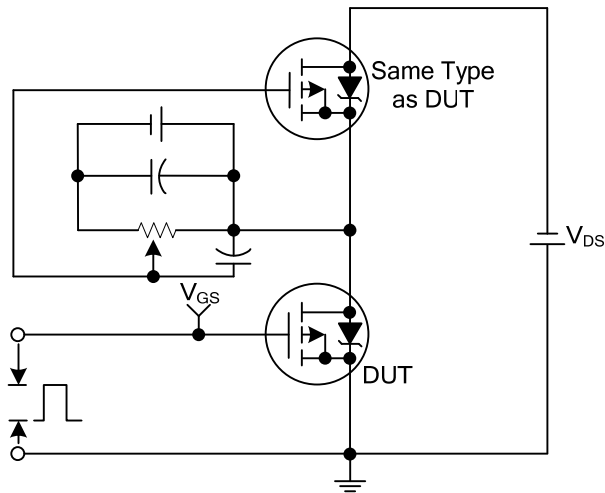
**Peak Diode Recovery dv/dt Test Circuit**



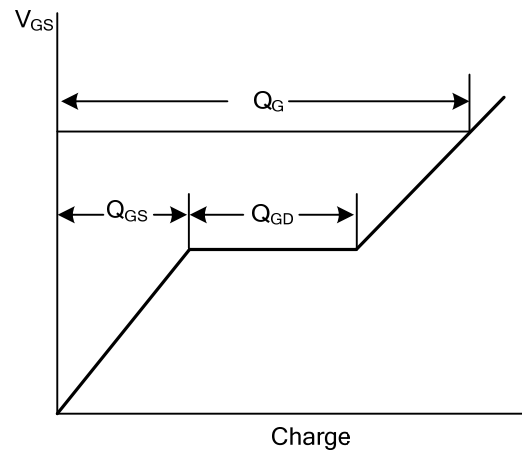
**Peak Diode Recovery dv/dt Test Circuit and Waveforms**

**Peak Diode Recovery dv/dt Waveforms**

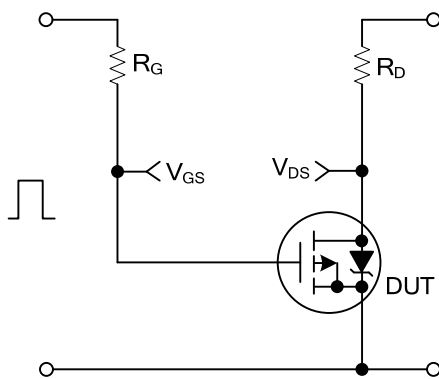
### ■ TEST CIRCUITS AND WAVEFORMS



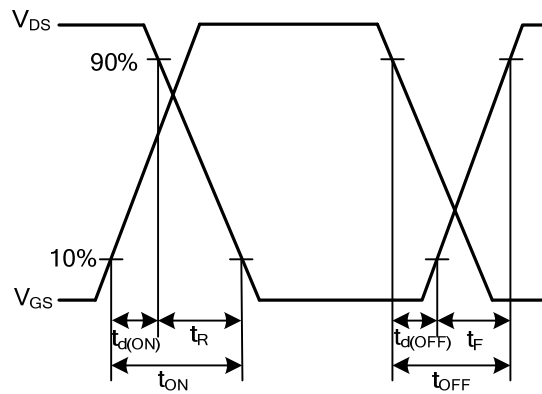
Gate Charge Test Circuit



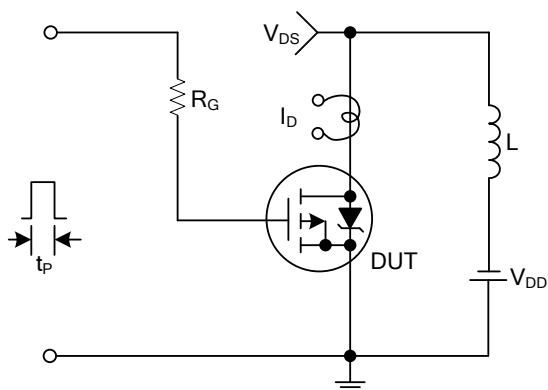
Gate Charge Waveforms



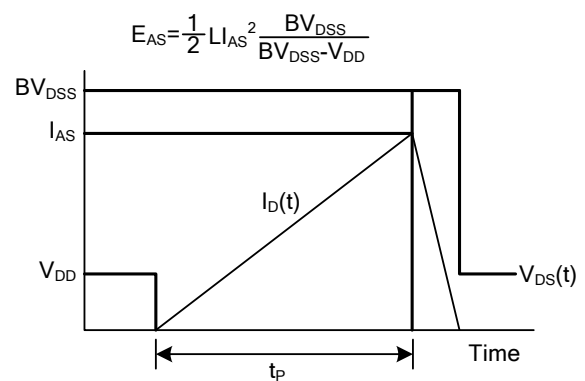
Resistive Switching Test Circuit



Resistive Switching Waveforms

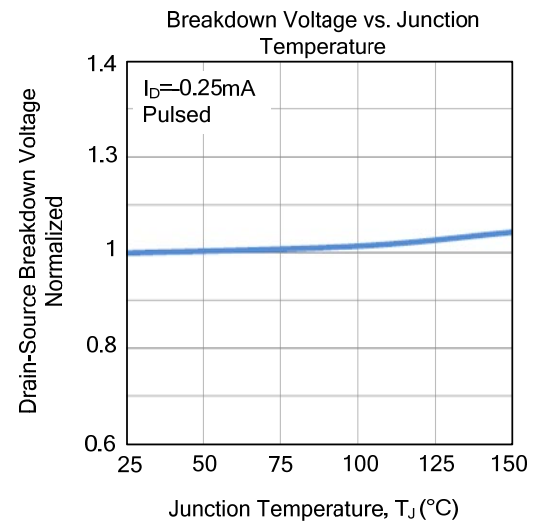
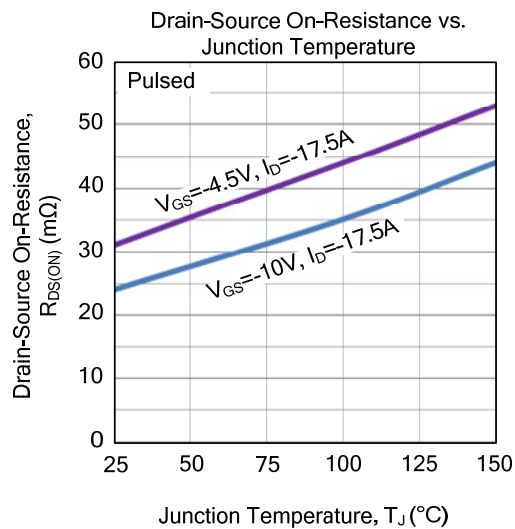
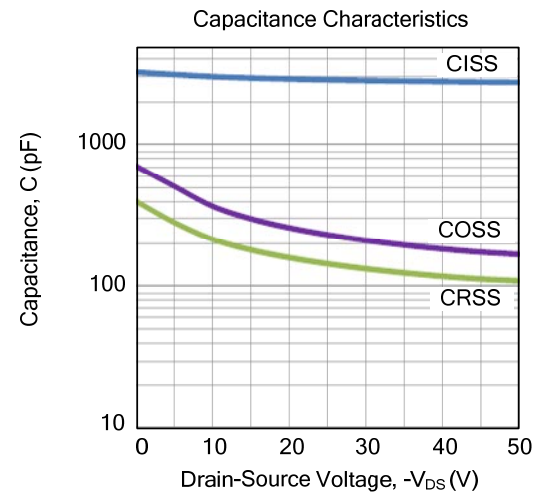
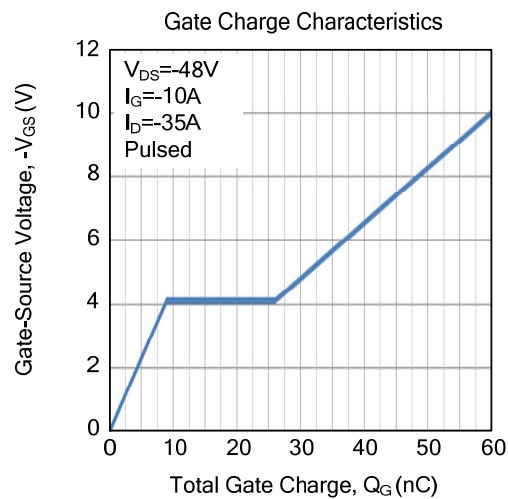
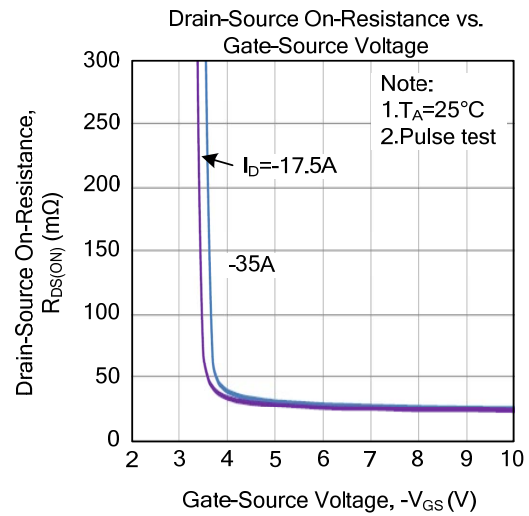
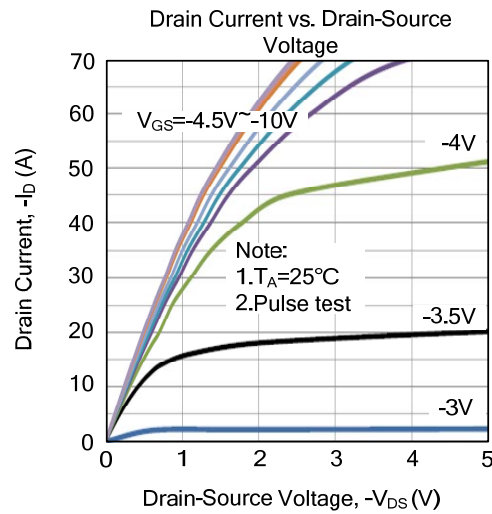


Unclamped Inductive Switching Test Circuit

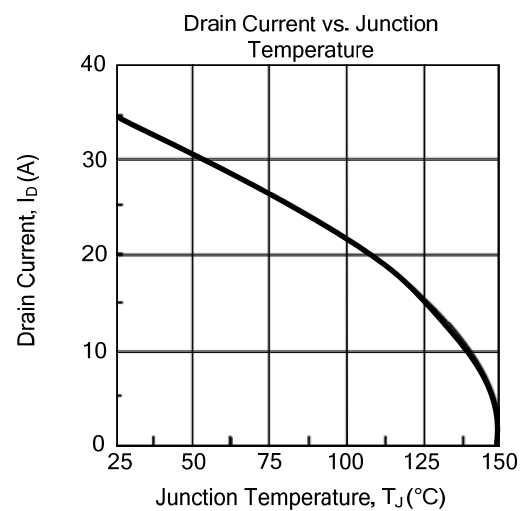
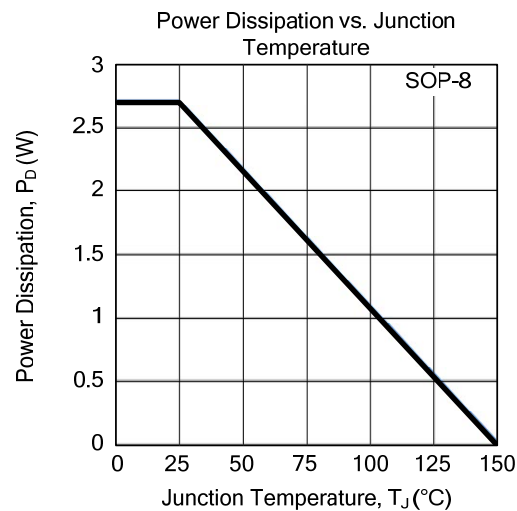
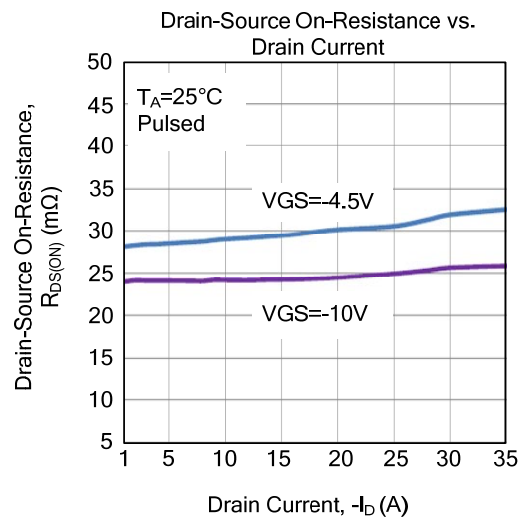
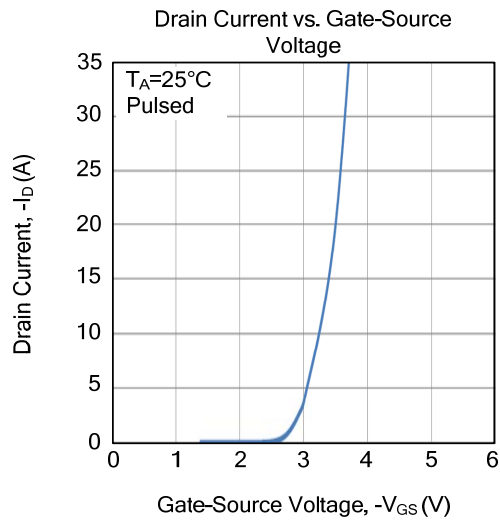
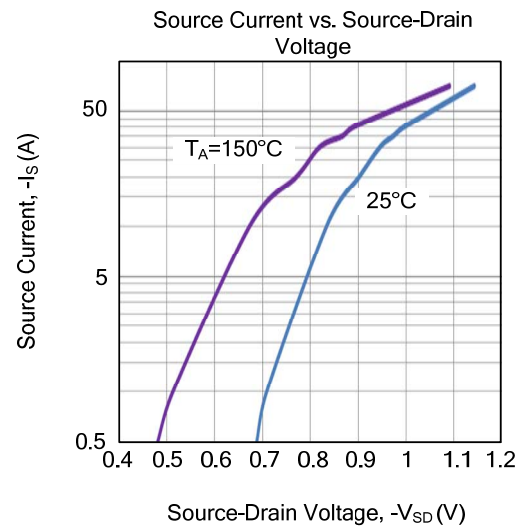
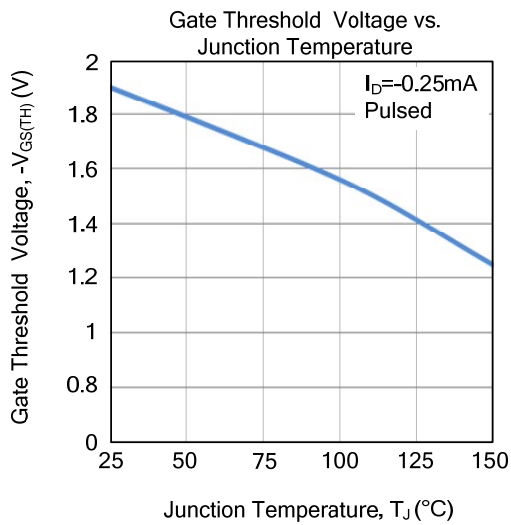


Unclamped Inductive Switching Waveforms

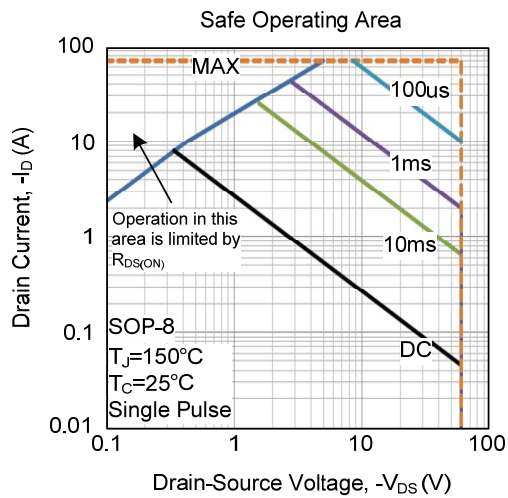
# TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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