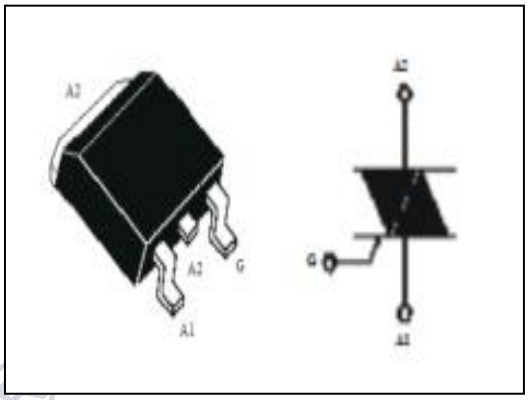
isc Thyristors USM8J48

DESCRIPTION

- With TO-263(D2PAK) packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

- Switching applications
- Phase control
- Static switching on inductive or resistive load

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER		MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage		600	V
V_{RRM}	Repetitive peak reverse voltage		600	V
$I_{T(RSM)}$	Average on-state current		8	A
I_{TSM}	Surge non-repetitive on-state current	50HZ 60HZ	80 88	A
$P_{G(AV)}$	Average gate power dissipation (over any 20 ms period)		0.5	W
T_j	Operating junction temperature		-40~125	℃
T_{stg}	Storage temperature		-40~125	℃

ELECTRICAL CHARACTERISTICS (T_C=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	V_R=V_{RRM} Rated; V_D=V_{DRM} Rated;	Tj=25℃		0.02	mA
I_{DRM}	Repetitive peak off-state current					
V_{TM}	On-state voltage	I_T=12A			1.5	V
I_{GT}	Gate-trigger current	V_D =12V;RL=20 Ω	I		30	mA
			II		30	
			III		30	
V_{GT}	Gate-trigger voltage	V_D =12V;RL=20 Ω			1.5	V
Rth (j-mb)	Junction to mounting base	Half cycle			2.8	℃/W

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