

DATA SHEET



MOS ANALOG INTEGRATED CIRCUIT

μ PD5741T6J

LOW NOISE AND HIGH GAIN AMPLIFIER FOR IMPEDANCE CONVERTER OF MICROPHONE

DESCRIPTION

The μ PD5741T6J is a silicon MOS monolithic integrated circuit designed as high gain impedance converter for electret condenser microphone. This device exhibits low noise and high voltage gain characteristics.

The package is 3-pin thin-type lead-less minimold, suitable for surface mount.

FEATURES

• Low Noise : Nv = -101 dBV TYP. @ $V_{DD} = 2$ V, $C_{in} = 3$ pF, $R_L = 2.2$ k Ω

: Nv = -102 dBV TYP. @ VDD = 2 V, Cin = 5 pF, RL = 2.2 k Ω

• High Gain : $G_V = +6.5 \text{ dB TYP}$. @ $V_{DD} = 2 \text{ V}$, $C_{in} = 3 \text{ pF}$, $R_L = 2.2 \text{ k}\Omega$

: Gv = +8.5 dB TYP. @ VDD = 2 V, Cin = 5 pF, RL = 2.2 k Ω

- Low Consumption Current : IDD = 250 μ A TYP. @ VDD = 2 V, RL = 2.2 k Ω
- · Built-in the capacitor for RF noise immunity
- · High ESD voltage
- 3-pin thin-type lead-less minimold (1.2 × 1.0 × 0.33 mm)

APPLICATIONS

· Microphone, Sensor, etc.

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μPD5741T6J-E4	μPD5741T6J-E4-A	E4-A 3-pin thin-type lead- less minimold (Pb-Free)		 Embossed tape 8 mm wide Pin 3 (GND) face the perforation side of the tape Qty 10 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: μ PD5741T6J

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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ABSOLUTE MAXIMUM RATINGS ($T_A = +25$ °C)

Parameter	Symbol	Ratings	Unit
Input Voltage (IN-GND)	Vin	-0.5 to +0.5	V
Input Current (IN-GND)	lin	0.5	mA
Output Voltage (OUT-GND)	Vout	0 to +5	V
Output Current (OUT-GND)	lout	0.5	mA
Channel Temperature	Tch	130	°C
Operating Ambient Temperature	TA	-40 to +85	°C
Storage Temperature	T _{stg}	-65 to +150	°C

RECOMMENDED OPERATING CONDITIONS (TA = +25°C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage Note	V_{DD}	1.0	2.0	5.0	V

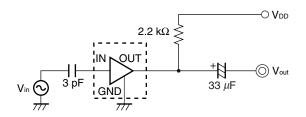
Note $R_L = 2.2 \text{ k}\Omega$



ELECTRICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)

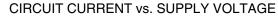
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	IDD	$V_{DD}=2~V,~V_{in}=0~V,~R_{L}=2.2~k\Omega$	150	250	350	μΑ
Input Capacitance	Cinput	$V_{DD} = 2 \text{ V}, \text{ RL} = 2.2 \text{ k}\Omega, \text{ f} = 1 \text{ MHz}$	I	1.5	ı	pF
Voltage Gain	Gv	$V_{DD} = 2 \text{ V, V}_{\text{in}} = 10 \text{ mV, R}_{\text{L}} = 2.2 \text{ k}\Omega,$ $C_{\text{in}} = 3 \text{ pF, f} = 1 \text{ kHz, see Test Circuit}$	5.0	6.5	8.0	dB
Reduced Voltage Gain Characteristics	⊿Gvv	$\begin{split} V_{DD} = 2 &\rightarrow 1.5 \text{ V, V}_{\text{in}} = 10 \text{ mV,} \\ R_{\text{L}} = 2.2 \text{ k}\Omega, C_{\text{in}} = 3 \text{ pF, f} = 1 \text{ kHz,} \\ \text{see Test Circuit} \end{split}$	-	0.5	-	dB
Frequency Characteristics	⊿Gvf	$\begin{aligned} &V_{DD}=2~V,~V_{in}=10~mV,~R_L=2.2~k\Omega,\\ &C_{in}=3~pF,~f=1~kHz\rightarrow110~Hz,\\ &see~Test~Circuit \end{aligned}$	-	0	-	dB
Output Noise Voltage	Nv	$V_{DD} = 2 \ V, \ V_{in} = 0 \ V, \ R_L = 2.2 \ k\Omega,$ $C_{in} = 3 \ pF, \ A-Curve, \ see \ Test \ Circuit$	-	-101	-	dBV
Total Harmonic Distortion	THD	$V_{DD} = 2 \text{ V, } V_{out} = 50 \text{ mV, } R_L = 2.2 \text{ k}\Omega,$ $C_{in} = 3 \text{ pF, } f = 1 \text{ kHz, see Test Circuit}$	-	0.5	-	%

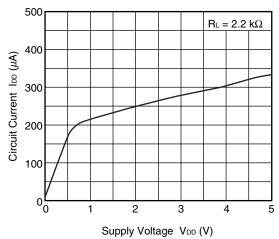
TEST CIRCUIT (Voltage Gain, Frequency Characteristics, Output Noise Voltage, Total Harmonic Distortion)



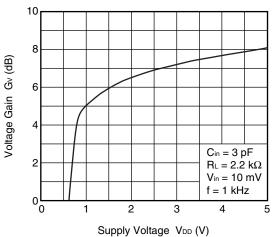
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TYPICAL CHARACTERISTICS (TA = +25°C, unless otherwise specified)

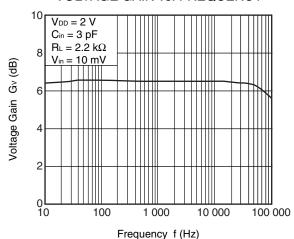




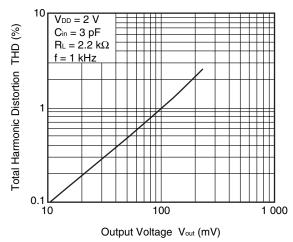
VOLTAGE GAIN vs. SUPPLY VOLTAGE



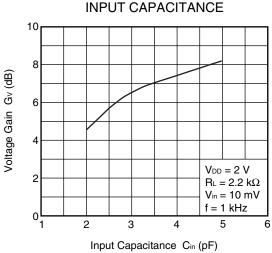
VOLTAGE GAIN vs. FREQUENCY



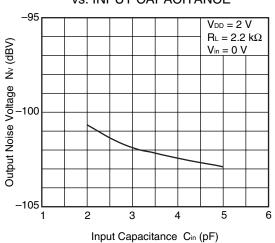
TOTAL HARMONIC DISTORTION vs. OUTPUT VOLTAGE



VOLTAGE GAIN vs.



OUTPUT NOISE VOLTAGE vs. INPUT CAPACITANCE

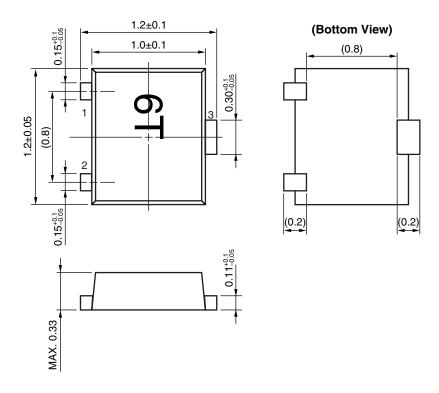


Remark The graphs indicate nominal characteristics.

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PACKAGE DIMENSIONS

3-PIN THIN-TYPE LEAD-LESS MINIMOLD (UNIT: mm)



PIN CONNECTIONS

- 1. OUT
- 2. IN
- 3. GND

Remark (): Reference value

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RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Soldering Conditions			
Infrared Reflow	Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below	IR260		
Wave Soldering	Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass)	: 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below	WS260		
Partial Heating	Peak temperature (terminal temperature) Soldering time (per side of device) Maximum chlorine content of rosin flux (% mass)	: 350°C or below : 3 seconds or less : 0.2%(Wt.) or below	HS350		

Caution Do not use different soldering methods together (except for partial heating).

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