

6427525 N E C ELECTRONICS INC

05E 22859 D

BIPOLAR ANALOG INTEGRATED CIRCUIT **μ PC1287G****FM MULTIPLEX STEREO DEMODULATOR WITH NOISE CANCELLER**

T-77-05-05

DESCRIPTION

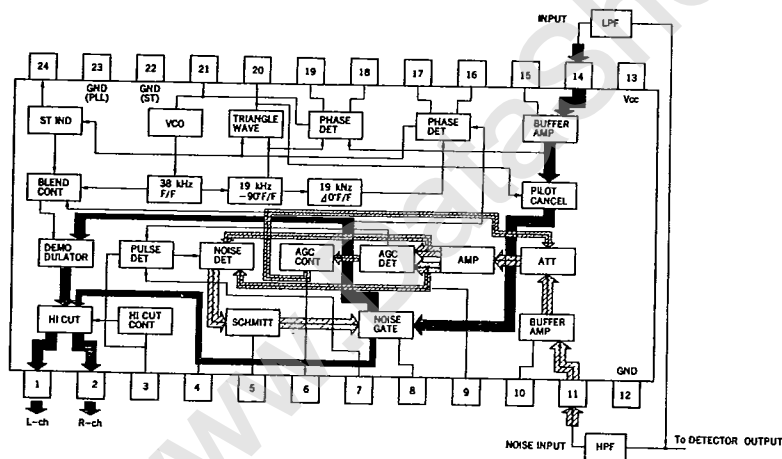
The μ PC1287G is a silicon monolithic integrated circuit which includes FM stereo demodulator and noise canceller on the single chip, for use in automotive radio receivers.

FUNCTIONS

- Pilot canceller (with automatic control circuit)
- Stereo-noise control (SNC)
- High-cut control (HCC)
- Forced monaural (control pin No.24)

FEATURES

- Low distortion. THD = 0.1 % (TYP.), @ $V_i = 300 \text{ mV}_{r.m.s.}$
- High ripple rejection ratio. SVR = 33 dB (TYP.), @ $f_r = 200 \text{ Hz}$
- High maximum input level. $V_i (\text{MAX.}) = 800 \text{ mV}_{r.m.s.}$ (TYP.), @ THD = 1.0 %
- A small number of external parts.

BLOCK DIAGRAM

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ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

Supply Voltage	V_{CC}	16	V
Circuit Current	I_{CC}	67	mA
Package Dissipation	P_D	470*	mW
Operational Temperature	T_{opt}	-30 to +75	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to +125	$^\circ\text{C}$

* $T_a = 75^\circ\text{C}$

RECOMMENDED OPERATING CONDITION ($T_a = 25^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V_{CC}	7.5	8.5	9.5	V

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

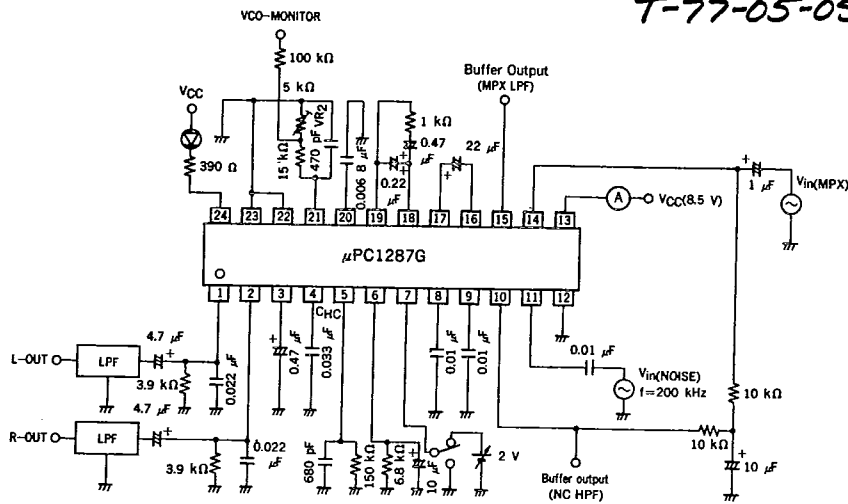
($V_{CC} = 8.5\text{ V}$, $V_{L+R} = 270\text{ mV}_{r.m.s.}$, Pilot = $30\text{ mV}_{r.m.s.}$, $f = 1\text{ kHz}$)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Circuit Current	I_{CC}		39	50	mA	Quiescent
Supply Voltage Rejection Ratio	SVR		33		dB	$f_r = 200\text{ Hz}$, $V_r = 0.5\text{ V}_{r.m.s.}$ (without Line-filter)
Separation	Sep	35	40		dB	$f = 1\text{ kHz}$
Monaural Total Harmonic Distortion	THD1		0.1	0.5	%	$V_{in} = 300\text{ mV}_{r.m.s.}$ (Monaural)
Stereo Total Harmonic Distortion	THD2		0.1	0.7	%	$L + R = 270\text{ mV}_{r.m.s.}$ Pilot = $30\text{ mV}_{r.m.s.}$
Voltage Gain (Mono)	G_v	-4	-1	+2	dB	Monaural
Channel Balance	CB	-1.5	0	+1.5	dB	
Lamp-on Level	LAMP-ON	6.5	10	13.5	$\text{mV}_{r.m.s.}$	Pilot Only
Lamp Hysteresis	Hy	3	5	7	dB	
Capture Range	CR	± 2.3			%	
Ultrasonic Frequency Rejection	Rej(19)	15	17		dB	Triangular Wave Cancel
SCA Rejection	Rej(SCA)		67		dB	$L + R = 240\text{ mV}_{r.m.s.}$ Pilot = $30\text{ mV}_{r.m.s.}$ SCA = $30\text{ mV}_{r.m.s.}$
Signal to Noise Ratio	S/N	71	77		dB	$R_g = 4.7\text{ k}\Omega$
Maximum Input Level	$V_i(\text{MAX.})$	700	800		$\text{mV}_{r.m.s.}$	THD = 1 %
Control Current (Stereo $\rightarrow$ Mono)	$I_{(\text{mono})}$		0.25	0.5	mA	$L + R = 270\text{ mV}_{r.m.s.}$ Pilot = $30\text{ mV}_{r.m.s.}$
Separation (Blend)	SepB		10		dB	Noise Level at $V_i\text{AGC} + 12\text{ dB}$
High-cut Operating Voltage	$V_{HC(1)}$	0.5	0.65	0.9	V	$C_{HC} = 10\text{ }\mu\text{F}$, -6 dB point (V_o)
Voltage Gain (L.P.F.)	G_{VLPF}	0	0.8	1.6	dB	
Voltage Gain (H.P.F.)	G_{VHPF}	2	3.0	4	dB	
High-cut Control Noise	NHC		-50	-45	dBm	Din Noise Peak
Input Signal Voltage (AGC DET.)	V_{AGC}	-75	-71.5	-68	dBm	$f = 200\text{ kHz}$ AGC Terminal Voltage (V_G) = 0.55 V

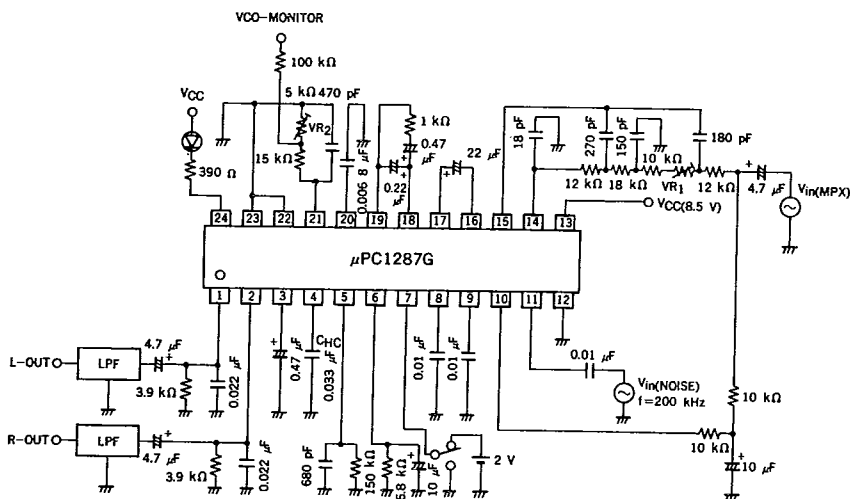
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TEST CIRCUIT 2 (SEPARATION, THD (ST))



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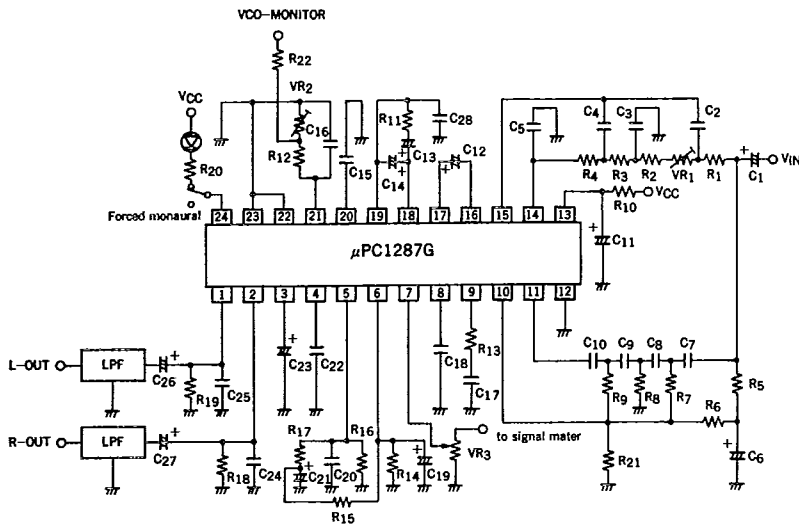
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TYPICAL APPLICATION



- VR₁ : SEPARATION ADJUSTMENT
 VR₂ : VCO ADJUSTMENT (76 kHz)
 VR₃ : HCC ADJUSTMENT (HCC off $V_T > 1.2$ V)
 C₂₂ : HIGH-CUT FREQUENCY $f = 2.84 \times 10^{-5} / C_{22}$
 R₂₁ : BLEND SHIFT ADJUSTMENT (shift operating SNC)
 C₁₅ : LAMP ON LEVEL ADJUSTMENT

NOTE 1 : Value of VR₃ is recommended less than 33 kΩ.

- 2 : In case of sensing pulsive noise through the output for signal meter, don't use capacitor with large capacity at the output terminal for signal meter.
 3 : Coefficient of VCO circuit (C₁₆, VR₂, R₁₂) is recommended -330 PPM/°C
 4 : C₂₁ and R₁₇ reduce AGC effect in the case that there is continual pulsive noise. Adjust AGC characteristic for successive pulsive noise by R₁₄ and R₁₅.
 5 : C₁₉ is recommended solid tantalum capacitor.

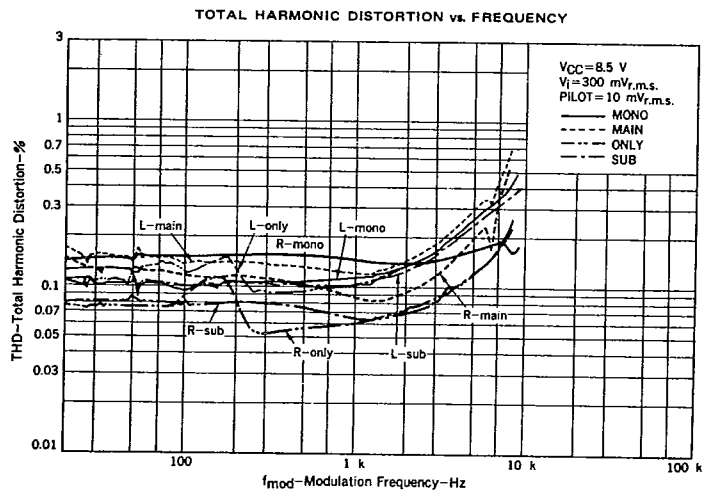
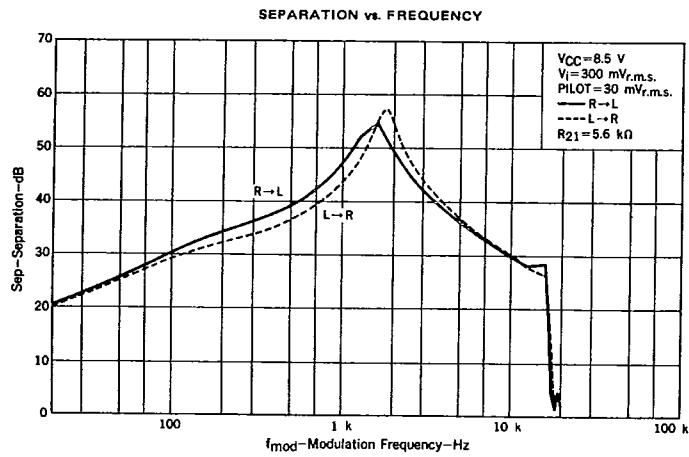
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TYPICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

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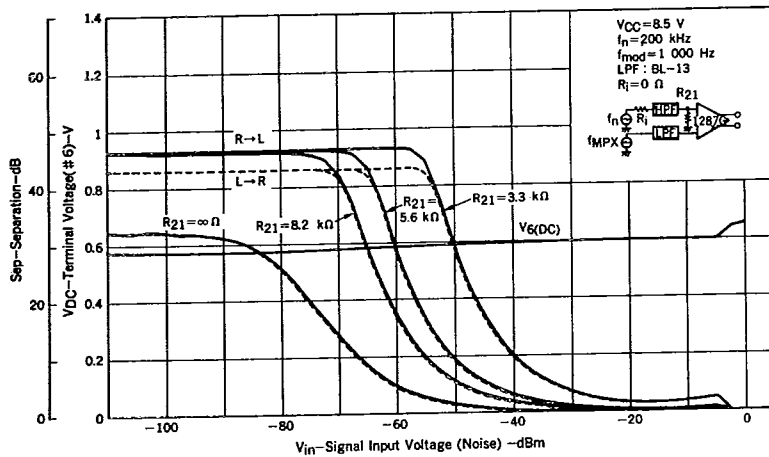
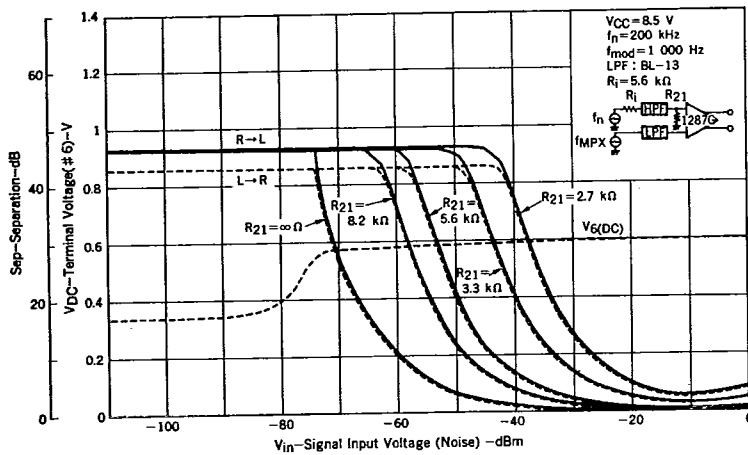
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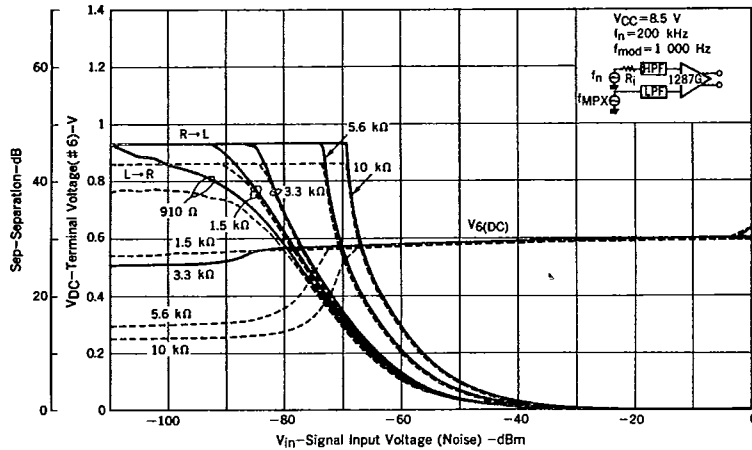
TERMINAL VOLTAGE (V_6), SEPARATION vs.
NOISE INPUT VOLTAGETERMINAL VOLTAGE (V_6), SEPARATION vs.
NOISE INPUT VOLTAGE

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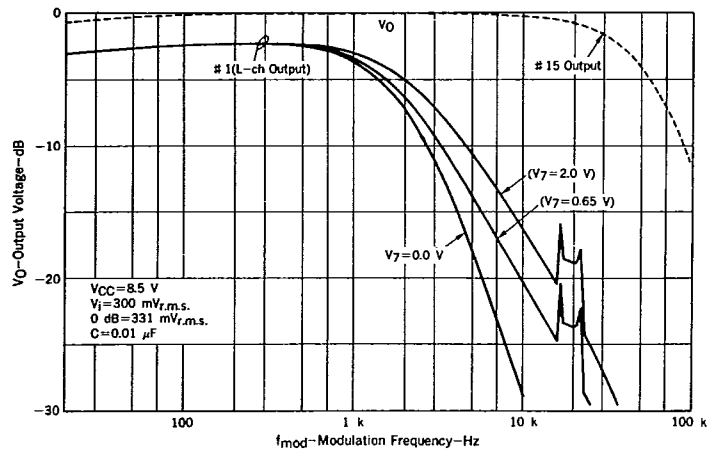
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TERMINAL VOLTAGE (V_G), SEPARATION vs.
NOISE INPUT VOLTAGE

OUTPUT VOLTAGE vs. FREQUENCY



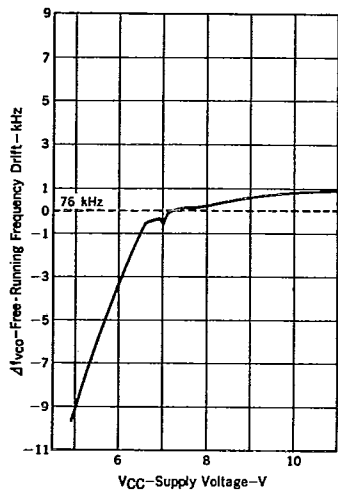
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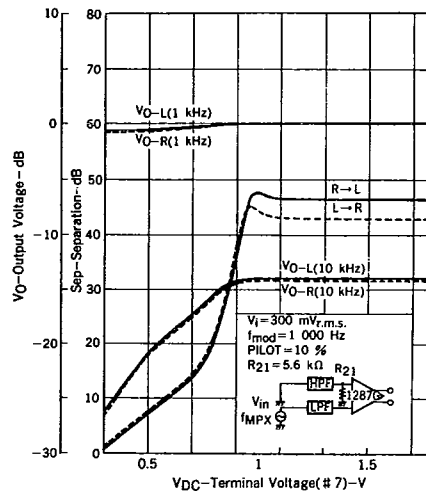
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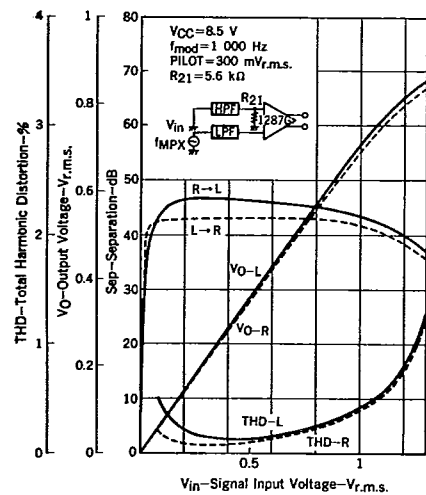
**FREE RUNNING FREQUENCY
 vs. SUPPLY VOLTAGE**



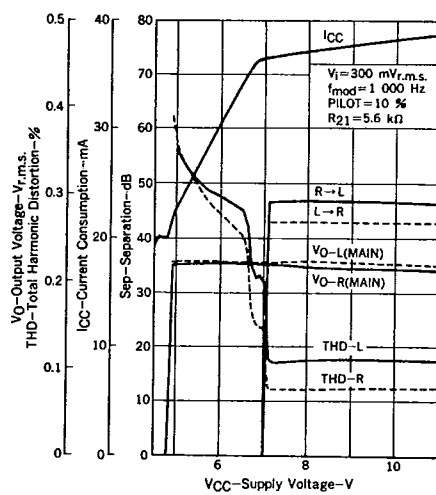
**OUTPUT VOLTAGE, SEPARATION
 vs. TERMINAL VOLTAGE (V_7)**



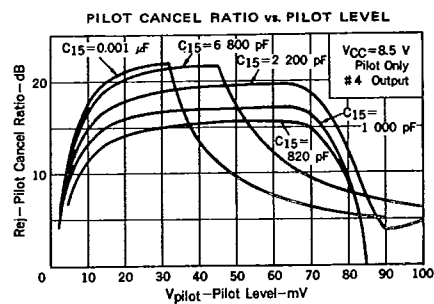
**SEPARATION, OUTPUT VOLTAGE,
 TOTAL HARMONIC DISTORTION vs.
 INPUT VOLTAGE**



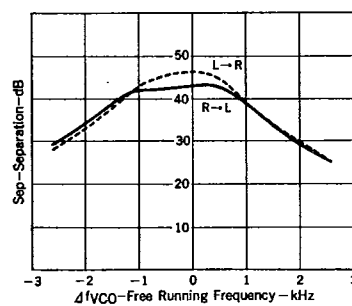
**SEPARATION, CIRCUIT CURRENT,
 OUTPUT VOLTAGE vs.
 SUPPLY VOLTAGE**



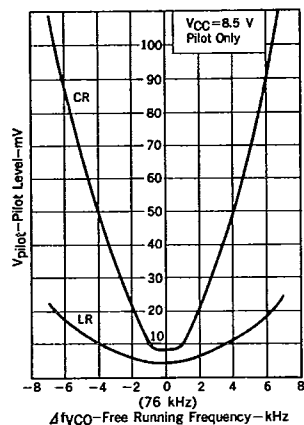
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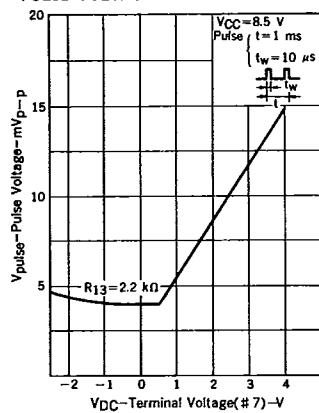
SEPARATION vs. FREE RUNNING FREQUENCY



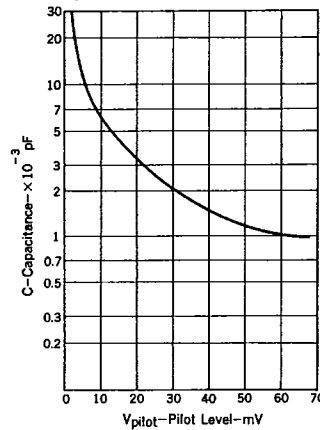
PILOT LEVEL vs. FREE RUNNING FREQUENCY



PULSE VOLTAGE vs. TERMINAL VOLTAGE



CAPACITANCE vs. PILOT LEVEL



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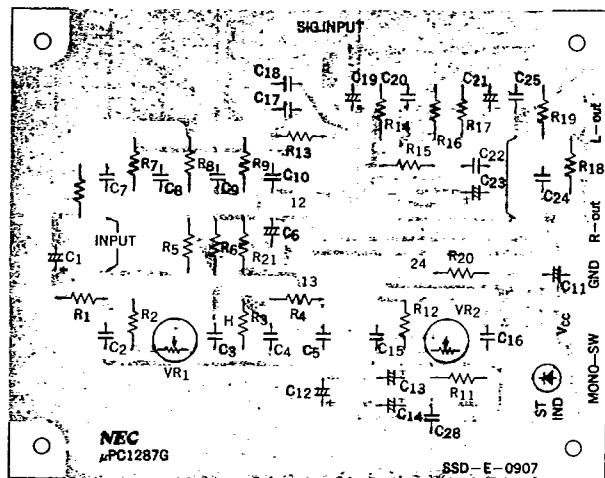
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OUTBOARD COMPONENTS MOUNTED ON A PRINTED-CIRCUIT BOARD

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R1 = 12 k Ω	R11 = 1 k Ω	R21 = 5.6 k Ω	C1 = 2.2 μ F	C11 = 100 μ F	C21 = 1 μ F
R2 = 10 k Ω	R12 = 15 k Ω	VR1 = 22 k Ω	C2 = 180 pF	C12 = 1 μ F	C22 = 0.01 μ F
R3 = 18 k Ω	R13 = 2.2 k Ω	VR2 = 5 k Ω	C3 = 150 pF	C13 = 0.47 μ F	C23 = 0.47 μ F
R4 = 12 k Ω	R14 = 22 k Ω		C4 = 270 pF	C14 = 0.22 μ F	C24 = 0.022 μ F
R5 = 10 k Ω	R15 = 12 k Ω		C5 = 18 pF	C15 = 0.0068 μ F	C25 = 0.022 μ F
R6 = 10 k Ω	R16 = 82 k Ω		C6 = 1 μ F	C16 = 470 pF	C26 = 4.7 μ F
R7 = 5.6 k Ω	R17 = 22 k Ω		C7 = 120 pF	C17 = 390 pF	C27 = 4.7 μ F
R8 = 5.6 k Ω	R18 = 3.9 k Ω		C8 = 120 pF	C18 = 0.01 μ F	C28 = 1 500 pF
R9 = 5.6 k Ω	R19 = 3.9 k Ω		C9 = 150 pF	C19 = 10 μ F	
R10 = 33 Ω	R20 = 470 Ω		C10 = 82 pF	C20 = 680 pF	

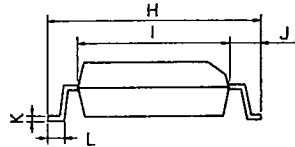
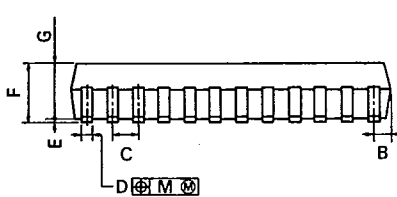
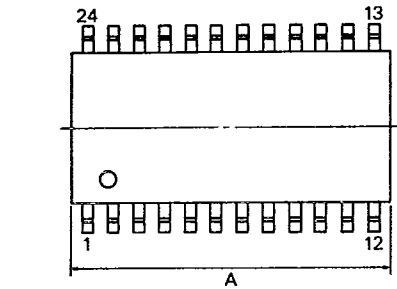
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24PIN PLASTIC SOP (375 mil)

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P24GM-50-375B

NOTE

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS	INCHES
A	15.54 MAX.	0.612 MAX.
B	0.78 MAX.	0.031 MAX.
C	1.27 (T.P.)	0.050 (T.P.)
D	0.40 ± 0.08	0.016 ± 0.003
E	0.1 ± 0.1	0.004 ± 0.004
F	2.9 MAX.	0.115 MAX.
G	2.50	0.098
H	10.3 ± 0.3	0.406 ± 0.012
I	7.2	0.283
J	1.6	0.063
K	0.15 ± 0.08	0.006 ± 0.003
L	0.8 ± 0.2	0.031 ± 0.008
M	0.12	0.005

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