

Silicon Carbide (SiC) Diode -EliteSiC, TO247-3, 20 A, 1200 V SiC Merged **PiN-Schottky (MPS) Dual Diode**

UJ3D1220KSD

Description

onsemi offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175 °C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

Features

- Maximum Operating Temperature of 175 °C
- Easy Paralleling
- Extremely Fast Switching not Dependent on Temperature
- No Reverse or Forward Recovery
- Enhanced Surge Current Capability, MPS Structure
- Excellent Thermal Performance, Ag Sintered
- 100% UIS Tested
- This Device is Pb-Free, Halogen Free and is ROHS Compliant

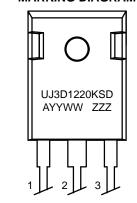
Typical Applications

- Power Converters
- Industrial Motor Drives
- Switch Mode Power Supplies
- Power Factor Correction Modules



TO247-3 CASE 340AK

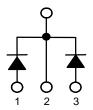
MARKING DIAGRAM



UJ3D1220KSD = Specific Device Code = Assembly Location

YY = Year WW = Work Week ZZZ = Lot ID

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

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UJ3D1220KSD

MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Value (Leg/Device)	Unit	
DC Blocking Voltage	V_R		1200	V	
Repetitive Peak Reverse Voltage, T _J = 25 °C	V_{RRM}		1200	V	
Surge Peak Reverse Voltage	V_{RSM}		1200	V	
Maximum DC Forward Current	I _F	T _C = 158 °C	10/20	А	
Non-repetitive Forward Surge Current Sine	I _{FSM}	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	120/240	А	
Halfwave		$T_C = 110 {}^{\circ}\text{C}, t_p = 10 \text{ms}$	110/220		
Repetitive Forward Surge Current Sine Halfwave, D = 0.1	I _{FRM}	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	56.7/113.4	А	
		$T_C = 110 {}^{\circ}\text{C}, t_p = 10 \text{ms}$	33.6/67.2		
Non-repetitive Peak Forward Current	I _{F,max}	$T_C = 25 {}^{\circ}\text{C}, t_p = 10 \mu\text{s}$	720/1440	А	
		$T_C = 110 {}^{\circ}\text{C}, t_p = 10 \mu\text{s}$	720/1440		
i ² t Value	∫i ² dt	$T_C = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}$	72/288	A ² s	
		$T_C = 110 {}^{\circ}\text{C}, t_p = 10 \text{ms}$	60/240		
Power Dissipation	P _{tot}	T _C = 25 °C	234.4/468.8	W	
		T _C = 158 °C	26.6/53.2		
Maximum Junction Temperature	$T_{J,max}$		175	°C	
Operating and Storage Temperature	T _J , T _{STG}		-55 to 175	°C	
Soldering Temperatures, Wavesoldering only Allowed at Leads	T _{sold}	1.6 mm from case for 10 s	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

			Value (Leg/Device)			
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$		-	0.49/0.245	0.64/0.32	°C/W

ELECTRICAL CHARACTERISTICS (T_J = +25 °C unless otherwise specified)

		Test Conditions	Val	Value (Leg/Device)		
Parameter	Symbol		Min	Тур	Max	Unit
Forward Voltage	V _F	I _F = 10 A/20 A, T _J = 25 °C	-	1.4	1.6	V
		I _F = 10 A/20 A, T _J = 150 °C	_	1.85	2.3	
		I _F = 10 A/20 A, T _J = 175 °C	_	2	2.6	
Reverse Current	I _R	V _R = 1200 V, T _J = 25 °C	_	10/20	110/220	μΑ
		V _R = 1200 V, T _J = 175 °C	_	450/900	-	
Total Capacitive Charge (Note 1)	$Q_{\mathbb{C}}$	V _R = 800 V	_	51/102	-	nC
Total Capacitance	С	V _R = 1 V, f = 1 MHz	_	510/1020	-	pF
		V _R = 400 V, f = 1 MHz	_	48/96	-	
		V _R = 800 V, f = 1 MHz	-	41/82	-	
Capacitance Stored Energy	E _C	V _R = 800 V	_	15/30	_	μJ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Q_C is independent on T_J , d_{IF}/dt , and I_F as shown in the application note <u>AND90316/D</u>

UJ3D1220KSD

TYPICAL PERFORMANCE DIAGRAMS (PER LEG)

I_F, Forward Current (A)

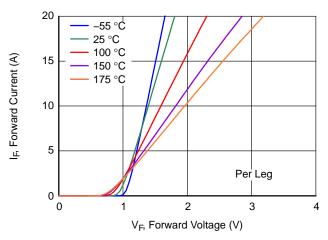


Figure 1. Typical Forward Characteristics

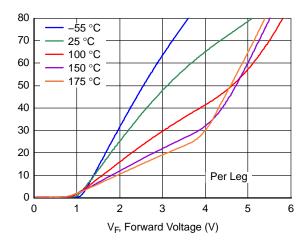


Figure 2. Typical Forward Characteristics in Surge Current

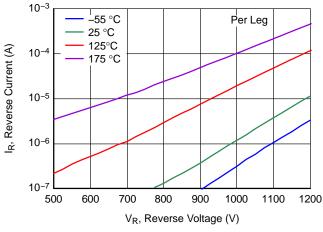


Figure 3. Typical Reverse Characteristics

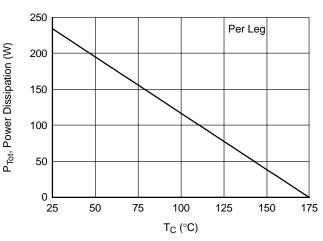


Figure 4. Power Dissipation

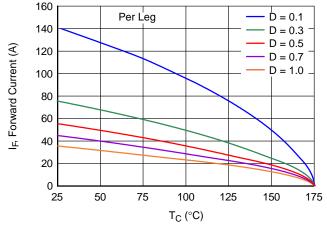


Figure 5. Diode Forward Current

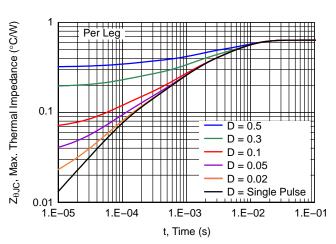


Figure 6. Maximum Transient Thermal Impedance

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TYPICAL PERFORMANCE DIAGRAMS (PER LEG) (CONTINUED)

 $Q_{C}\left(nC\right)$

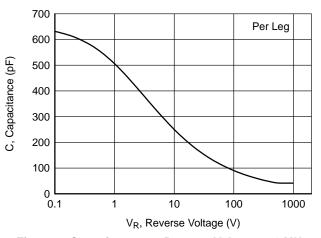


Figure 7. Capacitance vs. Reverse Voltage at 1 MHz

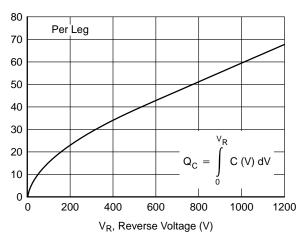


Figure 8. Typical Capacitive Charge vs. Reverse Voltage

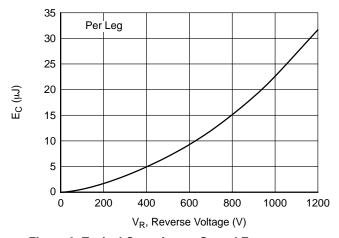


Figure 9. Typical Capacitance Stored Energy vs. Reverse Voltage

ORDERING INFORMATION

Part Number	Marking	Package	Shipping
UJ3D1220KSD	UJ3D1220KSD	TO247-3 (Pb-Free, Halogen Free)	600 / Tube

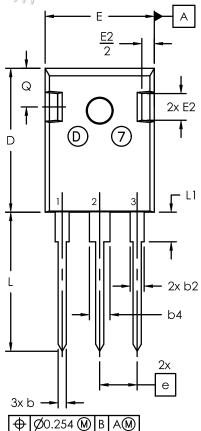


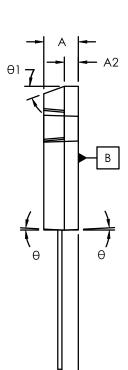


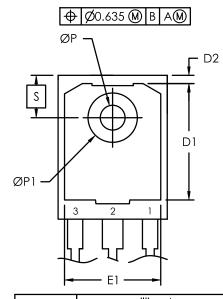


TO247-3 15.90x20.96x5.03, 5.44P CASE 340AK ISSUE A

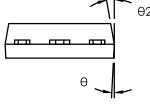
DATE 12 FEB 2025







SYM	millimeters				
	MIN	NOM	MAX		
Α	4.70	5.03	5.31		
A1	2.21	2.40	2.59		
A2	1.50	2.03	2.49		
b	0.99	1.20	1.40		
b2	1.65	2.03	2.39		
b4	2.59	3.00	3.43		
С	0.38	0.60	0.89		
D	20.70	20.96	21.46		
D1	13.08	_	ı		
D2	0.51	1.19	1.35		
Е	15.49	15.90	16.26		
е		5.44 BSC			
E1	13.00	14.02	13.60		
E2	3.43	3.89	5.20		
L	19.62	20.27	20.32		
L1	-	_	4.50		
ØP	3.40	3.60	3.80		
ØP1	7.06	7.19	7.39		
Q	5.38	5.62	6.20		
S	6.15 BSC				
θ	3°				
θ1	20°				
θ2	10°				



NOTE:

- 1. Dimensioning and tolerancing as per ASME Y14.5 2018
- 2. Controlling dimension: millimeters
- 3. Package Outline in compliance with JEDEC standard var. AD.
- 4. Dimensions D & E does not include mold flash.
- 5. ØP to have max draft angle of 1.7° to the top with max. hole diameter of 3.91mm.

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DESCRIPTION:	TO247-3 15.90x20.96x5.03, 5.44P		PAGE 1 OF 1	

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