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<u>Silicon Carbide (SiC) Diode</u> – EliteSiC, TO220-2, 16 A, 650 V SiC Merged PiN-Schottky (MPS) Diode

UJ3D06516TS

Description

onsemi offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175 °C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

Features

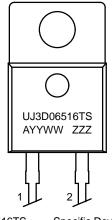
- 175 °C Maximum Operating Junction Temperature
- Easy Paralleling
- Extremely Fast Switching not Dependent on Temperature
- No Reverse or Forward Recovery
- Enhanced Surge Current Capability, MPS Structure
- Excellent Thermal Performance, Ag Sintered
- 100% UIS Tested
- This Device is Pb-Free, Halogen Free and is ROHS Compliant

Typical Applications

- Power Converters
- Industrial Motor Drives
- Switch-mode Power Supplies
- Power Factor Correction Modules



MARKING DIAGRAM



UJ3D06516TS = S A = A YY = Y WW = V ZZZ = L

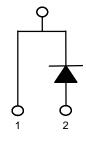
= Specific Device Code = Assembly Location

= Year

= Work Week

= Lot ID

PIN CONNECTIONS



ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

UJ3D06516TS

MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Value	Unit
DC Blocking Voltage	V _R		650	V
Repetitive Peak Reverse Voltage, $T_J = 25 \ ^{\circ}C$	V _{RRM}		650	V
Surge Peak Reverse Voltage	V _{RSM}		650	V
Maximum DC Forward Current	١ _F	T _C = 152 °C	16	А
Non-repetitive Forward Surge Current Sine	I _{FSM}	$T_{C} = 25 \ ^{\circ}C, t_{p} = 10 \ \text{ms}$	100	А
Halfwave		$T_{C} = 110 \ ^{\circ}C, t_{p} = 10 \ \text{ms}$	90	
Repetitive Forward Surge Current Sine	I _{FRM}	$T_{C} = 25 \ ^{\circ}C, t_{p} = 10 \ \text{ms}$	65.9	А
Halfwave, D = 0.1		$T_{C} = 110 \ ^{\circ}C, t_{p} = 10 \ ms$	40.7	
Non-repetitive Peak Forward Current	I _{F,max}	T _C = 25 °C, t _p = 10 μs	550	А
		$T_{C} = 110 \ ^{\circ}C, t_{p} = 10 \ \mu s$	550	
i ² t Value	∫i ² dt	$T_{C} = 25 \ ^{\circ}C, t_{p} = 10 \ \text{ms}$	50	A ² s
		$T_{C} = 110 \ ^{\circ}C, t_{p} = 10 \ ms$	40	
Power Dissipation	P _{Tot}	T _C = 25 °C	230.8	W
		T _C = 152 °C	35.4	
Maximum Junction Temperature	T _{J,max}		175	°C
Operating and Storage Temperature	T _J , T _{STG}		-55 to 175	°C
Soldering Temperatures, Wavesoldering only Allowed at Leads	T _{sold}	1.6 mm from case for 10 s	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS (T_J = +25 °C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Forward Voltage	V _F	I _F = 16 A, T _J = 25 °C	-	1.5	1.7	V
		I _F = 16 A, T _J = 150 °C	-	1.8	2.1	1
		I _F = 16 A, T _J = 175 °C	-	1.9	2.25	1
Reverse Current	I _R	$V_R = 650 \text{ V}, \text{ T}_J = 25 ^\circ\text{C}$	-	16	100	μΑ
		V_R = 650 V, T_J = 175 °C	-	58	-	1
Total Capacitive Charge (Note 1)	Q _C	V _R = 400 V	-	38	-	nC
Total Capacitance	С	V _R = 1 V, f = 1 MHz	-	500	-	pF
		V _R = 300 V, f = 1 MHz	-	62	-	1
		V _R = 600 V, f = 1 MHz	-	56	-	1
Capacitance Stored Energy	E _C	V _R = 400 V	-	5.6	-	μJ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Q_C is independent on T_J , di_F/dt, and I_F as shown in the application note <u>AND90316/D</u>

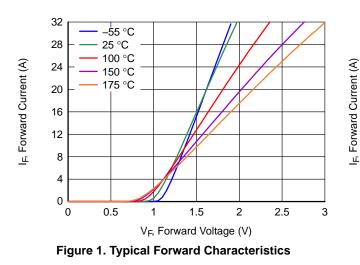
THERMAL CHARACTERISTICS

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Thermal Resistance, Junction-Case	$R_{ extsf{ heta}JC}$		-	0.5	0.65	°C/W

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TYPICAL PERFORMANCE

P_{Tot}, Power Dissipation (W)



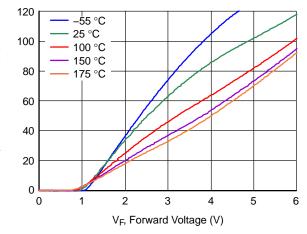
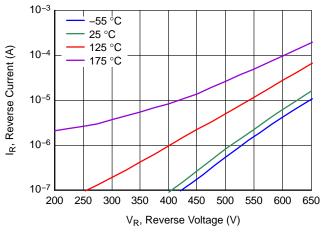
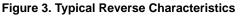


Figure 2. Typical Forward Characteristics in Surge Current





I_F, Forward Current (A)

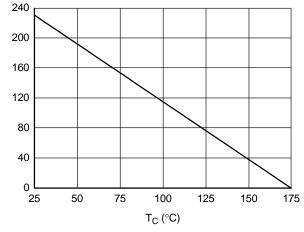


Figure 4. Power Dissipation

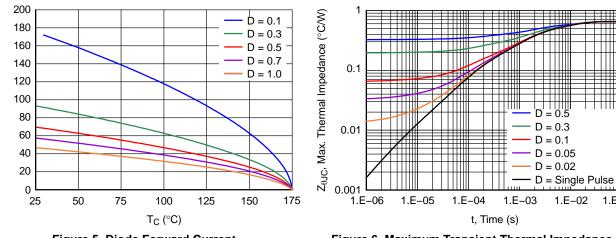




Figure 6. Maximum Transient Thermal Impedance

1.E-01

1.E-02

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TYPICAL PERFORMANCE (CONTINUED)

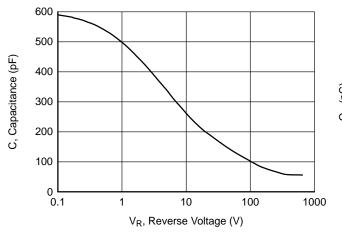
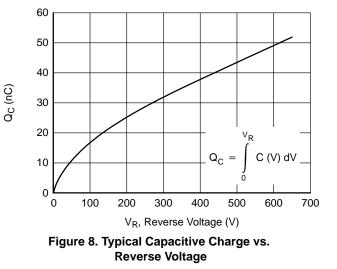


Figure 7. Capacitance vs. Reverse Voltage at 1 MHz



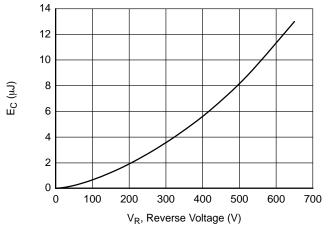
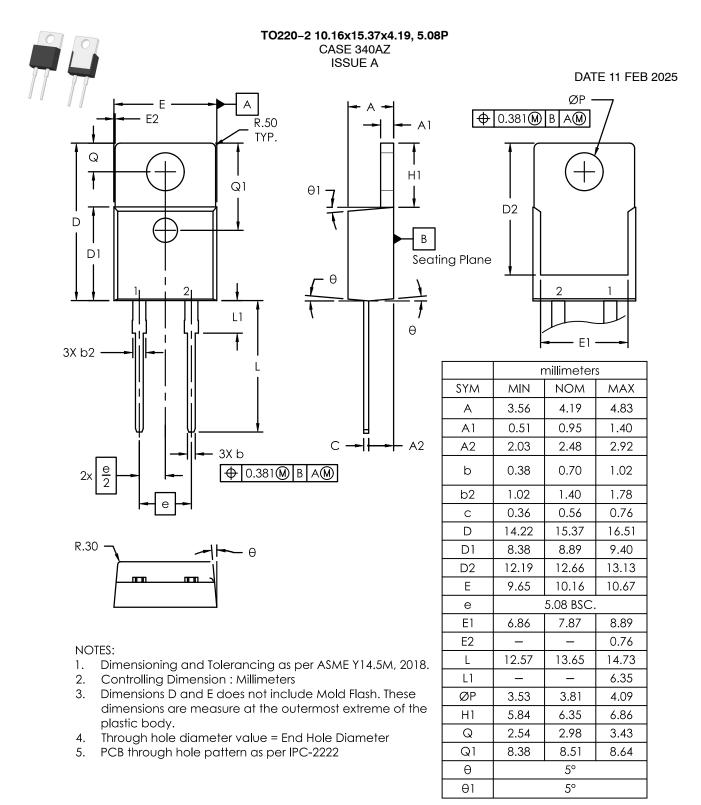


Figure 9. Typical Capacitance Stored Energy vs. Reverse Voltage

ORDERING INFORMATION

Part Number	Marking	Package	Shipping
UJ3D06516TS	UJ3D06516TS	TO220-2 (Pb-Free, Halogen Free)	1000 / Tube

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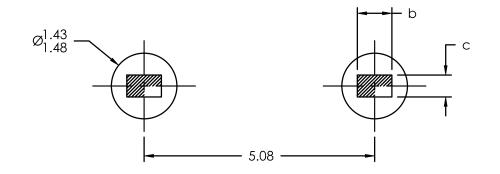
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TO220-2 10.16x15.37x4.19, 5.08P CASE 340AZ ISSUE A

DATE 11 FEB 2025

RECOMMENDED PCB PATTERN



NOTE: LAND PATTERN AND THROUGH HOLE DIMENSIONS SERVE ONLY AS AN INITIAL GUIDE. END-USER PCB DESIGN RULES AND TOLERANCES SHOULD ALWAYS PREVAIL.

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