Silicon Carbide (SiC) Diode - EliteSiC, TO-220-2L, 8 A, 650 V SiC Merged PiN-Schottky (MPS) Diode | UJ3D06508TS

Datasheet

Description

United Silicon Carbide, Inc. offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

CASE CASE RoHS AEC-Q101 Halogen Free Green

Part Number	Package	Marking		
UJ3D06508TS	TO-220-2L	UJ3D06508TS		

Features

- ◆ 175°C maximum operating junction temperature
- Easy paralleling
- Extremely fast switching not dependent on temperature
- No reverse or forward recovery
- Enhanced surge current capability, MPS structure
- Excellent thermal performance, Ag sintered
- 100% UIS tested

Typical Applications

- Power converters
- Industrial motor drives
- Switching-mode power supplies
- Power factor correction modules

Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units	
DC blocking voltage	V _R		650	V	
Repetitive peak reverse voltage, T _j =25°C	V_{RRM}		650	V	
Surge peak reverse voltage	V _{RSM}		650	V	
Maximum DC forward current	I _F	T _C = 152°C	8	Α	
Non-repetitive forward surge current		$T_C = 25$ °C, $t_p = 10$ ms	55	А	
sine halfwave	I _{FSM}	$T_C = 110$ °C, $t_p = 10$ ms	50	A	
Repetitive forward surge current		$T_C = 25$ °C, $t_p = 10$ ms	36.6	^	
sine halfwave, D=0.1	I _{FRM}	$T_C = 110^{\circ}C$, $t_p = 10$ ms	22.6	Α	
	I _{F,max}	$T_C = 25$ °C, $t_p = 10 \mu s$	370		
Non-repetitive peak forward current		$T_C = 110^{\circ}C$, $t_p = 10\mu s$	370	Α	
i ² t value	∫ i²dt	$T_C = 25^{\circ}C, t_p = 10 \text{ms}$	15	A ² s	
i t value		$T_{C} = 110^{\circ}C$, $t_{p} = 10$ ms	12.5		
- L		T _C = 25°C	115.4	144	
Power dissipation	P_{Tot} $T_c = 152$ °C 17.		17.7	W	
Maximum junction temperature	T _{J,max}		175	°C	
Operating and storage temperature	T _J , T _{STG}		-55 to 175	°C	
Soldering temperatures, wavesoldering only allowed at leads	T _{sold}	1.6mm from case for 10s	260	°C	

Datasheet

Electrical Characteristics

 $T_1 = +25$ °C unless otherwise specified

Parameter	Symbol	Test Conditions	Value			Units
			Min	Тур	Max	Units
	V _F	I _F =8A, T _J =25°C	-	1.5	1.7	V
Forward voltage		I _F =8A, T _J =150°C	-	1.8	2.1	
		I _F =8A, T _J =175°C	-	1.9	2.25	
Reverse current	I _R	V _R =650V, T _j =25°C	-	8	50	μΑ
		V _R =650V, T _J =175°C	-	29		
Total capacitive charge (1)	Q _c	V _R =400V		19		nC
	С	V _R =1V, f=1MHz		250		pF
Total capacitance		V _R =300V, f=1MHz		31		
		V _R =600V, f=1MHz		28		
Capacitance stored energy	E _C	V _R =400V		2.8		μЈ

⁽¹⁾ Q_c is independent on T_i , di_F/dt , and I_F as shown in the application note USCi_AN0011.

Thermal characteristics

Parameter	symbol	Test Conditions	Value			Units
			Min	Тур	Max	Ullits
Thermal resistance, junction - case	$R_{\theta JC}$			1.0	1.3	°C/W

Typical Performance

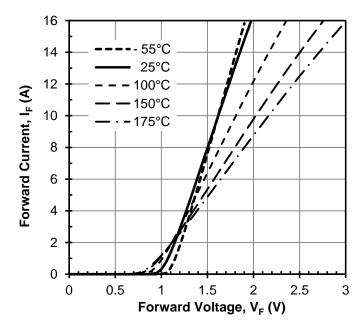


Figure 1 Typical forward characteristics

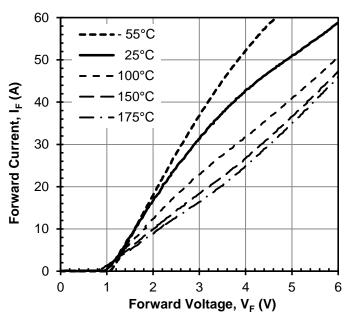
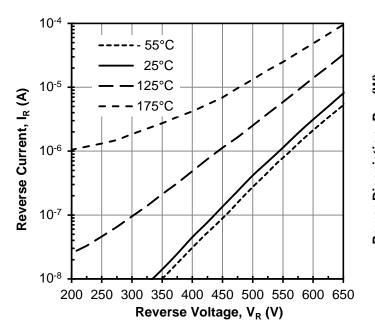


Figure 2 Typical forward characteristics in surge current

Datasheet



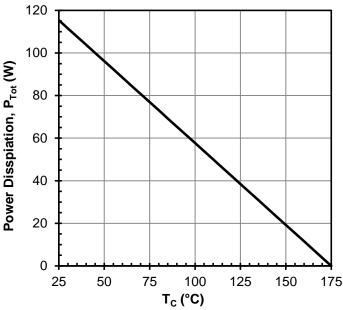
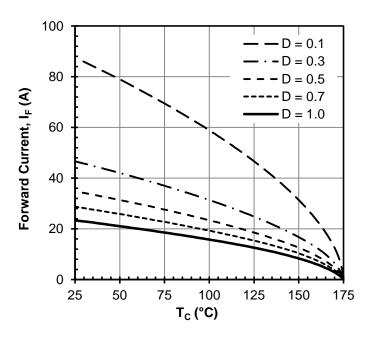


Figure 3 Typical reverse characteristics

Figure 4 Power dissipation



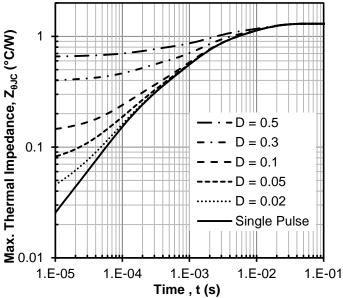


Figure 5 Diode forward current

Figure 6 Maximum transient thermal impedance



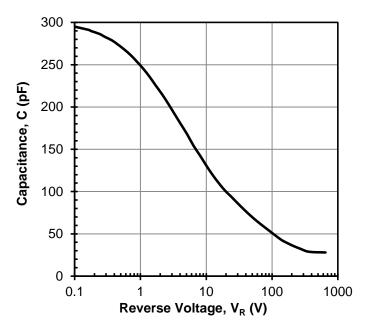


Figure 7 Capacitance vs. reverse voltage at 1MHz

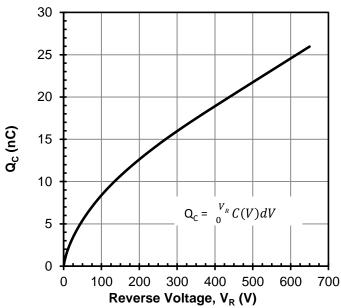


Figure 8 Typical capacitive charge vs. reverse voltage

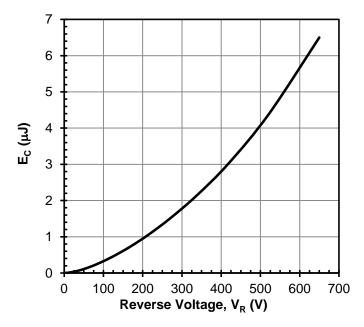


Figure 9 Typical capacitance stored energy vs. reverse voltage

Silicon Carbide (SiC) Diode - EliteSiC, TO-220-2L, 8A, 650V SiC Merged PiN-Schottky (MPS) Diode | UJ3D06508TS

Datasheet

Disclaimer

United Silicon Carbide, Inc. reserves the right to change or modify any of the products and their inherent physical and technical specifications without prior notice. United Silicon Carbide, Inc. assumes no responsibility or liability for any errors or inaccuracies within.

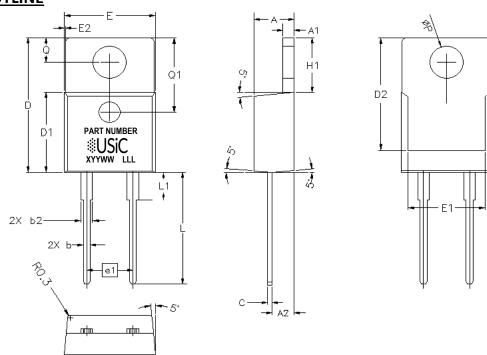
Information on all products and contained herein is intended for description only. No license, express or implied, to any intellectual property rights is granted within this document.

United Silicon Carbide, Inc. assumes no liability whatsoever relating to the choice, selection or use of the United Silicon Carbide, Inc. products and services described herein.



TO-220-2L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PACKAGE OUTLINE

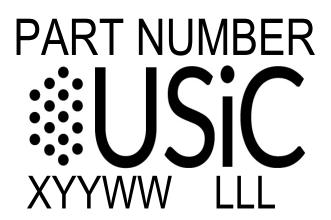


DIM	INC	HES	MILLIN	METERS
	MIN	MAX	MIN	MAX
Α	0.140	0.190	3.56	4.83
A1	0.020	0.055	0.51	1.40
A2	0.080	0.115	2.03	2.92
b	0.015	0.040	0.38	1.02
b2	0.040	0.070	1.02	1.78
С	0.014	0.030	0.36	0.76
D	0.560	0.650	14.22	16.51
D1	0.330	0.370	8.38	9.40
D2	0.480	0.517	12.19	13.13
E	0.380	0.420	9.65	10.67
e1	0.200	0.200 BSC		BSC
E1	0.270	0.350	6.86	8.89
E2	-	0.030		0.76
L	0.495	0.580	12.57	14.73
L1	-	0.250	1	6.35
ØΡ	0.139	0.161	3.53	4.09
Н	0.230	0.270	5.84	6.86
Q	0.100	0.135	2.54	3.43
Q1	0.330	0.340	8.38	8.64



TO-220-2L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PART MARKING



PART NUMBER = REFER TO
DS PN DECODER FOR DETAILS

X = ASSEMBLY SITE

YY = YEAR

WW = WORK WEEK

LLL = LOT ID

PACKING TYPE

ANTI-STATIC TUBE

QUANTITY /TUBE: 50 UNITS

DISCLAIMER

United Silicon Carbide, Inc. reserves the right to change or modify any of the products and their inherent physical and technical specifications without prior notice. United Silicon Carbide, Inc. assumes no responsibility or liability for any errors or inaccuracies within.

Information on all products and contained herein is intended for description only. No license, express or implied, to any intellectual property rights is granted within this document.

United Silicon Carbide, Inc. assumes no liability whatsoever relating to the choice, selection or use of the United Silicon Carbide, Inc. products and services described herein.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales