

Baseband Delay Line 64 µs (Improved Version)

Application

In TV sets, the integrated baseband delay line circuit is suitable for decoders with color-difference signal outputs

Description

The integrated delay line circuit U3665M is suitable for all chroma decoders with baseband color-difference outputs. It is suitable for PAL-, SECAM- and NTSC-signals as well. The U3665M contains two separate delay lines for processing (R–Y)-output and (B–Y)-output separately. The delay is performed by internally switched capacitors. On-chip postfiltering avoids the need for external filter components. In the case of the U3665M, the postfilter is tuned to Bessel-characteristic.

A summing circuitry combines the information of adjacent TV-lines, thus giving an interpolated sum for the PAL-system, storing preceding lines for the SECAM-system and providing a comb-filtered output for NTSC-signals. Due to internally generated timing, synchronization is easily done by feeding a line-frequent impulse (usually the SC-impulse) to the sync-input of the IC.

Features

- One line delay time, addition of delayed and non-delayed output signals
- Adjustment-free application, VCO without external components
- Handles negative or positive color-difference input signals
- Clamping of AC-coupled input signals [±(R-Y) and ±(B-Y)]

- Line-locked by the sandcastle pulse
- No crosstalk between SECAM color carriers (diaphoty)
- Comb-filtering functions for NTSC colordifference signals
- Correction of phase errors in the PAL system

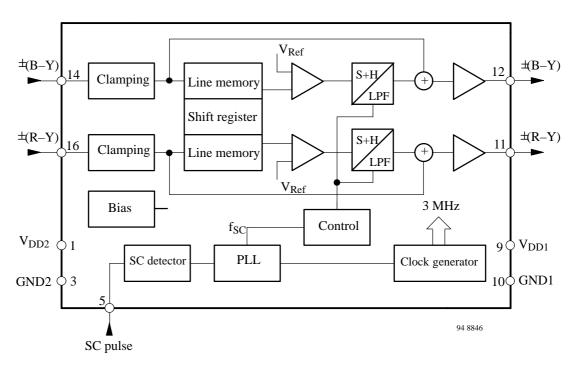


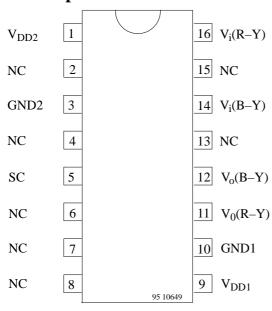
Figure 1. Block diagram



Ordering Information

Extended Type Number	Package	Remarks
U3665M-MDP	DIP16	
U3665M-MFP	SO16	

Pin Description



Pin	Cl1	F
Pin	Symbol	Function
1	V_{DD2}	Supply voltage for digital part
2	NC	Not connected
3	GND2	Ground for digital part
4	NC	Not connected
5	SC	Sandcastle-pulse input
6	NC	Not connected
7	NC	Not connected
8	NC	Not connected
9	V_{DD1}	Supply voltage for analog part
10	GND1	Ground for analog part
11	$V_{o (R-Y)}$	±(R-Y) output signal
12	$V_{o (B-Y)}$	±(B–Y) output signal
13	NC	Not connected
14	$V_{i(B-Y)}$	±(B-Y) input signal
15	NC	Not connected
16	$V_{i(R-Y)}$	±(R-Y) input signal

Figure 2. Connection diagram

Absolute Maximum Ratings

Reference point Pin 3, 10, unless otherwise specified

Parameters	Symbol	Min.	Тур.	Max.	Unit
Supply voltage (Pin 9)	V_{DD1}	-0.5		+7	V
Supply voltage (Pin 1)	V_{DD2}	-0.5		+7	V
Voltage on Pins 5, 11, 12, 14 and 16	V _n	-0.5		V_{S}	V
Output current, (Pin 11, Pin 12)	I _{out}			20	mA
Power dissipation	P			1.1	W
Storage temperature range	T _{stg}	-25		+150	°C
Electrostatic protection* for input/ output pins				500	V

^{*} MIL standard 883D, method 3015.7 machine model (all power pins connected together)

Operating Range

Parameters	Symbol	Value	Unit
Supply-voltage range (Pin 1, Pin 9)	V_s	4.5 to 5.5	V
Ambient-temperature range	T _{amb}	-10 to +70	°C

Thermal Resistance

Parameters	Symbol	Value	Unit
Junction ambient	R_{thJA}	80	K/W



Electrical Characteristics

 $V_{DD} = 5.0 \text{ V}$, $T_{amb} = 25 ^{\circ}\text{C}$, reference point, Pin 3 and Pin 10 connected together, sandcastle frequency of 15.625 kHz; unless otherwise specified

unless otherwise specified						
Parameters	Test Conditions / Pins	Symbol	Min.	Тур.	Max.	Unit
DC-supply	Pin 1, 9					
Supply voltage (analog part)	Pin 9	V_{DD1}	4.5	5.0	5.5	V
Supply voltage (digital part)	Pin 1	V_{DD2}	4.5	5.0	5.5	V
Supply current (analog part)	Pin 9	I _{S1}		3.5	8.0	mA
Supply current (digital part)	Pin 1	I_{S2}		1	2	mA
Power dissipation		P		30	60	mW
Color -ifference input signals	Pin 14, 16	5	L	l		
Input signal	,					
(peak-to-peak value)						
±(R–Y) PAL and NTSC	Pin 16	V_{i}		0.525	1.0	V
±(B–Y) PAL and NTSC	Pin 14	V_i		0.665	1.0	V
±(R-Y) SECAM	Pin 16	V_{i}		1.05	2.0	V
±(B–Y) SECAM	Pin 14	V_{i}		1.33	2.0	V
Input resistance	During clamping	R ₁₄ , R ₁₆			40	kΩ
Input capacitance		C ₁₄ , C ₁₆			10	pF
Input clamping voltage	Non color input level	V ₁₄		1.45		V
	during clamping	V ₁₆				
Color-difference output signa	Pin 11, 12	2				
Output signal						
(peak-to-peak value) ±(R-Y)	All standards Pin 11	V_{o}		1.05		V
±(B-Y)	All standards Pin 12	V_{o}		1.33		V
Ratio of output amplitudes at		V ₁₁ /V ₁₂	-0.4	0	+0.4	dB
equal input signals						
DC output voltage	Pin 11, 12	V _{11,12}		3.0		V
Output resistance	Pin 11, 12	R _{11,12}			400	Ω
Gain for PAL and NTSC	Ratio V _o / V _i	G_{v}	5.5	6.0	6.5	dB
Gain for SECAM	Ratio V _o / V _i	G_{v}	-0.5	0	+0.5	dB
Ratio of output signals for	$V_{i 14,16} = 1.33V_{pp},$	$V_{(n)}$		±0.1		dB
adjacent time samples at	SECAM signals,	$V_{(n+1)}$				
constant input signals	Pin 11 / Pin 12					
Noise voltage	$V_{i 14,16} = 0, R_{gen} < 300 \Omega$	V _{noise}			1.2	mV
(RMS value)	f = 10 kHz to 1 MHz					
	Pin 11, 12		62.04	64.0	64.06	
Delay of delayed signals		t _d	63.94	64.0	64.06	μs
Delay of non-delayed signals		t _d		65		ns
Transient time of delayed	300 ns transient of SECAM	t _{tr}		550		ns
signal	input signal, $C_{load} = 22 \text{ pF}$					
Transient time of	Pin 11, 12 300 ns transient of SECAM	+		250		no
non-delayed signal	input signal, C _{load} = 22 pF	t_{tr}		350		ns
non-delayed signal	Pin 11, 12					
Sandcastle-pulse input	Pin 5	1		1		
Sandcastle frequency	1 0	f_{SC}	14.0	15.625	17.0	kHz
Top pulse voltage	The leading edge of the burst-	V ₅	3	13.023	Vs+0.7	V
Top pulse voluge	key pulse is used for timing	, ,			7510.7	•
Internal slicing level	, r a a a a a a a a a a a a a a a a a a	V _{slice}	V ₅ –1.5	V ₅ -1.25	V ₅ -1.0	V
Input current		I ₅	J	3	10	μΑ
Input capacitance		C ₅			10	pF
_ r r r	I	1 -5		l		r*

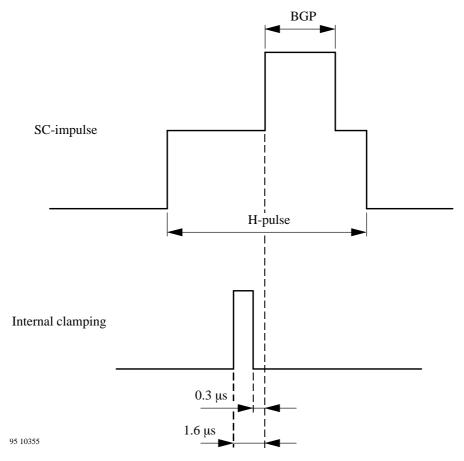


Figure 3. Timing of internal clamping

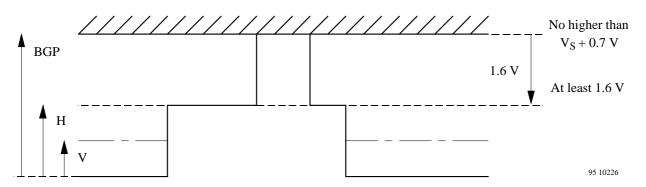
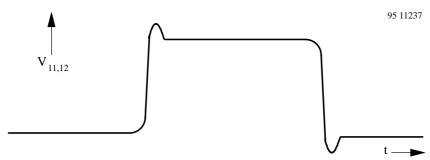
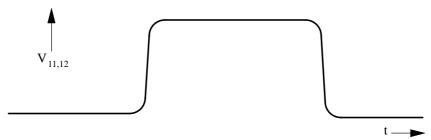


Figure 4. Restrictions to SC pulse





U3661M: internal postfilter with Butterworth characteristic



U3665M: internal postfilter with Bessel characteristic

Figure 5. Influence of internal postfilters on siganl outputs

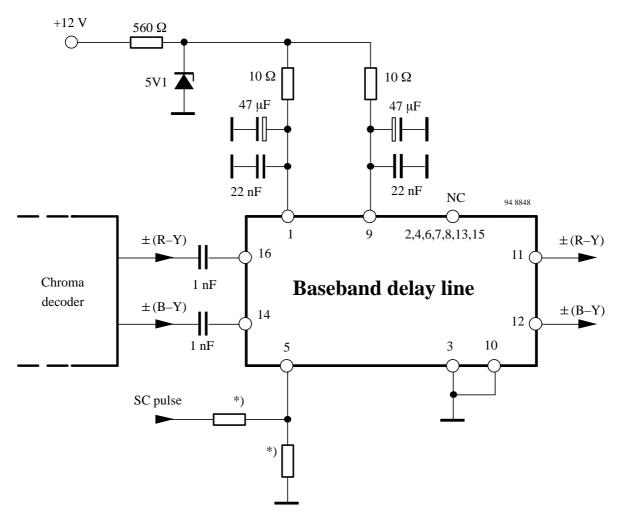


Figure 6. Typical application circuit

*)Depends on application (5 V - or 12 V SC pulse)

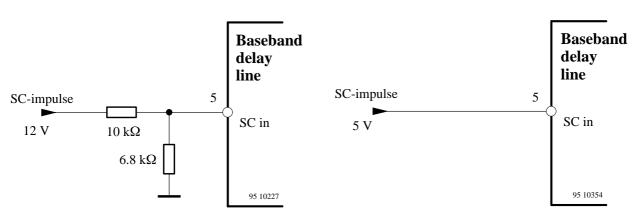


Figure 7. Application with 12 V SC-pulse

Figure 8. Application with 5 V SC-pulse

Internal Pin Circuits

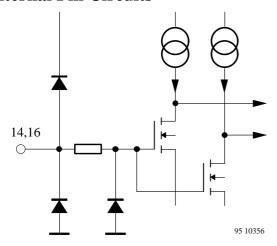


Figure 9. Color-difference signal inputs

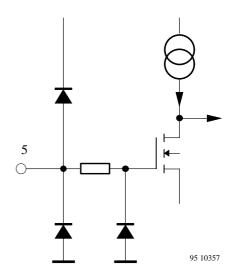


Figure 11. Sandcastle-pulse input

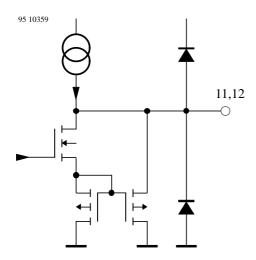


Figure 10. Color-difference signal outputs

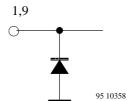
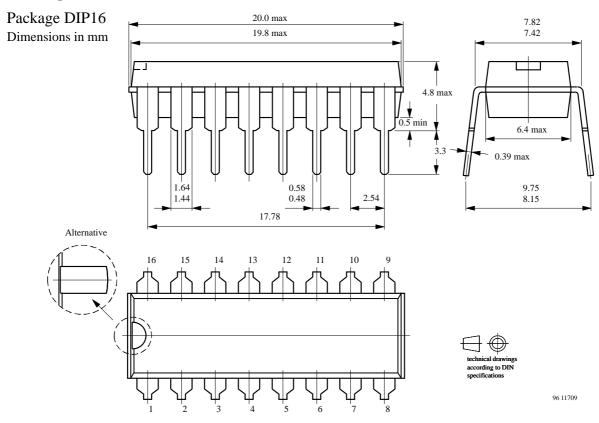


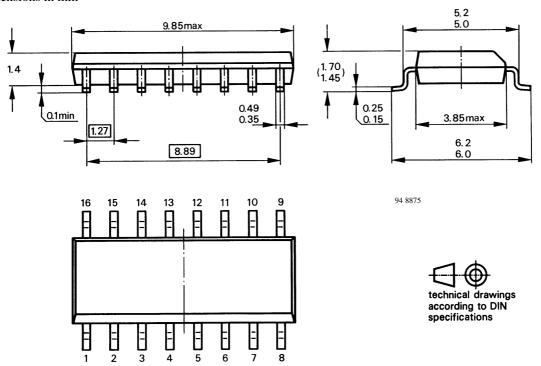
Figure 12. Supply voltage V_{DD2} , V_{DD1}



Package Information



Package SO16 Dimensions in mm





Ozone Depleting Substances Policy Statement

It is the policy of TEMIC TELEFUNKEN microelectronic GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC products for any unintended or unauthorized application, the buyer shall indemnify TEMIC against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

TEMIC TELEFUNKEN microelectronic GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany Telephone: 49 (0)7131 67 2831, Fax number: 49 (0)7131 67 2423