

monolithic dual n-channel JFETs designed for . . .



U231 U232 U233 U234 U235

Performance Curves NQP
See Section 4

■ Differential Amplifiers

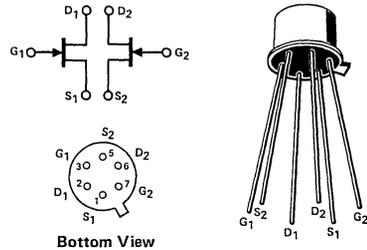
BENEFITS

- Good Matching Characteristics

ABSOLUTE MAXIMUM RATINGS (25°C)

Gate-Drain or Gate-Source Voltage	-50 V
Gate Current	50 mA
Total Device Dissipation at 25°C (Derate 1.7 mW/°C to 200°C)	300 mW
Storage Temperature Range	-65 to +200°C
Lead Temperature (1/16" from case for 10 seconds)	300°C

TO-71
See Section 6



Bottom View

ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

Characteristic		Min	Max	Unit	Test Conditions	
S T A T I C	1 I _{GSS} Gate Reverse Current		-100	pA	V _{GS} = -30 V, V _{DS} = 0	150°C
	2 BV _{GS} Gate-Source Breakdown Voltage	-50		nA		
	3 V _{GS(off)} Gate-Source Cutoff Voltage	-0.5	-4.5	V	I _G = -1 μA, V _{DS} = 0	
	4 V _{GS} Gate-Source Voltage	-0.3	-4.0		V _{DS} = 20 V, I _D = 1 nA	
	5 I _G Gate Operating Current		-50	pA	V _{DG} = 20 V, I _D = 200 μA	125°C
6 IDSS Saturation Drain Current (Note 1)	0.5	5.0	nA			
D Y N A M I C	7 g _{fs} Common-Source Forward Transconductance (Note 1)	1000	5000		V _{DS} = 20 V, V _{GS} = 0	f = 1 kHz
	8 g _{fs} Common-Source Forward Transconductance (Note 1)	1000				f = 100 MHz
	9 g _{os} Common-Source Output Conductance	600	1600	μmho	V _{DG} = 20 V, I _D = 200 μA	f = 1 kHz
	10 g _{os} Common-Source Output Conductance		35			
	11 C _{iss} Common-Source Input Capacitance		10		V _{DG} = 20 V, I _D = 200 μA	f = 1 MHz
12 C _{rss} Common-Source Reverse Transfer Capacitance		6	pF	V _{DS} = 20 V, V _{GS} = 0		
13 e _n Equivalent Short Circuit Input Noise Voltage		2			f = 100 Hz	

3

Characteristic		U231 Max	U232 Max	U233 Max	U234 Max	U235 Max	Unit	Test Conditions		
M A T C H I N G	15 I _{G1} -I _{G2} Differential Gate Current	10	10	10	10	10	nA	V _{DG} = 20 V, I _D = 200 μA	125°C	
	16 (I _{DSS1} -I _{DSS2}) / I _{DSS1} Saturation Drain Current Match (Note 1)	5	5	5	10	15	%	V _{DS} = 20 V, V _{GS} = 0		
	17 V _{GS1} -V _{GS2} Differential Gate-Source Voltage	5	10	15	20	25	mV	V _{DG} = 20 V, I _D = 200 μA		
	18 Δ V _{GS1} -V _{GS2} / ΔT Gate-Source Voltage Differential Drift (Note 2)	10	25	50	75	100	μV/°C			T _A = 25°C T _B = 125°C
	19 (g _{fs1} -g _{fs2}) / g _{fs1} Transconductance Match (Note 1)	3	5	5	10	15	%			T _A = -55°C T _B = 25°C
20 g _{os1} -g _{os2} Differential Output Conductance	5	5	5	5	5	μmho	f = 1 kHz			
21 g _{os1} -g _{os2} Differential Output Conductance	5	5	5	5	5	μmho				

NOTES:

1. Pulse test required, pulsewidth = 300 μs, duty cycle ≤ 3%.
2. Measured at end points, T_A and T_B.

NQP