

LIQUID CRYSTAL DISPLAY MODULE

Product Specification

PRODUCT NUMBER

TU41210EW1024G768WTL

INTERNAL APPROVALS						
Engir	neering	Docume	ent Control			
TED CHAN G	Digitally signed by TED CHANG Date: 2016.10.11 15:57:52 -07'00'		Digitally signed by Conner Martin DN: cn=Conner Martin, o=Densitron Corp., ou=Document Control, email=connor.martin@densitron.com, c=US Date: 2016.10.11 08:42:17 -07'00'			

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REVISION RECORD

Rev.	Date	Page	Sec.	Comment	ECN No.
A	10/11/16			New Densitron Release	E5277

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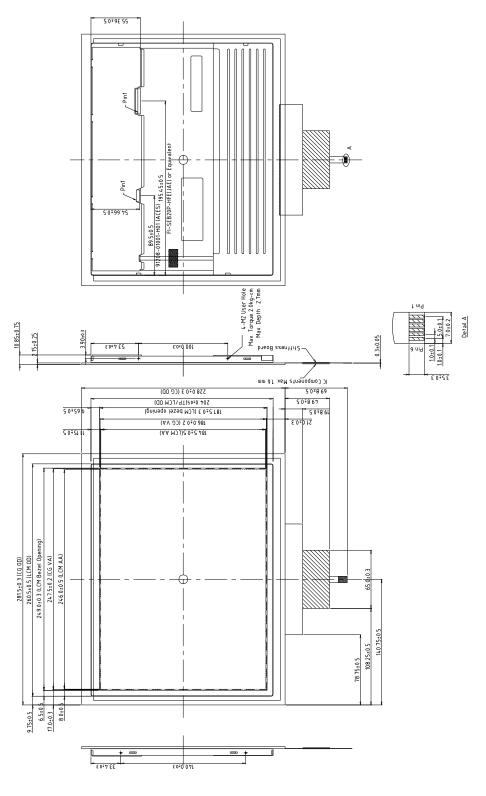
1 MAIN FEATURES

ITEM	DESCRIPTION	UNIT
Screen Size	12.1 Diagonal	in
Outline Dimension	281.5 (W) x 228.0 (H) x 10.85 (D)	mm
Display Format	1024 x 768	dots
Viewing Area	247.5 (W) x 186.0 (H)	mm
Active Area	246 (W) x 184.5 (H)	mm
LCD Type	Transmissive / Normally White	
Viewing Angle	80/80 (H right/left); 70/70 (V right/left)	
Backlight Type	LED	
Interface	LCD: LVDS PCT: USB	
Operating Temperature	-20 ~ 70	°C
Storage Temperature	-30 ~ 80	°C
RoHS Compliant	Yes	

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2 MECHANICAL DRAWING





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3 ABSOLUTE MAXIMUM RATINGS

ltem	Symbol	Condition	Min	Max	Unit	Note
Power Supply Voltage	VCC	Ta=25°C	-0.3	7	V	
Operating Temperature	ТОР		-20	70	°C	1
Storage Temperature	TST		-30	80	°C	1,2,3

Note:

- 1. 90% RH max for Ta < 50°C, and 60% RH for Ta ≥ 50 °C
- 2. In case of a temperature below 0° C, the response time of the liquid crystal (LC) becomes much slower and the color of the panel will appear darker. The level of retardation depends on the temperature due to the LC's characteristics.
- 3. Only operation is guaranteed at operating temperature. Contrast, response time, and other display qualities are evaluated at +25°C.

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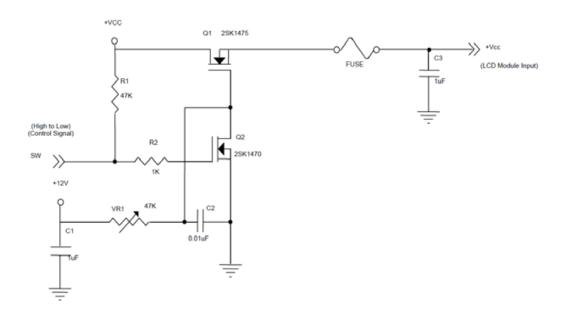
4 ELECTRICAL CHARACTERISTICS

ltem	Symbol	Condition	Min	Тур	Max	Unit	Note
Davisa Sumalis Valta as	V		3.0	3.3	3.6	V	1
Power Supply Voltage	V _{cc}		4.75	5.0	5.25	V	1
Rush Current	I _{RUSH}		-	-	4	Α	2
LVDS differential input voltage	[VID]		100	-	600	mV	
LVDS common input voltage	VICM		0.7	-	1.6	V	
Power Consumption	PL		1	2.0		W	1

Note:

1. The specified power consumption is under the conditions of Vcc = 3.3V or 5V, Fv = 60Hz.

2. Measurement Conditions:



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5 BACKLIGHT CHARACTERISTICS

5.1 LED DRIVING CONDITIONS

Itomo	Symbol Valu		lue	Unit	Note	
ltem	Symbol	Min.	Max.	Offic	Note	
Converter Voltage	Vi	-0.3	18	V	(1), (2)	
Enable Voltage	EN		5.5	V		
Backlight Adjust	ADJ		5.5	V		

Note:

- 1. Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under normal operating conditions.
- 2. Specified values are for lamp (see below)

Ta = 25 ± 2 °C

Parameter		Complete	Value			1114	Nata
		Symbol	Min.	Тур.	Max.	Unit	Note
Converter Power Supply V	oltage/	Vi	7	12.0	17	V	
Converter Power Supply C	urrent	lj	0.7	0.83	0.9	Α	@ Vi = 12V
converter rower supply c	diferi	"	0.7	0.65	0.5	A	(Duty 100%)
LED Device Communities		DLED		- 10		W	@ Vi = 12V
LED Power Consumption		PLED	-	10	-	VV	(Duty 100%)
EN Control Lovel	Backlight on		2.0	3.3	5.0	V	
EN Control Level	Backlight off	-	0		8.0	V	
PWM Control Level	PWM High Level		2.0	3.3	5.0	V	
PWW Control Level	PWM Low Level	-	0	-	0.15	V	
PWM Control Duty Ratio		-	10	İ	100	%	
PWM Control Frequency		fpWM	190	200	210	Hz	
LED Life Time		Լլ	50,000	-	-	Hrs	(2)

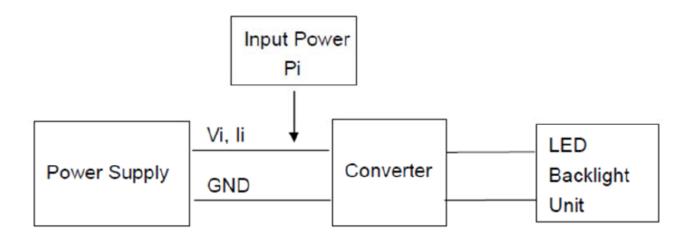
Note:

- 1. LED current is measured by utilizing a high frequency current meter as shown below:
- 2. The lifetime of the LED is defined as the time when it continues to operate under the conditions at $Ta = 25 \pm 2^{\circ}C$ and duty 100% until the brightness becomes $\leq 50\%$ of its original value. Operating the LED in a high temperature condition will reduce the life time and lead to a color shift.

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5.2 LED CIRCUIT





6 PROJECTED CAPACITIVE TOUCH PANEL

6.1 OPERATING CONDITION

Item	Symbol	Min	Тур.	Max	Unit	Note
Power Supply Voltage	V _{DD}	3.5	5.0	5.5	V	
Power Supply current (Active)	I _{DD}	-	85	-	mA	1
Power Supply current (Low Power)	I _{DD}	-	5	-	mA	
Output High Threshold Voltage	V _{OH}	2.5	-	-	V	
Output Low Threshold Voltage	V _{OL}	-	-	0.8	V	
Differential input sensitivity I(D+)-(D-)I	V_{DI}	0.2	-	-	V	
Differential input command mode range	V _{CM}	0.8	-	2.5	V	
Days and Dada	D.D.	100	-	-	11-	Single point
Report Rate	RR	70	-	-	Hz	Dual point

Note:

1. This test condition is touched with 10 points.

6.2 PIN ASSIGNMENT

Connector: CVILUX CF25101D0R0-05

Pin No.	Symbol	1/0	Description
1	GND	I	System ground
2	D-	1/0	USB D-
3	D+	1/0	USB D+
4	VDD	I	+5.0V power supply
5	NC	-	No Connection
6	RST	I	External reset signal, active high.

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7 INTERFACE TIMING

7.1 INPUT SIGNAL TIMING SPECIFICATIONS

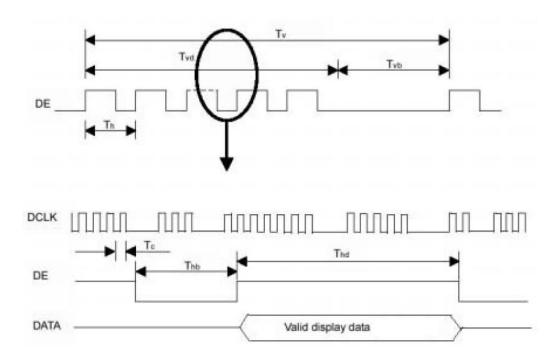
The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
	Frequency	Fc	57.5	64.9	74.4	MHZ	
	Total	Tv	774	806	848	Th	Tv=Tvd+Tvb
Vertical Active Display Term	Display	Tvd	-	768	-	Th	-
	Blank	Tvb	6	38	80	Th	-
Horizontal Active Display Term	Total	Th	1240	1344	1464	Tc	Th=Thd+Thb
	Display	Thd	-	1024	-	Tc	-
	Blank	Thb	216	320	440	Tc	-

Note:

- 1. Since this assembly is operated in DE only mode, Hsync and Vsync input signals should be set to low logic level. Otherwise, this assembly would operate abnormally.
- 2. Frame rate is 60Hz.

INPUT SIGNAL TIMING DIAGRAM

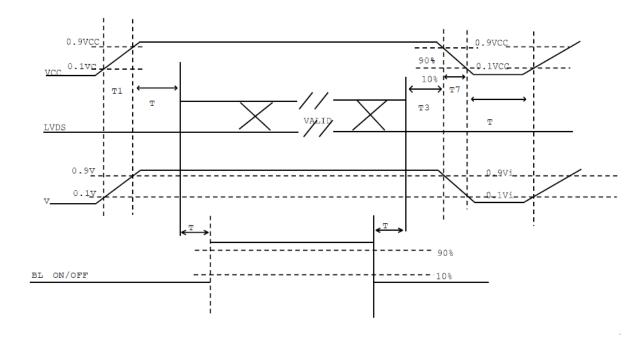


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7.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of an LCD assembly, the power on/off sequence should be conducted as shown in the diagram below.



Note:

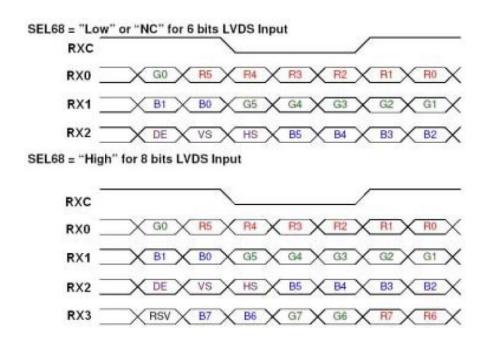
- 1. Please avoid the floating state of the interface signal at the invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply of LCD VCC to 0V.
- 3. The backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.

Parameter		Value		Units
Farameter	Min	Тур	Max	Offics
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	0	-	50	ms
T4	500	-	-	ms
T5	200	-	-	ms
T6	200	-	-	ms
T7	5	-	300	ms

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7.3 INPUT DATA FORMAT



Note:

- 1. R/G/B data 7: MSB, R/G/B data 0: LSB
- 2. Please follow PSWG

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data
R6	Red Data 6	Each red pixel's brightness data consists of these
R5	Red Data 5	8 bits pixel data.
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data
G6	GreenData 6	Each green pixel's brightness data consists of these
G5	GreenData 5	8 bits pixel data.
G4	GreenData 4	The second second second
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data
B6	Blue Data 6	Each blue pixel's brightness data consists of these
B5	Blue Data 5	8 bits pixel data.
B4	Blue Data 4	The production of the producti
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RXCLKIN+	LVDS Clock Input	
RXCLKIN-		
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

3. Output signals from any system shall be low or Hi-Z state when VCC is off.

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8.1 TEST CONDITIONS

Item	Symbol	Value	Unit	
Ambient Temperature	Ta	25 +/- 2	°C	
Ambient Humidity	Ha	50 +/- 10	%RH	
Supply Voltage	Vcc	3.3	V	
Input Signal	According to typical v	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Converter Voltage	Vin	12	V	
Converter Duty		100%		

8.2 OPTICAL SPECIFICATION

The relative measurement methods of optical characteristics are shown in 4.2. The following items should be measured under the test conditions described in 4.1 and stable environment shown in note 5.

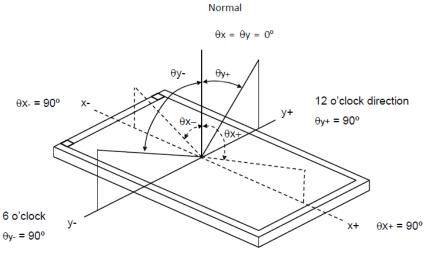
Iten	n	Symbol	Condition	Min.	Тур.	Max.	Unit	Note
	Dod	Rx			0.625		-	
	Red	Ry			0.358		-	
	Creen	Gx			0.324		-	
Color	Green	Gy		Тур -	0.604	Typ+	-]
Chromaticity	Blue	Bx	$\theta_X=0^\circ, \ \theta_Y=0^\circ$	0.05	0.144	0.05	-	(1), (5)
,	blue	Ву	CS-1000		0.088		-	
	White	Wx			0.313		-	
	wnite	Wy			0.329		-	
Center Luminance	of White	Lc		450	510	-	-	(4), (5)
Contrast Ratio		CR		500	700	-	-	(2), (5)
Contrast Ratio in	daylight		Sun lamp	100	120	-	-	(7)
Response Time		T _R	θ _x =0°, θ _Y =0°	-	5	10	ms	(3)
nesponse nine		T _F	θχ-υ , θγ -υ	-	11	16	ms	(3)
White Variation		W	θ_X =0°, θ_Y =0°	-	1.25	1.4	-	(5), (6)
	Hanisantal	x+		70	80	-		
	Horizontal	x-	07.40	70	80	-		
Viewing Angle	Vertical	γ+	CR≥10	60	70	-	Deg.	(1), (5)
	vertical	γ-		60	70	-		

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Note:

1. Definition of Viewing Angle (θx , θy)



2. Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

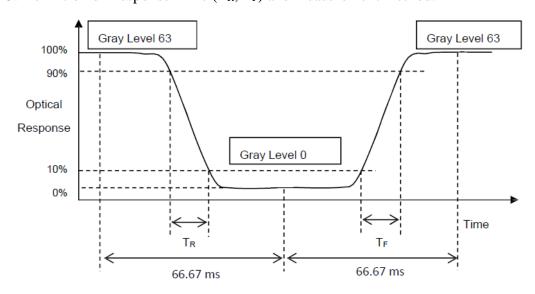
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

3. Definition of Response Time (T_R, T_F) and measurement method:



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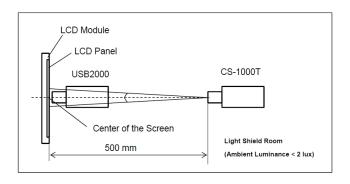


4. Definition of Luminance of White (Lc): Measure the luminance of gray level 63 at center point Lc = L (5)

L(x) is corresponding to the luminance of the point X in note 6.

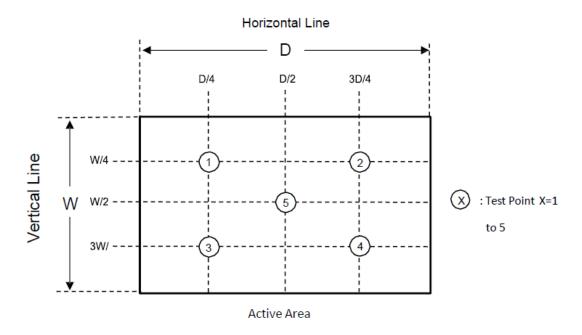
5. Measurement setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting the backlight for 20 minutes in a windless room.



6.

Measure the luminance of gray level 63 at 5 points



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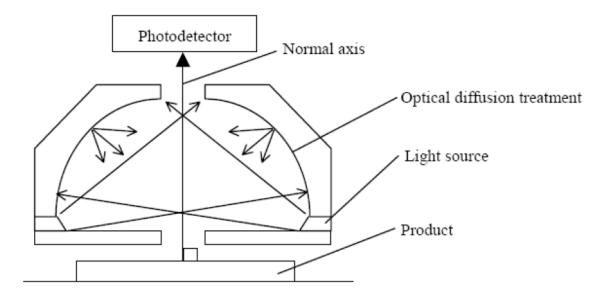


7. Contrast Ratio in daylight

Measuring carried out at backlight unit on

Sun lamp:10000 Lux

Contrast Ratio in daylight=Luminance of white screen/ Luminance of black screen



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9 INTERFACE PIN ASSIGNMENT

9.1 LCM PIN ASSIGNMENT

Pin	Name	Description	Remark
1	RX3+	Differential Data Input, CH3 (Positive)	
2	RX3-	Differential Data Input, CH3 (Negative)	
3	NC	NC	
		LVDS 6/8 bit select function control,	Note (3)
4	SEL68	Low or NC → 6 bit Input Mode High →	
		8bit Input Mode	
5	GND	Ground	
6	RXC+	Differential Clock Input (Positive)	
7	RXC-	Differential Clock Input (Negative)	
8	GND	Ground	
9	RX2+	Differential Data Input , CH2 (Positive)	
10	RX2-	Differential Data Input , CH2 (Negative)	
11	GND	Ground	
12	RX1+	Differential Data Input , CH1 (Positive)	
13	RX1-	Differential Data Input, CH1 (Negative)	
14	GND	Ground	
15	RX0+	Differential Data Input, CH0 (Positive)	
16	RXO-	Differential Data Input, CH0 (Negative)	
		Horizontal Reverse Scan Control,	Note (3)
17	reLR	Low or NC → Normal Mode.	
		High → Horizontal Reverse Scan	
		Vertical Reverse Scan Control,	Note (3)
18	reUD	Low or NC → Normal Mode, High	
		→ Vertical Reverse Scan	
19	VCC	Power supply	
20	VCC	Power supply	

Note:

- 1. Connector Part 076B20-0048RA-G4 or JAE FI-SEP20P-HFE or equivalent.
- 2. User's connector part no: JAE FI-SE20ME or equivalent.
- 3. "Low" stands for 0V. "High" stands for 3.3V. "NC" stands for "Not connected".

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Pin	Name	Description	Remark
1	Vi	Converter input voltage	12V
2	Vi	Converter input voltage	12V
3	Vi	Converter input voltage	12V
4	Vi	Converter input voltage	12V
5	V _{GND}	Converter ground	Ground
6	V_{GND}	Converter ground	Ground
7	V_{GND}	Converter ground	Ground
8	V _{GND}	Converter ground	Ground
9	EN	Enable pin	3.3V
10	ADJ	Backlight Adjust	PWM Diming (190-210Hz, Hi:3.3Vpc, Lo:0Vpc)
			(130-210HZ, HI.3.3VDC, LO.UVDC)

Note:

1. Connector Part No: 91208-01001-H01 or equivalent.

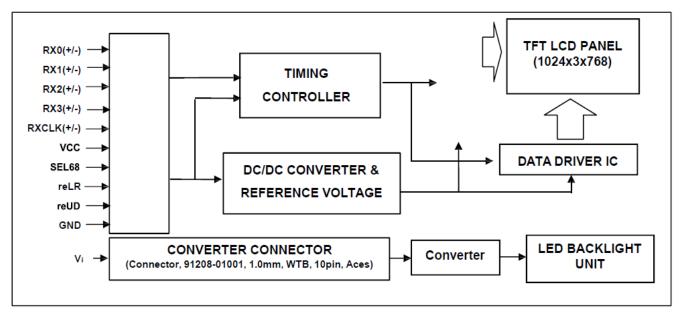
2. User's connector Part No: 91209-01011 or equivalent.

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10 BLOCK DIAGRAM



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11 RELIABILITY



11.1 RELIABILITY TEST

Test Item		Test Condition
	High Temperature Storage	Ta= 80°C 240h
ts l	Low Temperature Storage	Ta=-30°C 240h
Durability Test	Temperature Cycle Storage	-30°C for 30 min, then 80°C for 30 min, 100 cycles
rabili	High Temperature Operation	Tp= 70°C 240h
P	Low Temperature Operation	Tp= -20°C 240h
	High Temperature & Humidity Operation	Tp= 60°C RH= 90% 240h Non condensing
	Box Drop	1 Corner, 3 Edges, 6 Faces, 66 cm high

TA = Ambient Temperature TP = Panel Temperature

Note:

- 1. No dew condensation to be observed.
- 2. The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removing from the test chamber.
- 3. No cosmetic or functional defects should be allowed.
- 4. Total current consumption should be less than twice the initial value.

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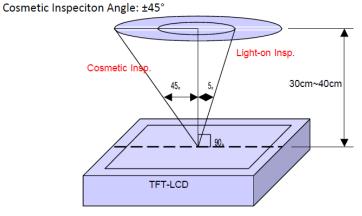




12.1 DELIVERY INSPECTION STANDARDS

Inspection distance: $35 \text{ cm} \pm 5 \text{ cm}$ Viewing angle: $\pm 45^{\circ}$

Light-on Inspection Angle: ±5°



(Perpendicular to LCD panel surface)

Environmental Conditions

Ambient Temperature		23°C±5°C
Ambient Humidity		55±10%RH
Ambient Illumination	Cosmetic Inspection	400~600 Lux
	Functional Inspection	100~200 Lux

Sampling Conditions

- 1. Lot size: quantity of shipment lot per model
- 2. Sampling method:

Sampling Plan		MIL-STD-105E
		Normal Inspection, Single Sampling
		Level II
	Major Defect	0.65%
AQL	Minor Defect	1.5%

3. The classification of Major(MA) and Minor(MI) defects is shown as 7.1.4 Inspection criteria.

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12.2 INSPECTION CRITERIA

Cosmetic Inspection (Panel):

Item	Judgment Criteria	Classification
Chipping on Panel/Touch Panel	a≤3.0mm, b≤3.0mm, c≤t (Bottom glass thickness)	MA
Scratch on Panel/Touch Panel *Note-2	W≤0.05 mm and L≤8 mm, Ignored; 0.05 mm <w≤0.1 l≤10mm,="" mmand="" n≤4<br="">W>0.1 mm or L>10 mm · Not allowed</w≤0.1>	MI
Bubble or Dent on Panel/Touch Panel *Note-3	D≤0.2mm, Ignored; 0.2mm <d≤0.6mm, n≤4<br="">D>0.6mm, Not allowed</d≤0.6mm,>	MI
Panel/Touch Panel Crack	Not Allowed	MA
Bezel Oxidation	Not allowed if it rusts continuously over 1 cm (It is out of warranty with rusted tin plate)	MI
Bezel Scratch	L≤20mm , W≤0.3 , N≤7	MI
Metal Squash Dent /Flange(Front Side)	D(W) ≤1 mm , L≤3 mm , N≤4	MI
Polarizer flaw or leak out resin	Defect is defined as the view area.	MI
Outline Dimension	Must be within spec, refer to mechanical drawing	MI

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Functional Inspection:

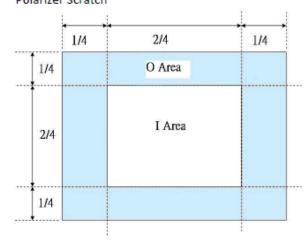
Item		Judgment Criteria		
	Bright dot	Random 2 dots adjacent 3 dots adjacent or more	3 1 0	
	Dark dot	Random 2 dots adjacent 3 dots adjacent or	3	
	Total Dot Def	more	0 5	1
Point Defect		Distance between Bright and Bright dot	L≥15mm	МІ
	Distance	Distance between Bright and Dark dot	L≥10mm	
		Distance between Dark dot	L≥5mm	
	(2)It is ignore (3) 'Weakness	d as Point Defect if defect area d if defect area≤0.5dot s will be defined as a bright dot if 6% ND filter		
Mura	Not allo	wed if it can be observed throu	igh ND Filter 6 %	МІ
Foreign Material in spot shape *Note-3	D≤0.15mm, Ignored 0.15mm <d≤0.5mm,n≤4(for d="" lcm="" lcm),="" n≤5(for="" tp);="" with="">0.5mm, Not allowed</d≤0.5mm,n≤4(for>			МІ
Foreign Material in line or spiral shape *Note-4	W≤0.05mm and L≤5mm,Ignored 0.05mm <w≤0.1mm, 5mm,="" and="" l="" lcm),<br="" n≤4(for="" ≤="">N≤5(For LCM With TP); W>0.1mm, L>5mm, Not allowed</w≤0.1mm,>			МІ
Touch panel Malfunction *Note-5	No Malfunction allowed in AA area.		MA	

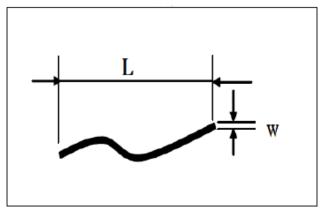
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Note-1 I/O Area Definition Polarizer Scratch

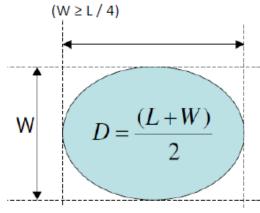
Note-2:

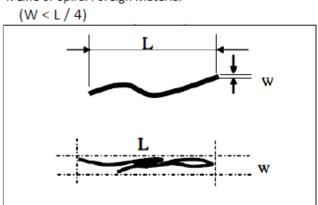


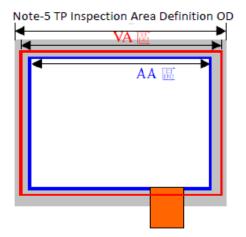


Note-3 Spot Foreign Material

Note-4: Line or Spiral Foreign Material







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13 PART NUMBER LABELLING

All parts will be labelled with the below Part Number.

DENSITRON TU41210EW1024G768WTL *Rev X **COUNTRY *YYMM

* where "Rev X" denotes the current Revision and YY and MM the 2-digit Year and Month of manufacture *where "Country" denotes the origin of the manufacture

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Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface. When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air very clean. Design the system so that no input signal is given unless the power supply voltage is applied.

Caution during LCD Cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane. Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

Caution against Static Charge

As the display uses C-MOS LSI drivers, connect any unused input terminals to VDD or VSS. Do not input any signals before power is turned on. Also, ground your body, work / assembly table and assembly equipment to protect against static electricity.

Packaging

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunlight or high temperature / humidity.

Caution during Operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation. Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged. If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once. Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

Storage

Store the display in a dark place where the temperature is $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ and the humidity below 50% RH. Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

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