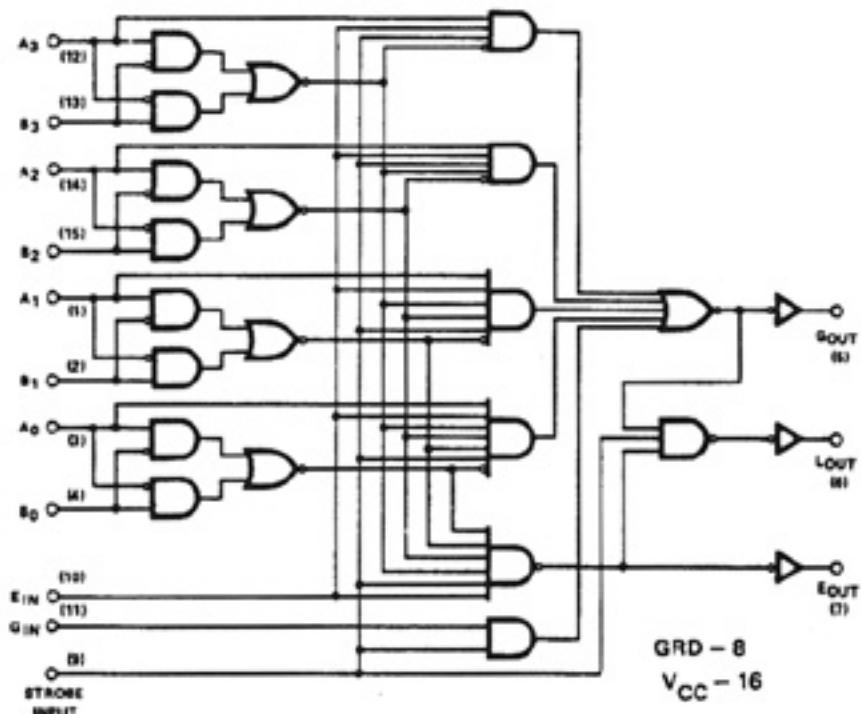


Features

- IDEAL FOR DIGITAL CONTROLS REQUIRING COMPARATORS
- CASCADES TO COMPARE LARGE BINARY OR BCD NUMBERS
- ACTIVE-HIGH OUTPUTS
- STROBE INPUT

Logic Diagram



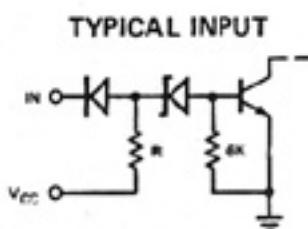
General Description

The 343 compares two 4-bit binary numbers, A_8, A_4, A_2, A_1 , and B_8, B_4, B_2, B_1 . The comparison generates an active high output indicating whether A is greater than, equal to, or less than B . Longer binary numbers can be compared by operating a number of comparators in cascade. A strobe is provided to hold all outputs low.

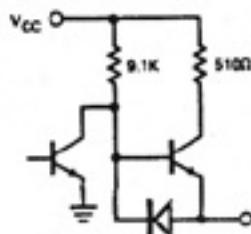
Truth Table

	INPUTS			OUTPUTS		
	E _{IN}	G _{IN}	STROBE	E _{OUT}	G _{OUT}	L _{OUT}
A > B	1	0	1	0	1	0
A = B	1	0	1	1	0	0
A < B	1	0	1	0	0	1
A > B	1	1	1	0	1	0
A = B	1	1	1	1	1	0
A < B	1	1	1	0	1	0
A > B	0	1	1	0	1	0
A = B	0	1	1	0	1	0
A < B	0	1	1	0	1	0
A > B	0	0	1	0	0	1
A = B	0	0	1	0	0	1
A < B	0	0	1	0	0	1

NOTE: If the device is being used correctly E_{IN} and G_{IN} will never be high at the same time. Whenever the strobe is low (logical "0"), all outputs will be low regardless of the input states.

Equivalent Circuits

R = 8.2K ON A,B
R = 16.4K ON G_{IN}
R = 4.1K ON E_{IN}, STROBE

TYPICAL OUTPUT**Specifications**

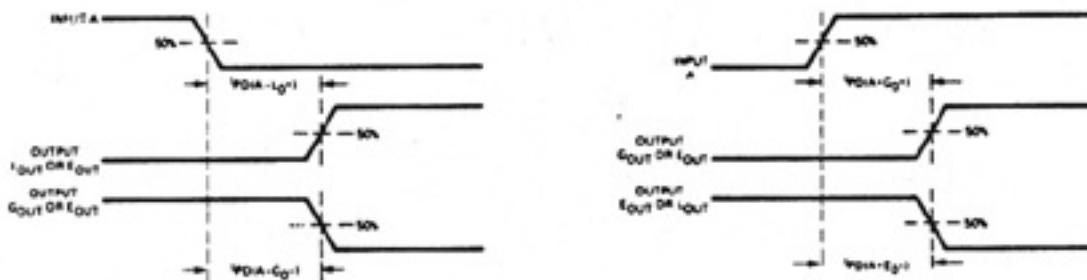
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I _{CC} (WORST-CASE)	42 mA @ 13V, 56 mA @ 16V
t _{PD} I/O FUNCTION FOR t _{PD}	1 microsecond A+ or A- to G, E, or L+ or -

NOTE:

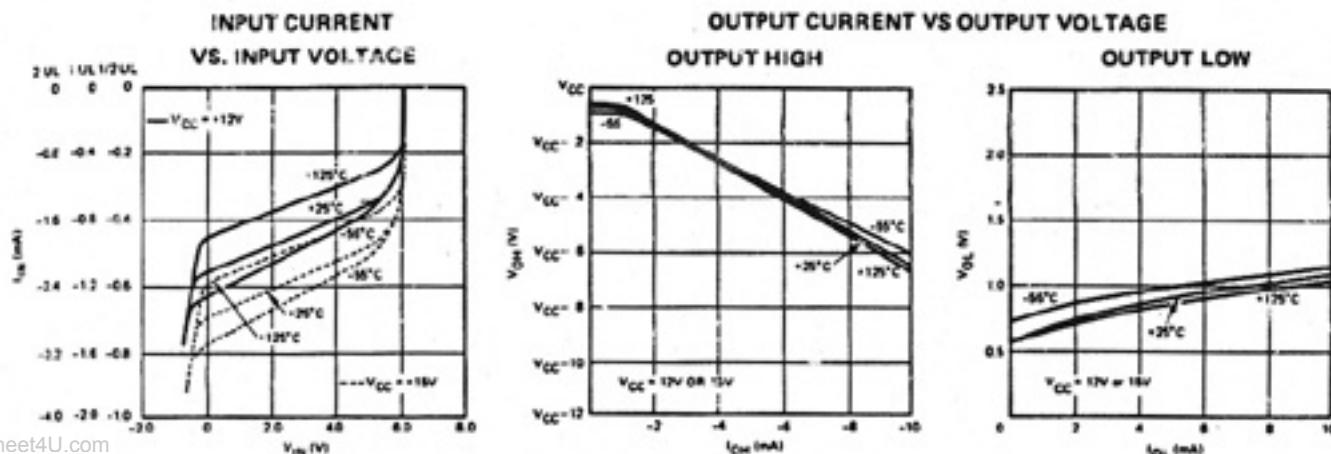
I_{CC} is tested at V_{CC} ±1 Volt (+13V for C type and 16V for A type) and is guaranteed across the applicable temp range.
t_{PD} is guaranteed at V_{CC} ±1V and across the applicable temp range with the output loaded with 5 unit loads.

Switching Time Waveforms**Loading Table**

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PINS	FUNCTION	LOADING
A,B	Data inputs	1 UL
E _{IN}	"Equal to" input	2 UL
G _{IN}	"Greater than" input	1 UL
Strobe	Strobe input	2 UL
E _{OUT}	"Equal to" output	5 UL
G _{OUT}	"Greater than" output	5 UL
L _{OUT}	"Less than" output	5 UL

Typical Performance Characteristics

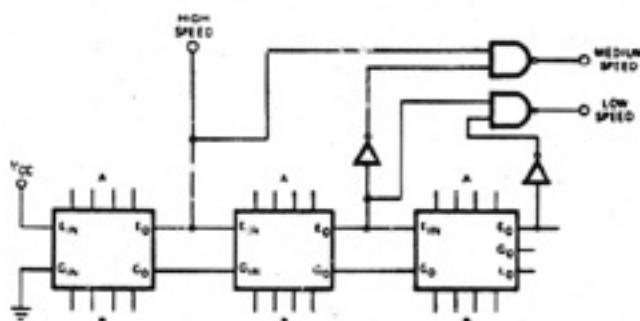


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Typical Applications

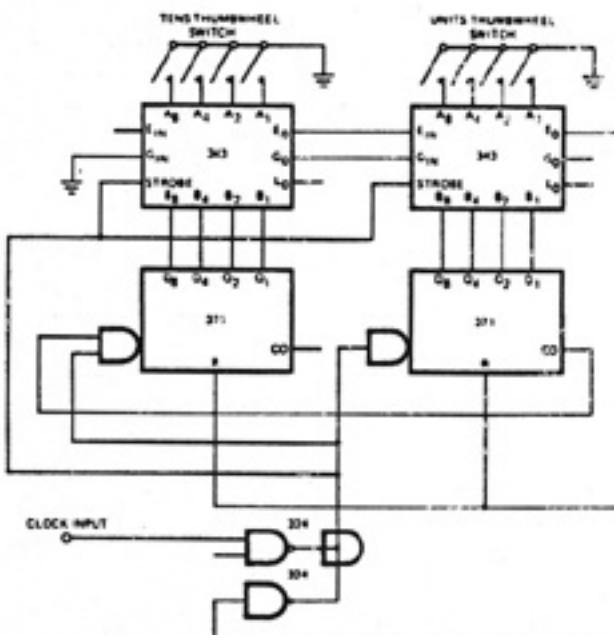
The "equal to" and "greater than" outputs can be connected to the corresponding inputs of another 343 to cascade the comparators. In this way, two binary or BCD numbers of any length can be compared. The final stage's outputs will indicate their relative magnitude. The unused "equal to" input should be left open, and the unused "greater than" input should be grounded.

SERVO OVERSHOOT CONTROL



Speed information is provided a servo positioning system by three comparators in cascade. The most significant digit of the control word is applied to the comparator at the left. If the device controlled is a long way from a new position, only the high speed output will be low. As the device approaches the new position, this output will go high, and the medium speed output will go low. When the device is very close, only the low speed output will be low. This allows regulation of motor speed to limit device overshoot. The circuit shown is a simplified version that is intended only to show the general application of the 343 to servo positioning controls. The actual circuit is more involved since the possibility exists that the controlled device may be on the other side of the desired position.

MOD N COUNTER



In this example, the counter modulus is any number up to 99 set with two thumbwheel switches. When the counters reach this number, the second comparator's E_{out2} resets the counters. The 343 strobe inputs are tied to the system clock to prevent the possibility of false comparisons being made when the counters are changing state.