Single Stage Flyback or Buck-Boost Current Control with Active PFC for LED lighting

DESCRIPTION

ONDUCTOR

TS19705 is constant current control IC with active power factor correction isolated or non-isolated AC-DC LED lighting control. The TS19705 is configurable as an isolated, primary-side-regulated Flyback controller or non-isolated Buck-Boost converter for LED lighting applications. The IC achieves high power factor and low Total Harmonic Distortion (THD) operation by Boundary Conduction Mode (BCM). The line and load regulation of LED current is about <±3% because of particular control method. TS19705 also provides gate driving voltage clamping, V_{CC} overvoltage protection and system output open/short circuit protection to increase IC performance.

FEATURES

- Accuracy Constant Current <±3%
- Low BOM Cost
- Flyback or Buck-Boost w/o photocoupler
- Application Voltage Range: 90~264V_{AC}
- Built-in Active Power Factor Correction
- Good Line and Load Regulation
- Open-LED Protection on DMG pin
- Over-Voltage Protection on V_{CC} pin
- Short-LED Protection
- Cycle by Cycle Over Current Protection on CS pin
- Over-Temperature Protection
- Gate Driving Voltage Clamping

APPLICATION

- LED lighting
- Down lights, Tube lamps, PAR Lamps, Bulbs





Pin Definition:

- 1. COM
- 2. GND
- 3. CS
- 4. Output
- 5. V_{CC} 6. DMG
- 6. DIVIG

TYPICAL APPLICATOIN CIRCUIT

Notes: MSL 3 (Moisture Sensitivity Level) per J-STD-020



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ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise specified) (Note 1)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Power supply pin	V _{CC}	40	V		
DMG voltage to GND	V _{DMG}	-0.3 to 40	V		
OUT voltage to GND	V _{OUT}	-0.3 to 40	V		
CS voltage to GND	V _{CS}	-0.3 to 5.5	V		
COM voltage to GND	V _{COM}	-0.3 to 5	V		
Junction Temperature Range	TJ	-40 to +150	°C		
Storage Temperature Range	T _{STG}	-65 to +150	°C		
Power Dissipation @T _A =25 °C	PD	0.3	W		
ESD Rating (Human Body Model)	ESD	2	kV		

THERMAL PERFORMANCE (Note 3)				
PARAMETER	SYMBOL	ТҮР	UNIT	
Thermal Resistance - Junction to Case	R _{ejc}	106	°C/W	
Thermal Resistance - Junction to Ambient	R _{θJA}	220	°C/W	

RECOMMENDED OPERATING CONDITION ($T_A = 25^{\circ}C$ unless otherwise specified) (Note 4)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Power supply pin	V _{cc}	38	V		
DMG voltage to GND	V _{DMG}	-0.3 to 38	V		
OUT voltage to GND	V _{OUT}	-0.3 to 38	V		
CS voltage to GND	V _{CS}	-0.3 to 5	V		
COM voltage to GND	V _{COM}	-0.3 to V_{CC}	V		
Operating Junction Temperature Range	TJ	-40 to +125	°C		
Operating Ambient Temperature Range	T _{OPA}	-40 to +85	°C		

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise specified)						
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Supply Voltage						
Turn-on voltage	V _{CC_ON}		17	18	19	V
Turn-off voltage	V_{CC_OFF}		8.5	9.5	10	V
Quiescent current 1	I _{Q1}	V _{CC} =17V, @ V _{CC} off		30	50	μA
Quiescent current 2	I _{Q2}	Start-up @ 4.5kHz		600	800	μA
Operation supply current	I _{CC}			2.5	30	mA
Protection						
V _{CC} voltage protection	V _{OVPA}		31	32	34	V
Output voltage protection	V _{OVPS}		10	10.5	11	V
CS limit voltage	V _{OCP}		1.15	1.25	1.35	V
Short circuit protection	V _{SCP}	Guaranteed by design		3		V
Oscillator						
Start-up timer	t _{STR}			220		μs



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ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise specified)						
PARAMETER	SYMBOL	CONDITION	MIN	ТҮР	MAX	UNIT
GM Amplifier						
Transconductance	g _m			60		μA/V
Source current	I _{COMP_SOU}			20		μA
MOSFET Section						
Dropout voltage	V _{он}	V_{CC} =33V, I_{O} = 10mA		1.2	1.5	V
	V _{OL}	V_{CC} =33V, I_{O} = -10mA		0.12		V
Rising time	t _r	V_{CC} =20V, C_{O} =1nF		40		ns
Falling time	t _f	V_{CC} =20V, C_{O} =1nF		80		ns
Output clamp voltage	V _{O_CLAMP}				19	V
Leading edge blanking time	LEBt			0.5		μs

Note:

 Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

2. Devices are ESD sensitive. Handing precaution recommended.

3. Thermal Resistance is specified with the component mounted on a low effective thermal conductivity test board in free air at $T_A=25$ °C.

4. The device is not guaranteed to function outside its operating conditions.

5. Guaranteed by design.

6. Auto Recovery Type.

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
TS19705CX6 RFG	SOT-26	3,000pcs / 7"Reel



FUNCTION BLOCK



PIN DESCRIPTION

PIN NO.	NAME	FUNCTION
1	COM	Output pin of error amplifier.
2	GND	Ground return for all internal circuitry.
3	CS	Input current sense pin.
4	OUT	Power MOS output pin.
5	V _{CC}	Power supply pin for all internal circuitry.
6	DMG	Zero current demagnetization sensing.



APPLICATION INFORMATION

Function Description

TS19705 is constant current control IC with active power factor correction isolated or non-isolated AC-DC LED lighting control. The TS19705 is configurable as an isolated, primary-side-regulated Flyback controller or nonisolated Buck-Boost converter for LED lighting applications. The controller is Boundary Conduction Mode (BCM) operation with constant on time based regulator design to achieve high power factor performance.

The TS19705 has built-in V_{CC} over voltage protection, open LED protection, short LED protection, over temperature protection, primary side current limit and gate clamp functions.

The average output current can be expressed as below.

$$Io_avg = \frac{NP}{NS} \times \frac{0.333 * \eta}{2 \times RS}$$

Where :

- I_{O_avg} is the average output current
- N_P is the primary-side turn ratio
- N_S is the secondary-side turn ratio
- R_S is the sensing resistor connected between the MOSFET source and the GND

Pin Detail

Compensation

This is the output of the g_m amplifier. Connect with a suitable RC network to ground.

Ground

GND is the reference node of internal circuit.

Current Sense

MOSFET current signal sensing for multiplication and current limit setting function.

$$ICS(LIMIT) = \frac{1.25}{Rs}$$

Where :

- I_{cs} is the input current sence
 R_s is the sensing resistor connected between the MOSFET source and GND

Output

Gate drive for external MOSFET switch and is built-in gate clamp function.

V_{cc}

Power supply for the controller during normal operation. The controller will start up when V_{CC} reaches 18V (typical) and will shut-down when V_{CC} voltage is below 9.5V (typical). A decoupling capacitor should be connected between the V_{CC} and GND pin as close as possible.

The TS19705 performs V_{CC} over voltage protection though V_{CC} pin. Once V_{CC} pin exceeds in 32V, TS19705 turns off and latchs out the MOSFET switcher until V_{CC} goes below V_{CC OFF}.

APPLICATION INFORMATION (Continue)

<u>Pin Detail</u>

DMG

The Output voltage is reflected by the auxiliary winding (N_{AUX}) voltage of Flyback transformer, the DMG pin can sense output information to depart from start up voltage ($V_{O_{-}STR}$) and protection voltage ($V_{O_{-}OVP}$).

When DMG sense voltage under $V_{O_{STR}}$, the circuit will work on short circuit protection, $f_{STR}=1/t_{STR}$.

When DMG sense voltage over $V_{O_{OVP}}$, the circuit will work on over voltage protection, it will latchs out until V_{CC} goes below $V_{CC_{OFF}}$.



Where :

- V_{OUT_OVP} is the output-over-voltage protection point
- V_{OVPS} is the over voltage protection signal
- V_{O_STR} is the start-up timer
- N_{AUX} is the number of auxiliary-winding turns
- N_{SEC} is the number of secondary-winding turns



PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

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ICONDUCTOR

2.90 (BSC.) 1 1.60 (BSC.) 2.80 (BSC.) 1 0.95 (BSC.) ŀ 0.40 ±0.10 10° ±5 R0.175 ±0.075 1.15 + 0.15 - 0.25R0.10 (MIN.) 0.15 ±0.07 0.25 (BSC.) 0.075 ±0.075 1.45 (MAX.) GAUGE PLANE 0.45 ±0.15 4° ±4° 1.90 (BSC.) 0.60 (REF.)

SOT-26

SUGGESTED PAD LAYOUT (Unit: Millimeters)

10° ±5



MARKING DIAGRAM

1	H	H	H	1		
	RSLYW					
#	ŧЪ	Η	Н	1		

- **RS** = Device code
- L = Lot Code A~Z
- Υ = Year Code
 - (F=2016, G=2017, H=2018, J=2019, K=2020)
- W = Week Code $A \sim Z = wk1 \sim wk26$
 - $\underline{A} \sim \underline{Z} = wk27 \sim wk52$



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