

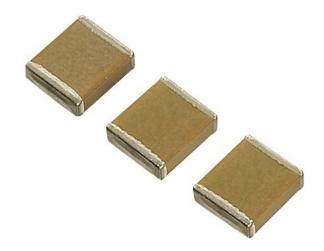
TS18H

FEATURES

- Small size
- Excellent Break down voltage, low DF
 Suit to re-flow soldering, wave soldering
- Suit to re-flow soldering, wave soldering, hand soldering

P P C \mathbf{T} N L A 0

TS18H SMD is widely used in Analog & Digital Modems, LAN/WAN Interface, Lighting Ballast Circuits, Voltage Multipliers, DC-DC Converter, Back-lighting Inverters.



OUTSIDE DIMENSION

	DIMENUOION			
Ty	vpe]	Dimension (inches)
British expression	Metric expression	Length(L±0.1)	Width(W \pm 0.1)	
0603	1608	0.06	0.03	W
0805	2012	0.08	0.05	
1206	3216	0.12	0.06	- 5
1210	3225	0.12	0. 10	3
1808	4520	0.18	0.08	2
1812	4532	0.18	0. 12	
2220	5750	0. 22	0. 20	
2225	5763	0. 22	0. 25	

Coefficient

Coefficient					
Parameter	NPO Temperature Wave		X7R Temperature Wave		
Temperature Coefficient	30 PPM -30 -55℃ -25 0℃ 25 50	75 100125℃	+15% 0% -15% -30%	C -25 0°C 25 50 75	/ 100125℃
Dissipation Factor	DF≤0.15%		DF≤2.50%		
Aging	None		≤2.5% decade hour		
Insulation Resistance	≥100GΩ		\geq 500 Ω F OR 50 G Ω		
	Rated Voltage Test Voltage			Time]
Dialactria Strangth	Ur=100V 2.5U			60±5S	
Dielectric Strength	200V≤Ur≤1000V 1.5Ur			$60\pm5S$	
	Ur>1000V 1			60±5S	



S P	E	1	I F	I	C	A	T	I	O	V	S
Item	Specifications						T	est Method			
	Class I	NPO: -55 ~ +125 °C									
Operating		X7R:	-55 ~ +125℃								
Temperature	Class II	X5R:	-55 ~ +85℃								
Range	Class II	Y5V:	-30 ~ +85℃								
		Z5U:	+10 ~ +85°C								
Tolerance	$\pm 5\%, \pm 10\% \pm 2$	20%									
Appearance	No visual defec	ts			Visual inspe	ction	<u>I</u>				
	Should be with		d be within the s	specified	Capacitance		Test Frequency	Test Volta	ge Tem	Temperature	
	Class I	tolera	tolerance		≤1000pF	1	MHz±10%	1.0±0.2Vrn			
Capacitance					>1000pF	1	KHz±10%	1.0±0.2 VIII	115		
	Class II	Should be within the specified		≤10μF	1	KHz±10%	1.0±0.2Vrn	ns 25±2℃			
			tolerance		>10µF		120±24Hz	0.5±0.1Vrn	ns	S	
		torerance		Z5U	1	.0±0.1KHz	0.5±0.05Vr	ms			
	Class I		≤0.15%								
Dissipation	Class II	X7R ≥50V		25V	16V		Test Method: The same as		3		
Factor		X5R	≤2.5%		≤.35%	≤3.5%		"Capacitance"			
(D.F.)		Y5V	≤7.0% (C<1.0μF)	≤12.5%	≤12	2.5%	Cupuchance			
		Z5U	≤9.0% (C≥1.0μ	F)					T.		
		C≤10	0nF, Ri≥50000	ΩΜΩ	Rated	Т	es Voltage	Duration	Charge/Discl	ı	
	Class I	C>10	OnF, Ri*C _R ≥5	00ΩF	Voltage				arge Current		
		X7R	C≤25nF, Ri	≽						Ten	nper
Insulation			10000ΜΩ		Ur<500V	Ur		60 ± 5 sec	≤50 mA		re:25
Resistance		X5R	C>25nF, Ri	$*C_R>$							2℃
(I.R.)	Class II	100ΩF								– Hun	nidit
			Y5V	C≤25nF, Ri	≽						y:<′
			4000ΜΩ		Ur<500V		V	60±5 sec	≤50 mA		
		Z5U	C>25nF, Ri	$*C_R>$							
			100ΩF								



S P	E C	I F	I C	A	T I	O	N S	
Item	S	Specifications	Test Method					
			Rated Voltage	Test Voltage	Dur	ration	Charge/Discha rge Current	
			Ur<200V	2.5Ur	1~	5sec.		
Dielectric Withstanding	No breakdown		200V≤Ur ≤ 1000V	1.5Ur	1~5sec.		≤50mA	
Voltage (D.W.V.)	or visual defects		Ur> 1000V	1.2Ur	1~	5sec.		
			Dielectric wi	ithstanding volt	age testing ma	y requires in	nmersion of the	
			capacitor in	a isolation fluid	at test voltage,	t test voltage over 2000Vdc.		
	Class I		Perform a heat temperature at 150+0/-10°C for 1hrs,then place room temp. for 24±2hrs.					
		NPO:0±30ppm/°C	According to the following sequence, measure the capacitance after					
Capacitance			temperature stabilize for 30min . ($\triangle C$ based on T3)					
Temperature			Step		Temperature (°C)			
Characteristic /		X7R:≤±15%	T1 25±2					
Coefficient	Class II	X5R:≪±15%	T2	T2 Low-category temp.				
		Y5V:+22%~-82%	Т3	25±2				
		Z5U:+22%~-56%	T4	High-category temp.			p.	
			T1		25±2			
	N 16 (>00	.0/ C 1 1 1 11	Preheating Conditions:80~120°C;10~30sec.					
Solderability	No defects, ≥90 be covered with	% of each terminal should	Solder Temperature: 245±5°C Immersing Speed:25±0.25mm/s					
	be covered with	iresn solder	Duration: 2±0.5sec.					
Adhesive			Applied Force:5N					
Strength of	Appearance: No	visible damage.	Duration:10±1sec.					
Termination			Speed:1mm/	sec	∠ _{Cap} — PCB			



S P	E C	I F I	С	A T	I	O N S		
	Annogrange	No crack or marked	Solder the capacit	tor on the		20 180		
Resistance	Appearance	defects should occur.	test jig,using a eu	Pressurize				
to Flexure Stresses	△C/C	Class I : ≤±5%	solder. Then apply in the direction.	a force		Flexure : ≤1		
Suesses	Δυ/υ	Class II: ≤±10%	Deflection:1mm Speed: 1mm/sec	2	Caps 45	adtance meter		
		No defects,≥90% of each						
	Appearance	terminal should be covered						
		with fresh solder						
		Class I: ≤±0.5% or						
	△C/C	±0.5pF	capacitor in a eutectic solder at 265±5°C for 5±1 seconds. Store at room temperature for 24±2 hours before measureing electric properties.					
Resistance		(whichever is larger)						
to Soldering		Class II:						
Heat		X7R X5R: -5~+10%						
		Y5V Z5U: -10∼+20%						
	D.F.	Meets Initial Values						
		(As Above)						
	I.R.	Meets Initial Values						
	1.K.	(As Above)						
	Appearance	No visual defect	Perform a heat temperature at 150+0/-10°C for 1hrs,then place room temp. for 24±2hrs.					
Temperature	△C/C	Class I: ≤±1% or ±1pF (whichever is larger) Class II:	Fix the capacitor to the supporting jig, Perform the five cycles according to the four heat treatments listed in the following table. Store at room temperature for 24±2 hours before measureing electric properties.					
Cycle		X7R X5R ≤±10%	Step	Temperatu	re (°C:)	Time (min.)		
Cycle		Z5U Y5V ≤±20%	1	Low-categ		30		
		Meets Initial Values	2	25±	• •	3		
	D.F.	(As Above)	3	High-categ		30		
	I.R.	Meets Initial Values (As Above)	4	25±		3		



S P	E C	I F I	\mathbf{C} A	ΓΙ	O N S			
	Appearance	No visual defect						
		Class I: ≤±2% or						
		±1pF						
	△C/C	(whichever is larger)						
	\(\triangle \triangle \tri	Class II:						
		X7R X5R ≤±10%						
Humidity		Z5U Y5V ≤±30%	Z5U Y5V $\leq \pm 30\%$ Let the capacitor sit at $40\pm 2^{\circ}$ and 90 to 95% humidity for 50					
Steady State	D.F.	≤Initial Values *2	hours.Remove and let sit for	r 48±2 hours at roo	om temperature before			
Steady State	<i>D</i> .1 .	(See Above)	measureing electric properti	es.				
		ClassI:Ri≥2500MΩor						
	I.R.	Ri*CR>25ΩF	i*CR>25ΩF (whichever is smaller)					
		(whichever is smaller)						
		ClassII:Ri≥1000MΩor						
		$Ri*CR>25\Omega F$						
		(whichever is smaller)						
	Appearance	No visual defect	Rated Voltage	Applied	Charge/Discharge			
			Raica Voltage	Voltage	Current			
		Class I: $\leq \pm 2\%$ or ± 1 pF	Ur<500V	2Ur				
		(whichever is larger)	500V≤Ur ≤ 1000V	1.5Ur	≤50mA			
	△C/C	Class II:	Ur>1000V	1.2Ur	Somma			
		X7R X5R ≤±20%	012 1000 V	1.201				
		Z5U Y5V ≤±30%						
Loading Life	D.F.	≤Initial Values *2						
		(See Above)						
		ClassI:Ri≥4000MΩor						
		Ri*CR>40ΩF						
	I.R.	(whichever is smaller)						
		ClassII:Ri≥2000MΩor						
		Ri*CR>50ΩF						
		(whichever is smaller)						



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Precautions on the use of MLCC:

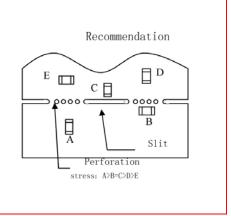
1.eneral Precautions On The Use Of MLCC:

The Multi-layer Ceramic Capacitors MLCC may fail when subjected to severe conditions of electrical environment and manchanical stress beyond the specified "rating" and specified condition in the specification. Following the precautions for satefy.

2. PCB Design

The amount of solder applied can affect the ability of chips to withstand mechanical stresses, which may lead to breaking or cracking. Therefore, when designing land-patterns it is necessary to consider the appropriate size and configuration of the solder pads, which determines the amount of solder necessary to form the fillets.

When designing the position of solder pads and SMD capacitors, it should be carefully performed to minimize stress.SMD capacitors should be located to minimize any possible mechanical stresses from board warp or deflection.



3. Considerations For Automatic Placement

If the lower limit of the pick-up nozzle is low,too much force may be imposed on the capacitors, causing damage. To avoid this, the following points should be considered before lowering the pick-up nozzle: The lower limit of the pick-up nozzle should be adjusted to the surface level of the PC board after correcting for deflection of the board.

The pick-up pressure should be adjusted between 1 and 3 N static loads.

To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins of back-up should be used the under PC board.

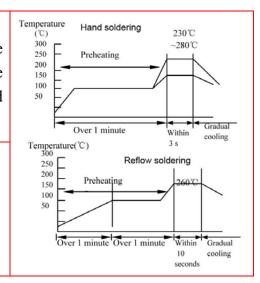


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4. Soldering

The ceramic section and metal section combine to the MLCC. As the poor heat conductivity of the ceramic section , ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling, especialy for large s

When hand soldering use a soldering iron with a maximum power of 25W and a maximum tip diameter of 1.0mm. The soldering iron should touch the capacitor directly.



5.Cleaning

The temperature difference between the components and cleaning process should not be greater than 100°C. In the case of ultrasonic cleaning, too much power output can cause excessive vibration of the PC board which may lead to the cracking of the capacitor or the soldered portion, or decrease the terminal electrodes' strength, thus the following condition

Ultrasonic output: Below20W/L Ultrasonic frequency: Below 40KHZ Ultrasonic washing period: 5min or less

6.Breakaway PC Boards

When splitting the PC board after mounting capacitors and other components, care is required so as not to give any stresses of twisting to board. 1. Be careful not to subject the capacitors to excessive mechanical shocks.

Board separation should not be done manually, but by using the appropriate devices.

7. Storage Conditions

To maintain the solderability of terminal electrodes and to keep the packaging material in good condition, recommended conditions as the following:

Temperature: $5-40^{\circ}C$; Humidity: 20-70% RH

Even though MLCC are stored in a good condition, the solderability of MLCC terminal electrodes will decrease as time goes by, so components should be used within 6 months from the time of delivery.



TS18H

Capacitance & Voltage

Size	Rated	Capacitance Range (pF)		Size	Rated	Capacitance Range (pF)		
	Voltage	NPO	X7R	Size	Voltage	NPO	X7R	
0.402	100V	1 ~ 470	100~22 000		100V	3.3~8 200	220~470 000	
0603	200V	1~330	100~8 200		200V	3.3~6 800	220~180 000	
	100V	1~1 000	100~56 000		500V	3.3~4 700	220~150 000	
0805	200V	1~820	100~27 000	1812	1000V	3.3~1 200	220~27 000	
	500V	1~560	100~12 000		2000V	3.3~390	220~12 000	
	100V	1.5~3 300	100~220 000		3000V	3.3~270	220~5 600	
	200V	1.5~2 200	100~120 000		4000V	3.3~220	220~1 500	
1206	500V	1.5~1 000	100~56 000		100V	10~12 000	470~1000 000	
Î	1000V	1.5~680	100~12 000	2225	200V	10~8 200	470~1000 000	
	1000V	1.5~10	100~5 600		500V	10~5 600	470~470 000	
	100V	2~5 600	150~330 000		1000V	10~2 700	470~68 000	
	200V	2~3 900	150~150 000		2000V	10~1 000	470~33 000	
1210	500V	2~2 200	150~100 000		3000V	10~680	470~4 700	
	1000V	2~820	150~15 000		4000V	10~560	470~3 900	
	2000V	2~470	150~8 200		100V	10~56 000	470~2200 000	
	100V	2~3 900	150~390 000		200V	10~47 000	470~2200 000	
	200V	2~3 000	150~180 000		500V	10~12 000	470~1000 000	
1000	500V	2~1 800	150~120 000	3035	1000V	10~10 000	470~390 000	
1808	1000V	2~820	150~22 000		2000V	10~5 600	470~270 000	
	3000V	2~150	150~2 700		3000V	10~3 900	470~8 200	
	4000V	2~100	150~1 000		4000V	10~560	470~3 000	

Note: Specification are subject to change without notice. For more detail and update, please visit our website.