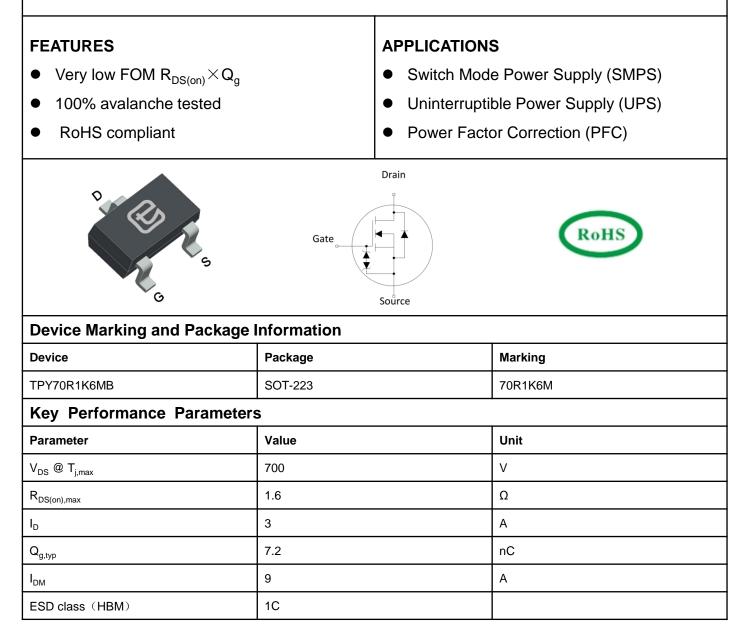


700V Super-Junction Power MOSFET

DESCRIPTION

700V super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The SJ MOSFET is a price-performance optimized product enabling to target cost sensitive applications in Consumer and Lighting markets, designed by Wuxi Unigroup Microelectronics Company.



Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted					
Parameter Drain-Source Voltage (V _{GS} = 0V)		Symbol	Value	Unit	
		V _{DSS}	700	V	
Continuous Drain Current	T _C = 25°C		3	A	
Continuous Drain Current	TC = 100°C	I I _D	1.8		
Pulsed Drain Current (note1)		I _{DM}	9	A	
Gate-Source Voltage		V _{GSS}	±20	V	
Single Pulse Avalanche Energy (note2)		E _{AS}	26	mJ	
Repetitive Avalanche Energy (note2)		E _{AR}	0.10	mJ	
Avalanche Current		I _{AR}	0.6	А	
MOSFET dv/dt ruggedness, V _{DS} = 0480V		dv/dt	50	V/ns	
Power Dissipation		P _D	6.2	W	
Continuous Body Diode Current		۱ _s	2.5	1	
Pulsed Diode Forward Current (note1)		I _{SM}	33		
Reverse diode dv/dt (note3)		dv/dt	15	V/ns	
Maximum diode commutation speed (note3)		di _f /dt	500	A/us	
Operating Junction and Storag	ge Temperature Range	T _J , T _{stg}	-55~+150	°C	

Thermal Resistance					
Parameter	Symbol	Value	Unit		
Thermal Resistance, Junction-to-Case	R _{thJC}	20	- ∘c/w		
Thermal Resistance, Junction-to-Ambient	R _{thJA}	160	°C/W		



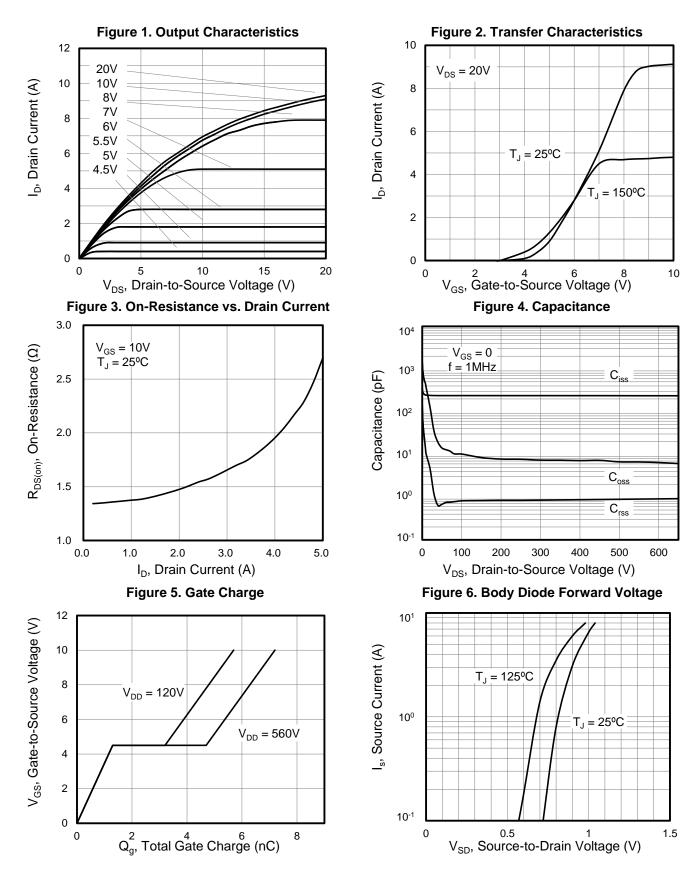
Deremeter		Test Canditions	Value			11-1-14	
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					-		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250 \mu A$	700			V	
Zara Cata Valtaga Drain Current		$V_{DS} = 700V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 700V, V_{GS} = 0V, T_{J} = 150^{\circ}C$			100		
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 20 V$			±1	μA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.5		4.5	V	
Drain-Source On-Resistance	R _{DS(on)}	$V_{GS} = 10V, I_D = 1A$		1.4	1.6	Ω	
Gate resistance	R _G	f = 1.0MHz open drain		44		Ω	
Dynamic				•			
Input Capacitance	C _{iss}			239		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		10			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		1			
Total Gate Charge	Qg			7.2		nC	
Gate-Source Charge	Q _{gs}	$V_{DD} = 560V, I_{D} = 3A, V_{GS} = 10V$		1.3			
Gate-Drain Charge	Q _{gd}			3.4			
Turn-on Delay Time	t _{d(on)}			57			
Turn-on Rise Time	t _r	V _{DD} = 400V, I _D = 3A,		63			
Turn-off Delay Time	t _{d(off)}	$R_{G} = 25\Omega$		111		ns	
Turn-off Fall Time	t _f			53			
Drain-Source Body Diode Characte	eristics						
Body Diode Voltage	V _{SD}	$T_{J} = 25^{\circ}C, I_{SD} = 1A, V_{GS} = 0V$		0.9	1.2	V	
Reverse Recovery Time	t _{rr}			200		ns	
Reverse Recovery Charge	Q _{rr}	V _R = 400V, I _F = 3A, di _F /dt = 100A/µs		0.9		μC	
Peak Reverse Recovery Current	I _{rrm}			8		А	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. $I_{AS} = 0.6A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^{\circ}C$
- 3. Identical low side and high side switch with identical ${\sf R}_{\sf G}$



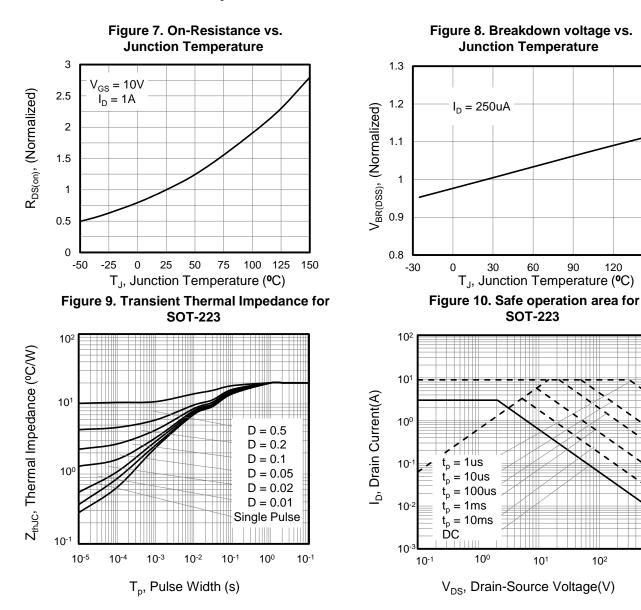
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



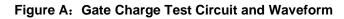
150

10³

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Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



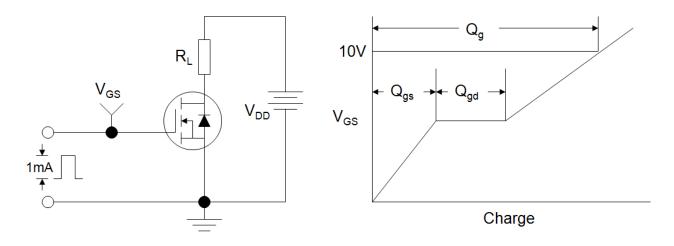


Figure B: Resistive Switching Test Circuit and Waveform

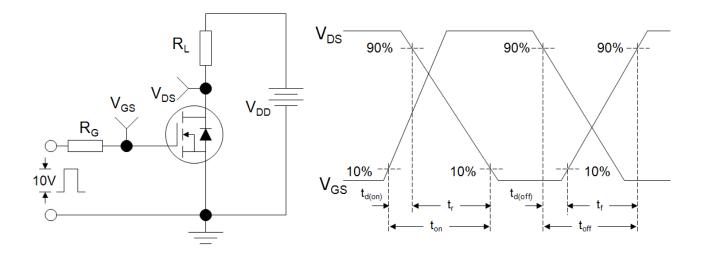
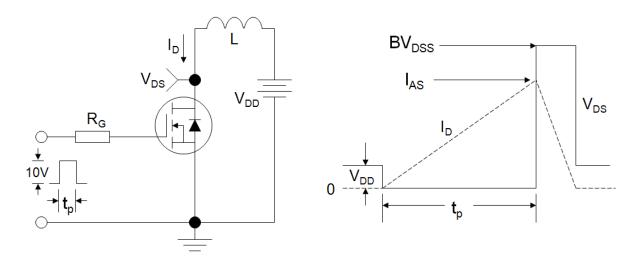
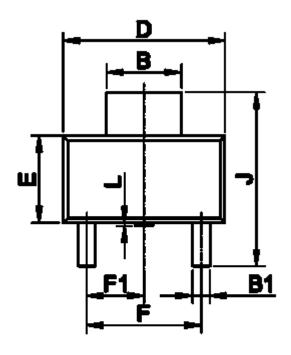


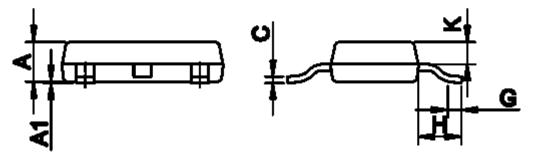
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





SOT-223





Unit:mm				
Symbol	Min.	Тур.	Max.	
А	1.50	1.60	1.80	
A1	0.01	0.06	0.10	
В	2.90	3.00	3.10	
B1	0.60	0.07	0.80	
С	0.22	0.254	0.32	
D	6.30	6.50	6.70	
E	3.30	3.50	3.70	

	Unit:mm					
	Symbol	Min.	Тур.	Max.		
1	F		4.60			
	F1		2.30			
	G	0.70	0.90	1.10		
	Н	1.50	1.75	2.00		
	J	6.70	7.00	7.30		
	к		0.90			
	L	0	0.10	0.20		



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