

Tripolar overvoltage protection for network interfaces

Features

- Triple crowbar protection
- Low capacitance
- Low holding current: I_H = 30 mA minimum
- Surge current:
 I_{PP} = 200 A, 2/10 μs
 I_{PP} = 30 A, 10/1000 μs

Benefits

- TrisilTM technology is not subject to ageing and provides a fail safe mode in short circuit for a better protection.
- This device can be used to help equipment to meet main standards such as UL1950, IEC 950 / CSA C22.2 and UL1459.
- Trisils have UL94 V0 approved resin.
- SO8 package is JEDEC registered.
- Trisils comply with the following standards GR-1089 Core, ITU-T-K20/K21, VDE0433, VDE0878, IEC 61000-4-2.

Applications

Dedicated to data line protection, this device provides a tripolar protection function. It ensures the same protection capability with the same breakdown voltage in both common and differential modes.

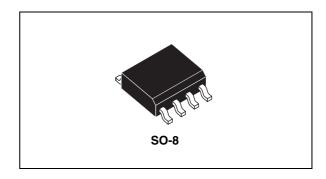
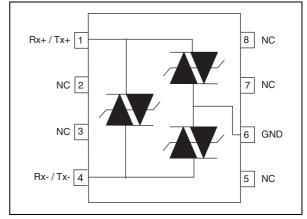


Figure 1. Schematic diagram



Description

The TPN is a low capacitance transient surge arrestor designed for protection of high debit rate communication networks. Its low capacitance avoids distorsion of the signal as it has been designed for T1/E1 and Ethernet networks.

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Characteristics TPN3021

1 Characteristics

Table 1. Compliant with the following standards

	Peak surge voltage (V)	Voltage waveform (µs)	Required peak current (A)	Current waveform (µs)	$\begin{array}{c} \text{Minimum} \\ \text{serial resistor} \\ \text{to meet} \\ \text{standard} \\ (\Omega) \end{array}$
GR-1089-CORE First level	2500 1000	2/10 10/1000	500 100	2/10 10/1000	7.5 25
GR-1089-CORE Intrabuilding	1500	2/10	100	2/10	0
ITU-T-K20/K21	1000	10/700	25	5/310	0
ITU-T-K20 (IEC 61000-4-2)	6000 8000	1/60 ns	ESD contac ESD air c	•	
VDE0433	4000 2000	1 10//00 1 1 5/310		5/310	40 0
VDE0878	4000 2000	1.2/50	100 50	1/20	0
IEC 61000-4-5	2000 2000	10/700 1.2/50	50 50	5/310 8/20	0

Table 2. Absolute ratings $(T_{amb} = 25 \, ^{\circ}C)$

Symbol	Parameter	Value	Unit	
І _{РР}	Peak pulse current: t_r / t_p	10/1000 8/20 10/560 5/310 10/160 1/20 2/10	30 100 40 50 75 100 200	А
1	Non repetitive surge peak on-state current One cycle	50 Hz 60 Hz	8 9	Α
ITSM	Non repetitive surge peak on-state current (F = 50Hz)	0.2 s 2 s	3 1.5	Α
T _{stg}	Storage temperature range		-55 to +150	°C
Tj	Operating junction temperature range		-40 to +150	°C
T _L	Maximum lead temperature for soldering during 10s	260	°C	

Table 3. Thermal resistances

Symbol	Parameter	Value	Unit
R _{th(j-a)}	Junction to ambient	170	°C/W

TPN3021 Characteristics

Table 4. Electrical characteristics - definitions ($T_{amb} = 25^{\circ} C$)

Symbol	Parameter		
V _{RM}	Stand-off voltage		
I _{RM}	Leakage current at stand-off voltage		
V _R	Continuos Reverse voltage		
V _{BR}	Breakdown voltage		
V _{BO}	Breakover voltage		
I _H	Holding current		
I _{BO}	Breakover current		
I _R	Continuos reverse voltage		
I _{PP}	Peak pulse current		
С	Capacitance		

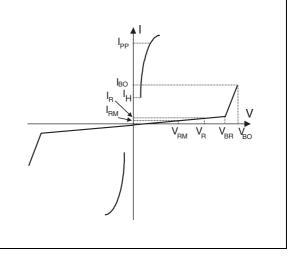


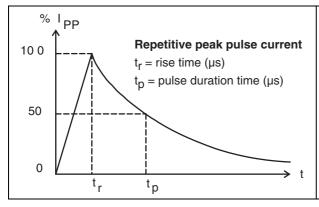
Table 5. Static parameters

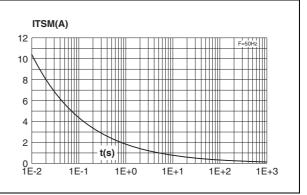
Order code	I _{RM} max. @ V _{RM}		V _{BO} ⁽¹⁾ max.@ I _{BO}		l _H ⁽²⁾ min.	C ⁽³⁾ typ.	
Order code	μΑ	V	V	mA	mA	pF	
TPN3021	4	28	38	300	30	16	

- 1. See Figure 6: Test circuit 1 for IBO and VBO parameters.
- 2. See Figure 7: Test circuit 2 for dynamic IH parameter
- 3. $V_R = 0 \text{ V bias}, V^{RMS} = 1 \text{ V}, F = 1 \text{ MHz}$

Figure 2. Pulse waveform

Figure 3. Non repetitive surge peak on-state current versus overload duration (T_i initial = 25 °C)

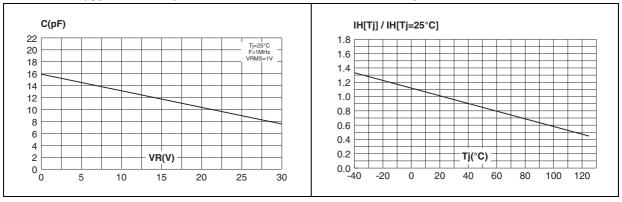




Test circuits TPN3021

Figure 4. Variation of junction capacitance versus reverse voltage applied (typical values)

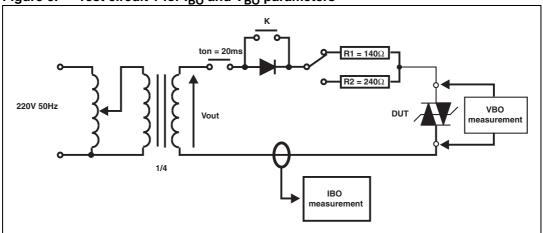
Figure 5. Relative variation of holding current versus junction temperature



2 Test circuits

2.1 Test procedure for test circuit 1

Figure 6. Test circuit 1 for I_{BO} and V_{BO} parameters



Pulse test duration ($t_p = 20 \text{ ms}$):

- For bidirectional devices = switch K is closed
- For unidirectional devices = switch K is open

V_{OUT} selection:

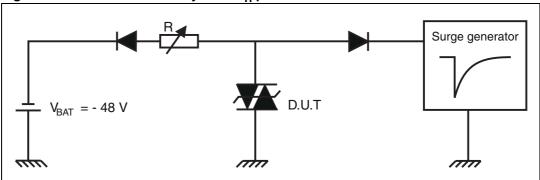
Device with V_{BO} < 200 V, V_{OUT} = 250 V_{RMS} , R1 = 140 Ω

Device with $V_{BO} \geq$ 200 V, V_{OUT} = 480 $V_{RMS},~R2$ = 240 Ω

TPN3021 Test circuits

2.2 Test procedure for test circuit 2

Figure 7. Test circuit 2 for dynamic I_H parameter

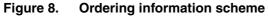


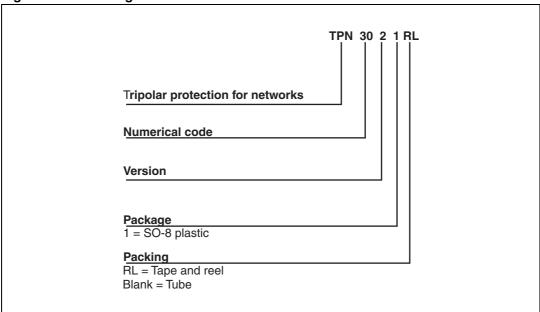
This is a go no-go test, which can confirm the holding current (I_H) level.

Procedure

- 1. Adjust the current level at the $I_{\mbox{\scriptsize H}}$ value by short circuiting the AK of the D.U.T.
- 2. Fire the D.U.T. with a surge current $I_{PP} = 10A$, $10/1000\mu s$.
- 3. The D.U.T. will come back off-state within 50 ms maximum.

3 Ordering information scheme





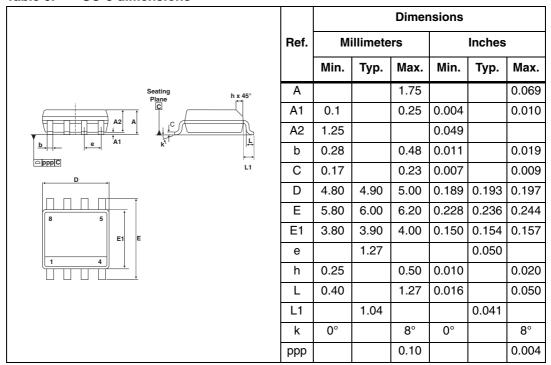
TPN3021 Package information

4 Package information

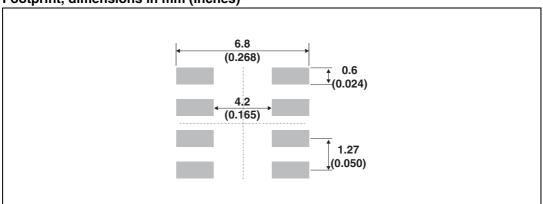
- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 6. SO-8 dimensions







Ordering information TPN3021

5 Ordering information

Table 7. Ordering information

Ordering code	Marking	Package	Weight	Base qty	Delivery mode
TPN3021	TPN302	SO-8	0.08g	100	Tube
TPN3021RL ⁽¹⁾	TPN302	30-6		2500	Tape and reel

^{1.} Preferred device

6 Revision history

Table 8. Document revision history

Date	Revision	Changes	
Sep-2001	3	Previous release	
07-Feb-2006	4	Reformatted to current template. Maximum junction temperature parameter replaced by Operating junction temperature range in Table 3. Added footnote 1 to Ordering information table.	
25-Jun-2010	5	Updated trademark statement.	

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