TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS IC

# TPD1037BS

Low-Side Switch for Motor, Solenoid and Lamp Drive

TPD1037BS is a monolithic power IC for low-side switch. The IC has a vertical MOSFET output which can be directly driven from a CMOS or TTL logic circuit (e.g., an MPU). The IC offers intelligent self-protection functions.

## Features

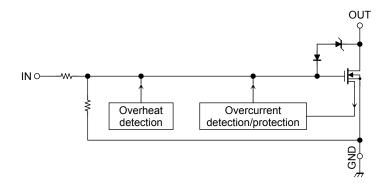
- A monolithic power IC with a new structure combining a control block and a vertical power MOSFET ( $\pi$ -MOS) on a single chip.
- Can directly drive a power load from a CMOS or TTL logic.
- Built-in protection circuits against overvoltage, overheat, and overcurrent.
- Low ON-resistance. RDS (ON) =  $0.25 \Omega$  (max) (@VIN = 5 V, T<sub>j</sub> =  $25^{\circ}$ C)
- Package TO-92 (MOD) can be packed in tape.

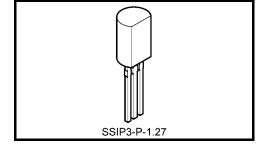
## **Pin Assignment**



Note: That because of its MOS structure, this product is sensitive to static electricity.

## **Block Diagram**



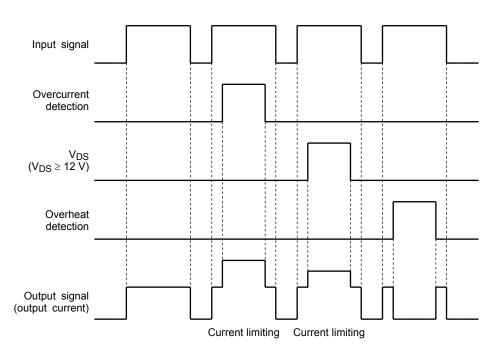


Weight: 0.36 g (typ.)

## **Pin Description**

Pin No.	Symbol	Pin Description
1	IN	Input pin. This pin is connected to a pull-down resistor internally, so that even when input wiring is open-circuited, output can never be turned on inadvertently.
2	OUT	Output pin. If an inrush current flows (e.g., from a lamp), the current is clamped at 10 A (typ.) by an overcurrent protective circuit. Also, a 150 $\mu$ s (typ.) mask circuit is included internally, so that if V <sub>DS</sub> $\geq$ 12 V (typ.) after this mask time, the current is clamped at 3 A (typ.)
3	GND	Ground pin.

## Timing Chart



## **Truth Table**

IN	V <sub>OUT</sub>	Mode		
L	Н	Normal		
Н	L	inolliai		
L	Н	Overcurrent		
Н	L	(during inrush)		
L	Н	Overcurrent		
Н	L	(shorted load)		
L	Н	Overheat		
H COM	Н	Overneat		

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage	V <sub>DS (DC)</sub>	40	V	
Output current	I <sub>D</sub>	1.5	А	
Input voltage	V <sub>IN</sub>	–0.5 to 6	V	
Power dissipation (Ta = $25^{\circ}$ C)	PD	0.9	W	
Energy tolerance	ES/B	200	mJ	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Junction temperature	Tj	150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## Electrical Characteristics ( $T_j = 25^{\circ}C$ )

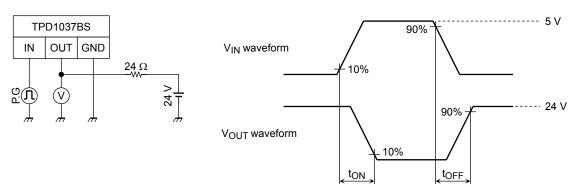
Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Drain-source breakdown voltage	V (BR) DSS	_	V <sub>IN</sub> = 0 V, I <sub>D</sub> = 10 mA	40	_	_	V
High level input voltage	VIH	—	$V_{DS} = 10$ to 40 V, $I_D = 1$ A	3.5	5	6	V
Low level input voltage	V <sub>IL</sub>	—	$V_{DS} = 10$ to 40 V, $I_D = 10 \ \mu A$	_	_	0.8	V
Current at output OFF	I <sub>DSS (1)</sub>	4 <u> </u>	$V_{IN} = 0 V, V_{DS} = 40 V$	_	_	100	μA
Current at output OFF	I <sub>DSS (2)</sub>		V <sub>IN</sub> = 0 V, V <sub>DS</sub> = 24 V	_	_	10	
Input current	I <sub>IN</sub>	—	$V_{IN} = 5 V$ , at normal operation	_	_	300	μA
ON-resistance	R <sub>DS (ON)</sub>	—	V <sub>IN</sub> = 5 V, I <sub>D</sub> = 1 A	_	_	0.25	Ω
Overheat protection	Τ <sub>S</sub>	_	$V_{IN} = 5 V$		160	_	°C
0	I <sub>S (1)</sub>		$V_{DS} = 24 \text{ V}, V_{IN} = 5 \text{ V}, \text{ during inrush}$		10	_	A
Overcurrent protection	I <sub>S (2)</sub>		$V_{DS} = 24 \text{ V}, V_{IN} = 5 \text{ V},$ when shorted load	_	3	_	
Shorted load detection voltage	V <sub>DS</sub>	—	when shorted load	_	12	_	V
Cuitobing time	ton		$V_{DS} = 24$ V, $V_{IN} = 5$ V, $R_L = 24$ $\Omega$		70	_	μS
Switching time	tOFF	1			120	_	
Diode forward voltage between drain and source	V <sub>DSF</sub>	_	I <sub>F</sub> = 1.5 A		0.9	1.8	V

## **Test Circuit 1**

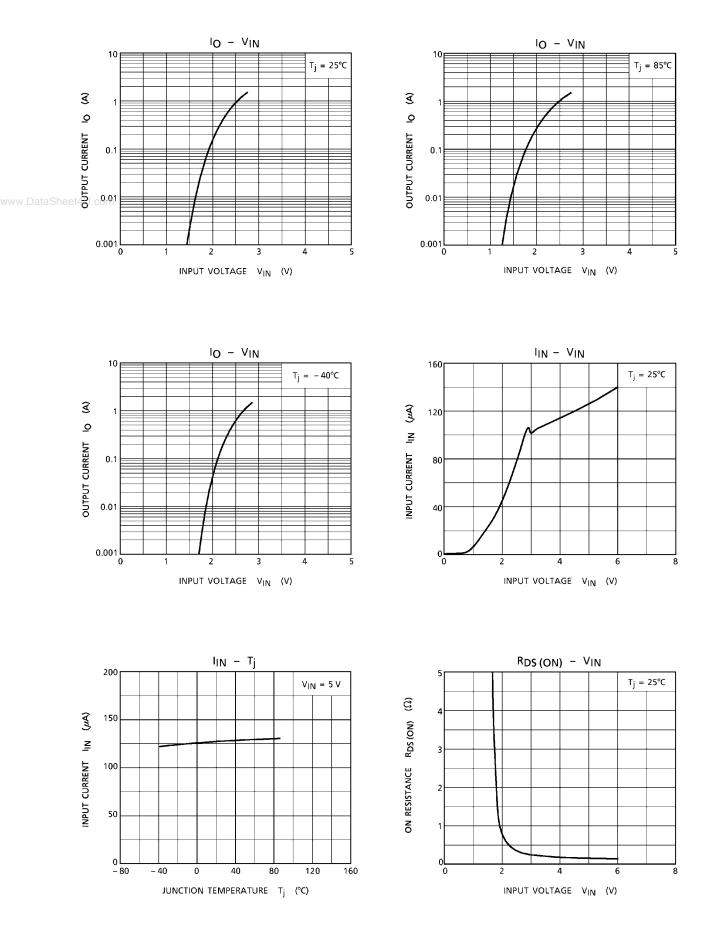
## Switching Time Measuring Circuit

**Test Circuit** 

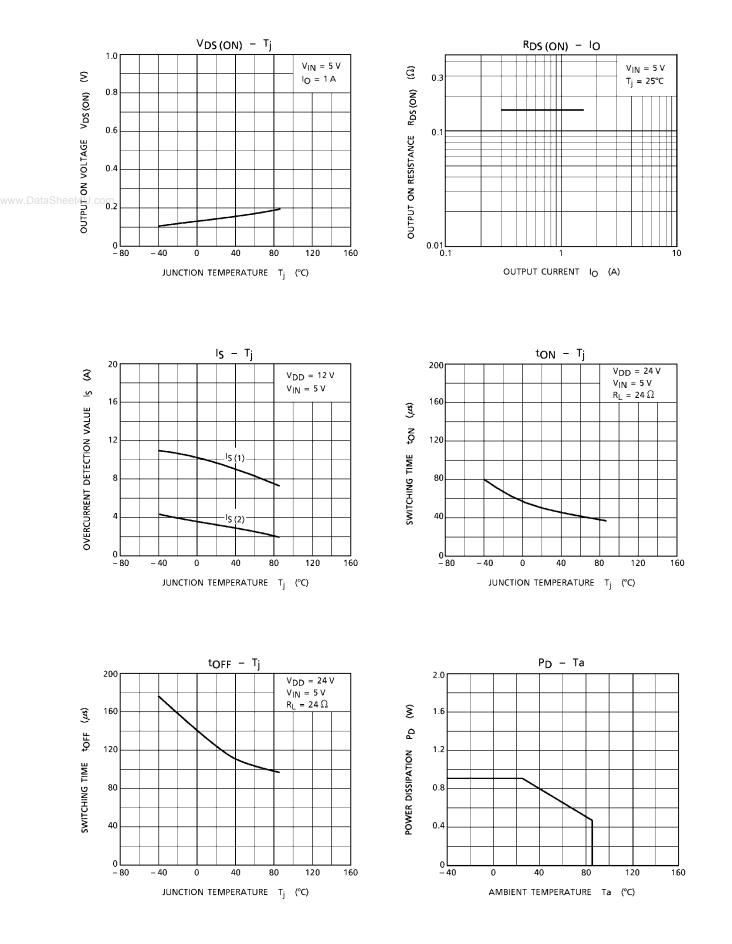
#### **Measured Waveforms**



# **TOSHIBA**

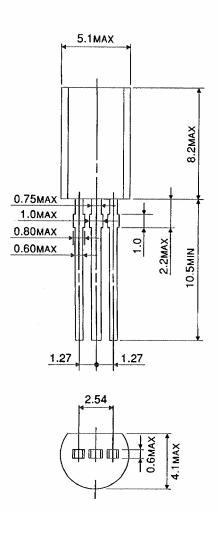


# TOSHIBA



## **Package Dimensions**

SSIP3-P-1.27



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Weight: 0.36 g (typ.)

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### **RESTRICTIONS ON PRODUCT USE**

20070701-EN

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in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such
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In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.

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