TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

TPCP8601

High-Speed Switching Applications DC-DC Converter Applications Strobo Flash Applications

- High DC current gain: $h_{FE} = 200 \text{ to } 500 \text{ (IC} = -0.6 \text{ A)}$
- Low collector-emitter saturation: V_{CE} (sat) = -0.19 V (max)
- High-speed switching: $t_f = 35$ ns (typ.)

Absolute Maximum Ratings (Ta = 25°C)

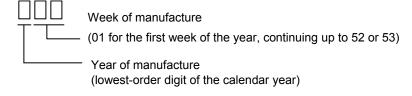
Characteristic		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	-20	V	
Collector-emitter voltage		V _{CEO}	-20	V	
Emitter-base voltage		V_{EBO}	-7	V	
Collector current	DC (Note 1)	IC	-4.0	Α	
	Pulse (Note 1)	I _{CP}	-7.0	^	
Base current	Base current		-0.5	Α	
Collector power dissipation (t = 10s)	t = 10s	Do (Noto 2)	3.3	W	
	DC	Pc (Note 2)	1.3		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

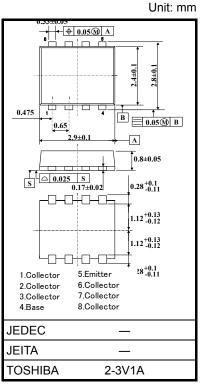
Note 1: Ensure that the junction temperature does not exceed 150°C during use of this device.

Note 2: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

Note 3: ● on the lower left of the marking indicates Pin 1.

* Weekly code (three digits):





Weight: 0.017 g (typ.)

Figure 1.
Circuit Configuration
(Top View)

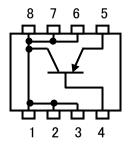
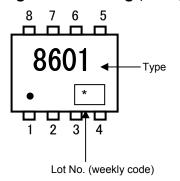


Figure 2. Marking (Note 3)

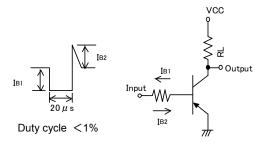


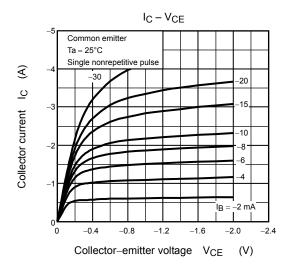
Start of commercial production 2004-06

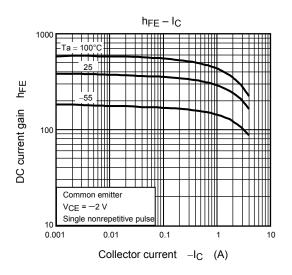
Electrical Characteristics (Ta = 25°C)

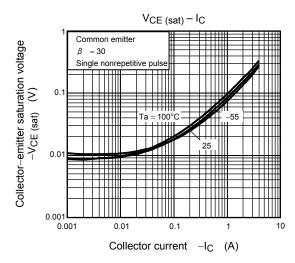
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	_	_	-100	nA	
Emitter cut-off curre	ent	I _{EBO}	$V_{EB} = -7 \text{ V}, I_{C} = 0$	_	_	-100	nA	
Collector-base brea	kdown voltage	V (BR) CBO	$I_C = -1 \text{ mA}, I_B = 0$	-20	_	_	٧	
Collector-emitter bro	eakdown voltage	V (BR) CEO	$I_C = -10 \text{ mA}, I_B = 0$	-20	_	_	٧	
DC ourrent gain		h _{FE} (1)	$V_{CE} = -2 \text{ V}, I_{C} = -0.6 \text{ A}$	200	_	500		
DC current gain	DC current gain		$V_{CE} = -2 \text{ V}, I_{C} = -2.0 \text{ A}$	100	_	_		
Collector-emitter sa	turation voltage	V _{CE} (sat)	$I_C = -2 \text{ A}, I_B = -67 \text{ mA}$	_	_	-0.19	-0.19 V	
Base-emitter satura	tion voltage	V _{BE (sat)}	$I_C = -2 \text{ A}, I_B = -67 \text{ mA}$	_	_	-1.1	V	
Switching time	Rise time	t _r	See Figure 3 circuit diagram $V_{CC} \simeq 12 \text{ V}, R_L = 6 \Omega$ $I_{B1} = -I_{B2} = -67 \text{ mA}$	_	72	_		
	Storage time	t _{stg}		_	170	_	ns	
	Fall time	t _f		_	35	_		

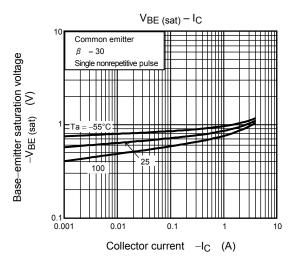
Figure 3. Switching Time Test Circuit & Timing Chart

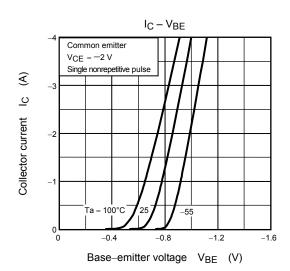




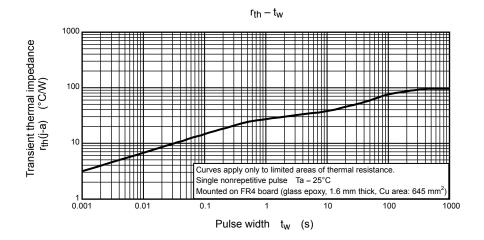


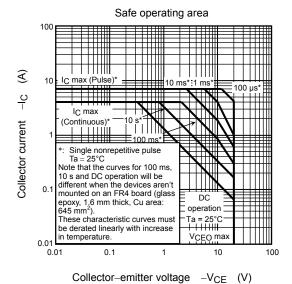






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TOSHIBA Transistor Silicon NPN Epitaxial Type

Unit: mm

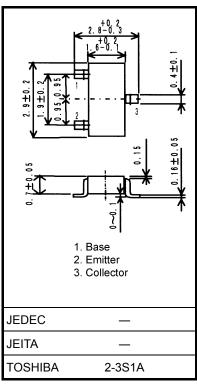
2SC5755

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: $h_{FE} = 400$ to 1000 ($I_{C} = 0.2$ A)
- Low collector-emitter saturation voltage: $V_{CE (sat)} = 0.12 \text{ V (max)}$
- High-speed switching: $t_f = 25$ ns (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V_{CBO}	20	V	
Collector-emitter voltage		V _{CEO}	10	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	Ic	2	Α	
	Pulse	I _{CP}	3.5	^	
Base current		lΒ	200	mA	
Collector power dissipation	DC	PC	500	mW	
	t = 10 s	(Note 1)	750	11100	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 0.01 g (typ.)

- Note 1: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)
- Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$	_	_	100	nA
Emitter cut-off current		I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	100	nA
Collector-emitter breakdo	wn voltage	V (BR) CEO	$I_C = 10 \text{ mA}, I_B = 0$	10	_	_	V
DC ourrent sein		h _{FE} (1)	V _{CE} = 2 V, I _C = 0.2 A	400	_	1000	
Do current gain	DC current gain		V _{CE} = 2 V, I _C = 0.6 A	200	_	_	
Collector-emitter saturation	n voltage	V _{CE (sat)}	I _C = 0.6 A, I _B = 12 mA	_	_	0.12	V
Base-emitter saturation vo	oltage	V _{BE (sat)}	I _C = 0.6 A, I _B = 12 mA	_	_	1.10	V
Switching time	Rise time	t _r	See Figure 1.	_	60	_	
	Storage time	t _{stg}	$V_{CC} \approx 6 \text{ V}, R_L = 10 \Omega$	_	215	_	ns
	Fall time	t _f	$I_{B1} = -I_{B2} = 12 \text{ mA}$	_	25	_	

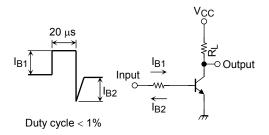
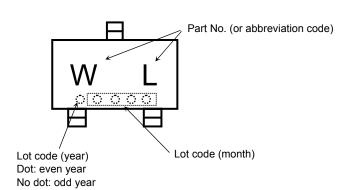
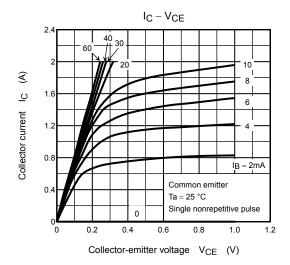
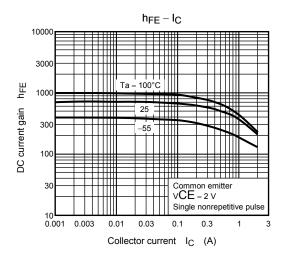


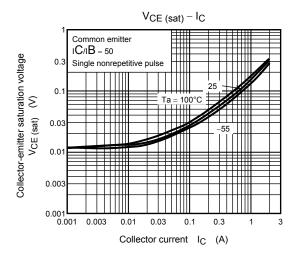
Figure 1 Switching Time Test Circuit & Timing Chart

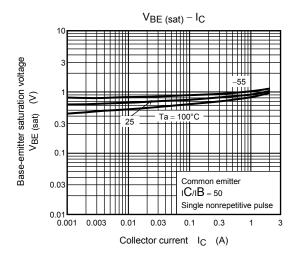


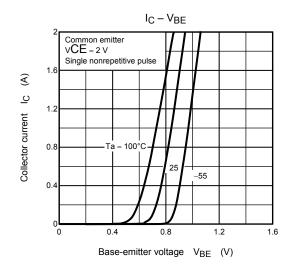
Marking

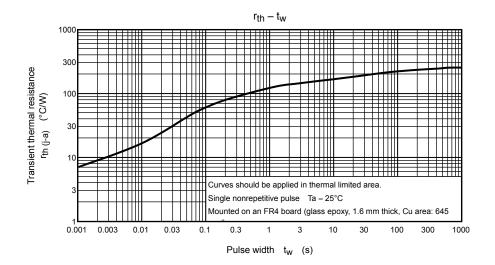


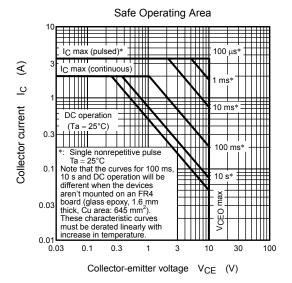












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