TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (Ultra High speed U-MOSIII)

ТРСР8103-Н

High Efficiency DC / DC Converter Applications Notebook PC Applications Portable Equipment Applications CCFL Inverter Applications

- Small footprint due to a small and thin package
- High speed switching
- Small gate charge: QSW = 6.5 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = $31 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 10 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = -40V)$
- Enhancement mode: $V_{th} = -0.8$ to -2.0 V ($V_{DS} = -10$ V, $I_D = -1$ mA)

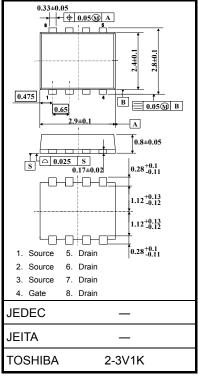
Absolute Maximum Ratings (Ta = 25°C)

Characte	eristic	Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	-40	V
Drain-gate voltage (F	R _{GS} = 20 kΩ)	V _{DGR}	-40	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	۱ _D	-4.8	А
Drain current	Pulsed (Note 1)	I _{DP}	-19.2	~
Drain power dissipati	on (t = 5 s) (Note 2a)	PD	1.68	W
Drain power dissipati	on (t = 5 s) (Note 2b)	PD	0.84	W
Single-pulse avalanc	he energy (Note 3)	E _{AS}	10.7	mJ
Avalanche current		I _{AR}	-4.8	A
Repetitive avalanche	energy Γc=25°C) (Note 4)	E _{AR}	0.09	mJ
Channel temperature	•	T _{ch}	150	°C
Storage temperature	range	T _{stg}	–55 to 150	°C

Note: For Notes 1 to 4, refer to the next page.

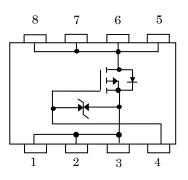
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

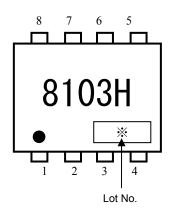
This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.017 g (typ.)

Circuit Configuration





Unit: mm

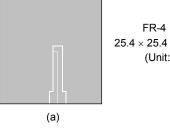
TOSHIBA

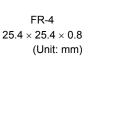
Thermal Characteristics

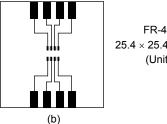
Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2a)	R _{th (ch-a)}	74.4	°C/W	
Thermal resistance, channel to ambient $(t = 5 s)$ (Note 2b)	R _{th (ch-a)}	148.8	°C/W	

Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)







(b) Device mounted on a glass-epoxy board (b)

FR-4 $25.4\times25.4\times0.8$ (Unit: mm)

- Note 3: V_{DD} = -24 V, T_{ch} = 25°C (initial), L = 0.5 mH, R_G = 25 Ω , I_{AR} = -4.8A
- Note 4: Repetitive rating: pulse width limited by max channel temperature
- Note 5: * Weekly code: (Three digits)



Week of manufacture

(01 for first week of the year, continuing up to 52 or 53)

Year of manufacture

(The last digit of the calendar year)

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Electrical Characteristics (Ta = 25°C)

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Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 16~V,~V_{DS}=0~V$	_	—	±10	μA
Drain cutoff curre	nt	I _{DSS}	V_{DS} = -40 V, V_{GS} = 0 V		_	-10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_{D} = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-40	_	_	v
		V (BR) DSX	$I_D = -10$ mA, $V_{GS} = -20$ V	-20	_	—	
Gate threshold vo	bltage	V _{th}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain-source ON-resistance		Ppg (QNI)	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -2.4 \text{ A}$		42	54	mΩ
		R _{DS (ON)}	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -2.4 \text{ A}$	_	31	40	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -2.4 \text{ A}$	5	10	_	S
Input capacitance		C _{iss}		_	800	_	pF
Reverse transfer capacitance		C _{rss}	V_{DS} = -10 V, V_{GS} = 0 V, f = 1 MHz	_	115	_	
Output capacitance		C _{oss}		_	165	_	
	Rise time	tr	V _{GS} -10 V C C C C C C C C C C C C C C C C C C C	_	6.5	_	- ns
	Turn-on time	t _{on}		_	12.5	_	
Switching time	Fall time	t _f		_	9		
	Turn-off time	t _{off}	Duty ≦ 1%, t _w = 10 μs	_	37		
Total gate charge (gate-source plus gate-drain)		Qg	$\label{eq:VDD} \begin{array}{l} V_{DD}\simeq -32 \mbox{ V}, V_{GS}=-10 \mbox{ V}, \\ I_D=-4.8 \mbox{ A} \end{array}$		19	_	
			$\label{eq:VDD} \begin{array}{l} V_{DD}\simeq -32 \mbox{ V}, \mbox{ V}_{GS}=-5 \mbox{ V}, \\ I_D=-4.8 \mbox{ A} \end{array}$	_	11	_	nC
Gate-source charge 1		Q _{gs1}	V _{DD} ≃ -32 V, V _{GS} = -10 V, I _D = -4.8 A		1.5	_	-
Gate-drain ("Miller") charge		Q _{gd}			5.5		
Gate switch charge		Q _{SW}		_	6.5	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	—	_	_	-19.2	А
Forward voltage (diode)			V _{DSF}	$I_{DR} = -4.8 \text{ A}, V_{GS} = 0 \text{ V}$			1.2	V

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