TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

TPC8107

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

- · Small footprint due to small and thin package
- Low drain-source ON resistance: $RDS(ON) = 5.5 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 31 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement-mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

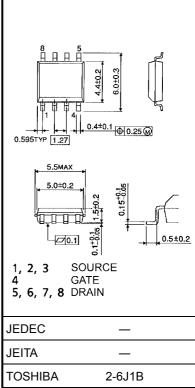
Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit
Drain-source voltage	ource voltage		-30	V
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	-30	V
Gate-source voltage		V_{GSS}	±20	٧
Drain current	DC (Note 1)	I _D	-13	А
Diam current	Pulse (Note 1)	I_{DP}	-52	^
Drain power dissipati	on $(t = 10 s)$ (Note 2a)	P_{D}	1.9	W
Drain power dissipati	on (t = 10 s) (Note 2b)	P_{D}	1.0	W
Single pulse avalanc	he energy (Note 3)	E _{AS}	219	mJ
Avalanche current		I _{AR}	-13	Α
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.19	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature	range	T _{stg}	-55 to 150	°C

Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

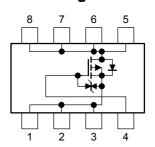
This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm



Weight: 0.080 g (typ.)

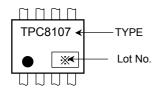
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	65.8	°C/W	
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R _{th (ch-a)}	125	°C/W	

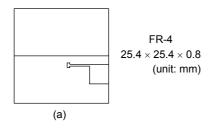
Marking (Note 5)

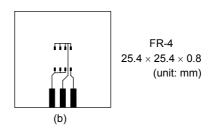


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = -24~V,~T_{ch} = 25^{\circ}C$ (initial), L = 1.0 mH, R_G = 25 $\Omega,~I_{AR} = -13~A$

Note 4: Repetitive rating; pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

Weekly code: (Three digits)
 Week of manufacture

 (01 for first week of year, continues up to 52 or 53)

 Year of manufacture

 (One low-order digits of calendar year)

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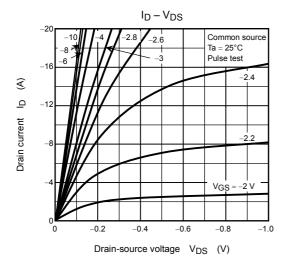
Electrical Characteristics (Ta = 25°C)

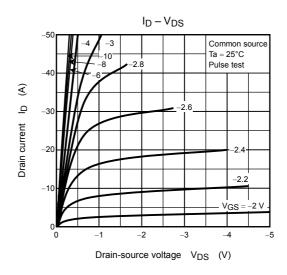
Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	rrent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source brea	akdowa voltago	V _{(BR) DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
Diani-source brea	akdown voltage	V _{(BR) DSX}	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	v
Gate threshold vo	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	10 15 5.5 7.0		V
·		_	$V_{GS} = -4 \text{ V}, I_D = -6.5 \text{ A}$	_	10	15	- mΩ
Dialii-source ON	resistance	KDS (ON)	$V_{GS} = -10 \text{ V}, I_D = -6.5 \text{ A}$	= 0 V	7.0		
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -6.5 \text{ A}$	15.5	31	_	S
Input capacitance)	C _{iss}		_	5880	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	1000	_	pF
Output capacitance		Coss		_	1050	_	
	Rise time	t _r	$V_{GS} \stackrel{0}{\underset{-10}{\text{V}}} \stackrel{V}{\underset{-10}{\text{V}}} \stackrel{V}$	_	11	_	
Switching time	Turn-ON time	t _{on}		±10 -102.0 15 7.0	_		
	_	110	_	ns			
	Turn-OFF time	t _{off}		_	395	_	
Gate-source charge 1		Q _{gs1}	$I_D = -13 \text{ A}$		10	_	nC
Gate-drain ("mille	r") charge	Q _{gd}			30	_	

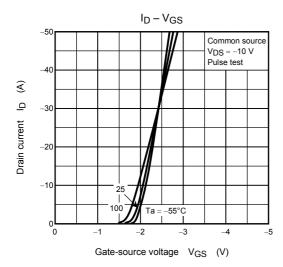
Source-Drain Ratings and Characteristics (Ta = 25°C)

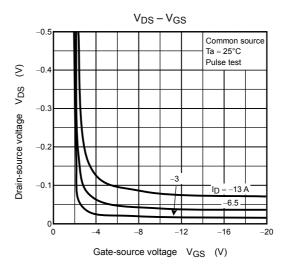
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	-52	Α
Forward voltage (diode)			V _{DSF}	$I_{DR} = -13 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

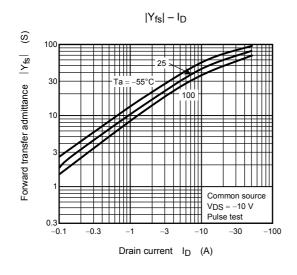
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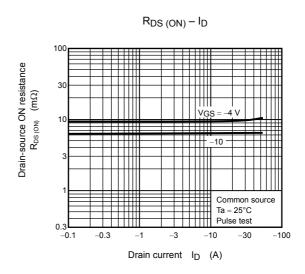


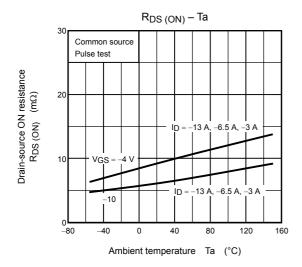


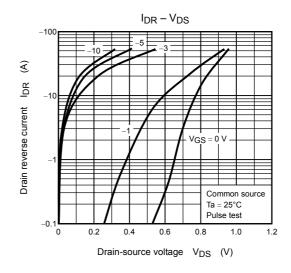


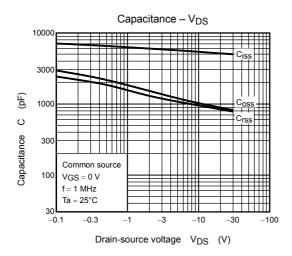


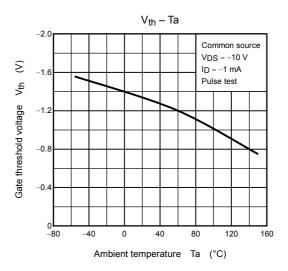


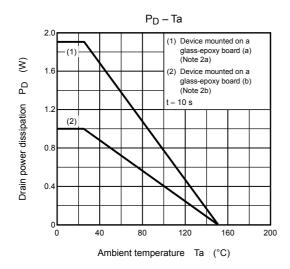


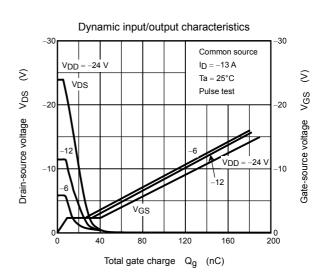




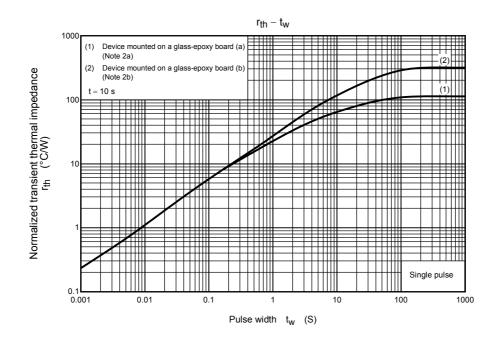


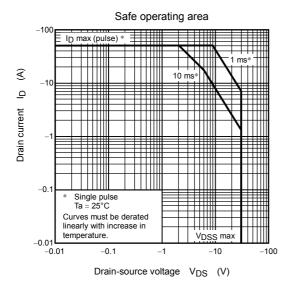






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