



## P-Channel Enhancement-Mode Vertical DMOS Power FETs Quad Array

### Ordering Information

BV <sub>DSS</sub> / BV <sub>DGS</sub>	R <sub>DS(ON)</sub> (max)	Order Number / Package	
		14-Pin P-Dip	14-Pin C-Dip*
-60V	3.5Ω	TP0606N6	TP0606N7

\*14-pin Side Brazed Ceramic Dip.

### Features

- ☐ 4 independent channels
- ☐ 4 electrically isolated die
- ☐ Commercial and Military versions available
- ☐ Freedom from secondary breakdown
- ☐ Low power drive requirement
- ☐ Low C<sub>ISS</sub> and fast switching speeds
- ☐ High input impedance and high gain

### Applications

- ☐ Motor control
- ☐ Convertors
- ☐ Amplifiers
- ☐ Switches
- ☐ Power supply circuits
- ☐ Driver (Relays, Hammers, Solenoids, Lamps, Memories, Displays, Bipolar Transistors, etc.)

### Thermal Characteristics

Package	Plastic DIP	Ceramic DIP
I <sub>D</sub> continuous & I <sub>DR</sub> (single die)	0.65A	0.75A
I <sub>D</sub> pulsed* & I <sub>DRM</sub> †	3.5A	3.5A
Power Dissipation @ T <sub>C</sub> = 25°C†	3W	4W
θ <sub>JA</sub> (°C/W)	83.3	62.5
θ <sub>JC</sub> (°C/W)	41.6	31.2

\* Pulse test 300 μS pulse, 2% duty cycle.

† Total for package.

### Advanced DMOS Technology

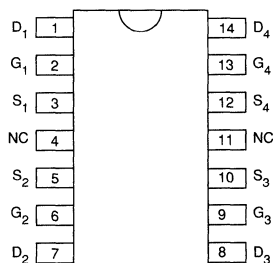
These enhancement-mode (normally-off) power transistors utilize a vertical DMOS structure and Supertex's well-proven silicon-gate manufacturing process. This combination produces devices with the power handling capabilities of bipolar transistors and with the high input impedance and negative temperature coefficient inherent in MOS devices. Characteristic of all MOS structures, these devices are free from thermal runaway and thermally-induced secondary breakdown.

Supertex Vertical DMOS Power FETs are ideally suited to a wide range of switching and amplifying applications where high break-down voltage, high input impedance, low input capacitance, and fast switching speeds are desired.

### Electrical Characteristics

Refer to TP06A Data Sheet for detailed characteristics.

### Pin Configuration



top view  
14-pin DIP