



General Description

The TNMNG100 is the high cell density trenched N-ch MOSFETs, which provides excellent R_{DSON} and efficiency for most of the small power switching and load switch applications.

The TNMNG100 meets the RoHS and Green Product requirement with full function reliability approved.

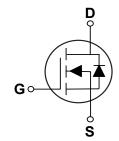
| BV _{DSS} | R _{DS(ON)} | Ι _D |
|-------------------|---------------------|----------------|
| 60 V | 100 mΩ | 2.3 A |

Features

- $R_{DS(ON)} \le 100 m\Omega @V_{GS} = 10V$
- · Green Device Available
- Super Low Gate Charge
- Excellent Cdv/dt Effect Decline
- · Advanced High Cell Density Trench Technology

SOT-23 Pin Configuration





Applications

- · Battery Protection
- · Load Switch
- Uninterruptible Power Supply

| Absolute Maximum Ratings T _c =25°C unless otherwise noted | | | | | | | |
|--|---|------------|-------|--|--|--|--|
| Symbol | Parameter | Rating | Units | | | | |
| V_{DS} | Drain-Source Voltage | 60 | V | | | | |
| V_{GS} | Gate-Source Voltage | ±20 | V | | | | |
| I _D | Drain Current - Continuous (T _A =25°C) | 2.3 | Α | | | | |
| I _{DM} | Drain Current - Pulsed | 9.2 | Α | | | | |
| P_D | Total Power Dissipation (NOTE 2) (T _A =25°C) | 1 | W | | | | |
| T_J | Operating Junction Temperature Range | -55 to 150 | °C | | | | |
| T _{STG} | Storage Temperature Range | -55 to 150 | °C | | | | |
| Marking Code | | A4 , 6003 | | | | | |

| Thermal Characteristics | | | | | |
|-------------------------|--|--------|------|--|--|
| Symbol | Parameter | Rating | Unit | | |
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient | 125 | °C/W | | |
| $R_{\theta JC}$ | Thermal Resistance Junction to Case | 80 | °C/W | | |





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|-------------------|--------------------------------|--|------|------|------|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V_{GS} =0V , I_D =250uA | 60 | | | V |
| I _{DSS} | Drain-Source Leakage Current | V_{DS} =48V , V_{GS} =0V , T_{J} =25°C | | | 1 | uA |
| I _{GSS} | Gate-Source Leakage Current | V_{GS} =±20V , V_{DS} =0V | | | ±100 | nA |

On Characteristics

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|---------------------|------------------------------------|--|------|------|------|------|
| R _{DS(ON)} | IStatic Drain-Source On-Resistance | V_{GS} =10V , I_D =2A | | | 100 | mΩ |
| | | V _{GS} =4.5V , I _D =1A | | | 110 | |
| $V_{GS(th)}$ | Gate Threshold Voltage | $V_{GS}=V_{DS}$, $I_D=250uA$ | 1.2 | | 2.5 | V |
| gfs | Forward Transconductance | V_{DS} =5V , I_{D} =2A | | 13 | | S |

Dynamic and switching Characteristics

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|--------------------|------------------------------|---|------|------|------|------|
| Q_g | Total Gate Charge | | | 5 | | |
| Q_gs | Gate-Source Charge | V_{DS} =48V , V_{GS} =4.5V , I_{D} =2A | | 1.68 | | nC |
| Q_{gd} | Gate-Drain Charge | | | 1.9 | | |
| T _{d(on)} | Turn-On Delay Time | | | 1.6 | | |
| T_r | Rise Time | V_{DD} =30V , V_{GS} =10V , R_{G} =3.3 Ω , I_{D} =2A | | 7.2 | | no |
| $T_{d(off)}$ | Turn-Off Delay Time | | | 25 | | ns |
| T _f | Fall Time | | | 14.4 | | |
| C _{iss} | Input Capacitance | V _{DS} =15V , V _{GS} =0V , F=1MHz | | 511 | | |
| C _{oss} | Output Capacitance | | | 38 | | pF |
| C _{rss} | Reverse Transfer Capacitance |] | | 25 | | |

Drain-Source Diode Characteristics and Ratings

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Unit |
|-----------------|---------------------------|---|------|------|------|------|
| Is | Continuous Source Current | V _G =V _D =0V,Force Current | | | 2.3 | А |
| I _{SM} | Pulsed Source Current | | | | 9.2 | Α |
| V_{SD} | Diode Forward Voltage | V_{GS} =0V , I_{S} =1A , T_{J} =25 $^{\circ}$ C | | | 1.2 | V |
| t _{rr} | Reverse Recovery Time | I _F =2A,dI/dt=100A/us,T _J =25°C | | 9.7 | | ns |
| Q_{rr} | Reverse Recovery Charge | | | 5.8 | | nC |

NOTES:

- 1. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 2. The power dissipation is limited by 150°C junction temperature.
- 3. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.





Characteristics Curves

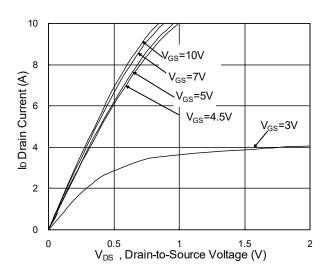


Fig.1 Typical Output Characteristics

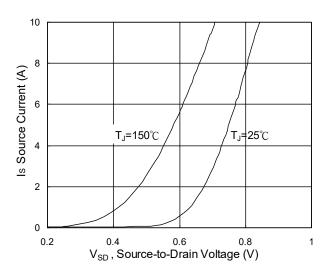


Fig.3 Source-Drain Diode Forward

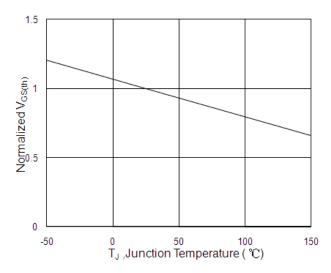


Fig.5 Normalized V_{GS(th)} v.s T_J

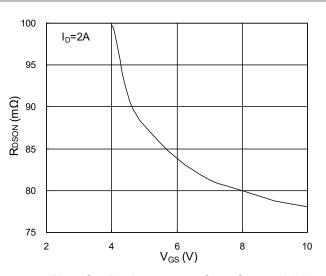


Fig.2 On-Resistance v.s Gate-Source Voltage

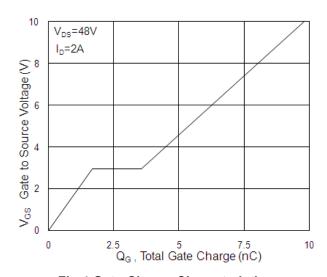


Fig.4 Gate-Charge Characteristics

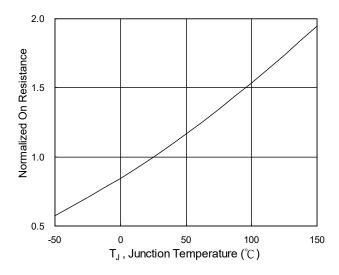
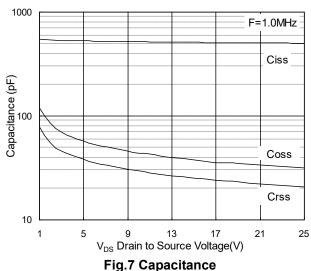


Fig.6 Normalized R_{DSON} v.s T_J





Characteristics Curves



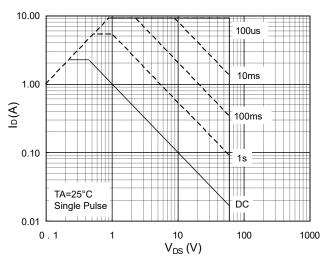


Fig.7 Capacitance

Fig.8 Safe Operating Area

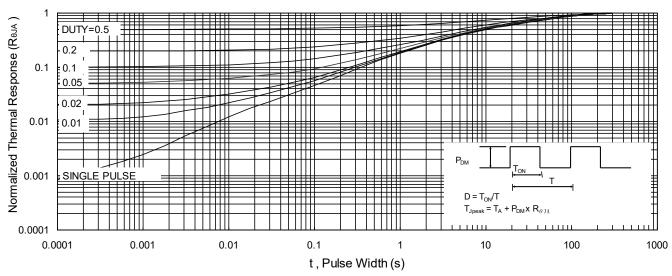
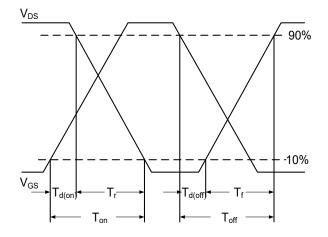


Fig.9 Normalized Maximum Transient Thermal Impedance



 V_{GS} 10V / 4.5V Gate Charge

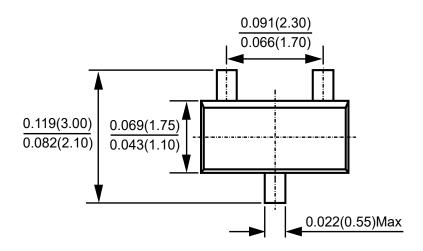
Fig.10 Switching Time Waveform

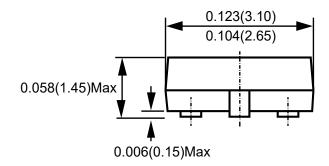
Fig.11 Gate Charge Waveform

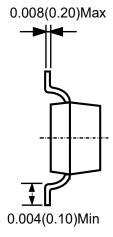




Package Outline Dimensions







SOT-23 Dimensions in inches and (millimeters)





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