

32bit	TLCS-900 Family TLCS-900/H1 Series		100pin
	[NEW]	TMP92FD28FG/DFG	

32-bit microcontroller with USB host controller

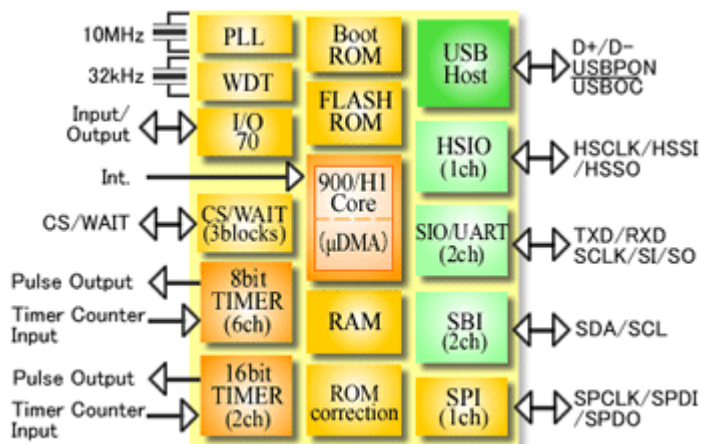
Features

● TLCS-900/H1 CPU Core

- Operating voltage:
3.0 to 3.6 V
- Minimum instruction execution time:
50 ns (at 20 MHz/ 3.0 to 3.6 V internal)
- Internal ROM: 512 Kbytes
- Internal RAM: 32 Kbytes

● Built-in Functions

- USB Host controller : 1 channel
- UART/SIO : 2 channels
- SBI : 2 channels
- HSIO : 1 channel
- SPI : 1 channel
- 8-bit timer : 6 channels
- 16-bit timer : 2 channels
- Key-on wake-up : 4 channels
- Program patch logic : 8 banks



● Product Lineup

** : Under development

Part number	ROM	RAM
TMP92CD28FG/DFG**	512 Kbytes	32 Kbytes

For further information about Toshiba microcomputers, please visit <http://www.semicon.toshiba.co.jp/eng/prd/micro/index.html>.

Development Systems

● Software Products

Toshiba Integrated Development Environment (TIDE)		Real-Time OS (μITRON 3.0)
C Compiler	Integrated Development Environment	
SW96CN0-ZCC: 1 license	SW96MN0-ZCC: 1 license	SW96RN2-ZCC: Object code can be freely copied
SW96CN3-ZCC: 10 licenses	SW96MN3-ZCC: 10 licenses	SW96RNC-ZCC: Object code can be freely copied. With source code.

● Hardware Products

** : Under development

Part number	Test tool	
	Controller	Emulation pod
TMP92FD28FG/DFG	BM1040R0A/BM1055R0B	BM92CD28F0A-M15**

As to hardware products, additional accessories are also needed.

For further information about Toshiba microcomputer development systems, please visit http://www.semicon.toshiba.co.jp/mctool/index_e.htm.

* This product uses the SuperFlash® technology under license of Silicon Storage Technology, Inc. Super Flash® is a registered trademark of Silicon Storage Technology, Inc.

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● Pin Assignments



Package Name DFG: P-QFP100-1420-0.65A