32bit	TLCS-900 Family TLCS-900/H1 Series	100pin
	Under Development **TMP92CD28FG/DFG**	

## 32-bit microcontroller with USB host controller

Features

● TLCS-900/H1 CPU Core

·Operating voltage:
  Internal = 1.4 to 1.6 V I/O = 3.0 to 3.6 V
·Minimum instruction execution time:
  50 ns (at 20 MHz internal)

● Built-in Functions

·USB Host controller	:	1 channel
·UART/SIO	:	2 channels
·SBI	:	2 channels
·HSIO	:	1 channel
·SPI	:	1 channel
·8-bit timer	:	6 channels
·16-bit timer	:	2 channels
·Key-on wake-up	:	4 channels
·Program patch logic	:	8 banks
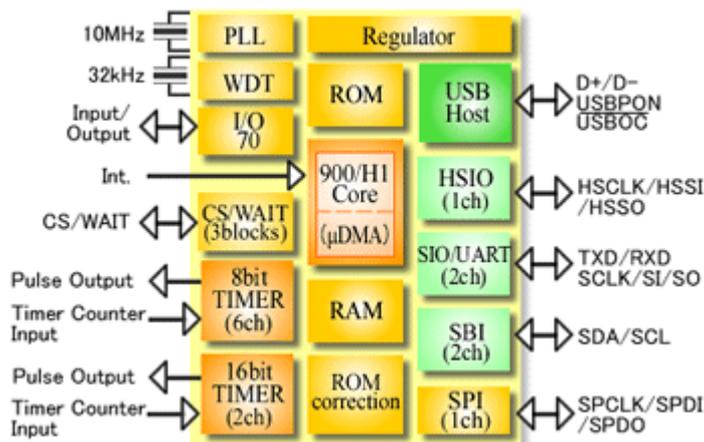
● Memory Size

*Products with flash memory are also available.
**:Under development

Part number	ROM	RAM
TMP92CD28FG**	512 Kbytes	32 Kbytes
TMP92CD28DFG**	512 Kbytes	32 Kbytes

› For further information about Toshiba microcomputers, please visit http://www.semicon.toshiba.co.jp/eng/prd/micro/index.html.

Development Systems

● Software Products

Toshiba Integrated Development Environment (TIDE)		Real-Time OS (μITRON 3.0)
C Compiler	Integrated Development Environment	
SW96CN0-ZCC: 1 license SW96CN3-ZCC: 10 licenses	SW96MN0-ZCC: 1 license SW96MN3-ZCC: 10 licenses	SW96RN2-ZCC: Object code can be freely copied SW96RNC-ZCC: Object code can be freely copied. With source code.

● Hardware Products

**:Under development

Part number	Test tool	
	Controller	Emulation pod
TMP92CD28FG**	BM1040R0A/BM1055R0B	BM92CD28F0A-M15**
TMP92CD28DFG**		

›As to hardware products, additional accessories are also needed.
› For further information about Toshiba microcomputer development systems, please visit http://www.semicon.toshiba.co.jp/mctool/index_e.htm.

● Pin Assignments

TMP92CD28FG

LQFP100

TOPVIEW

www.DataSheet4U.com
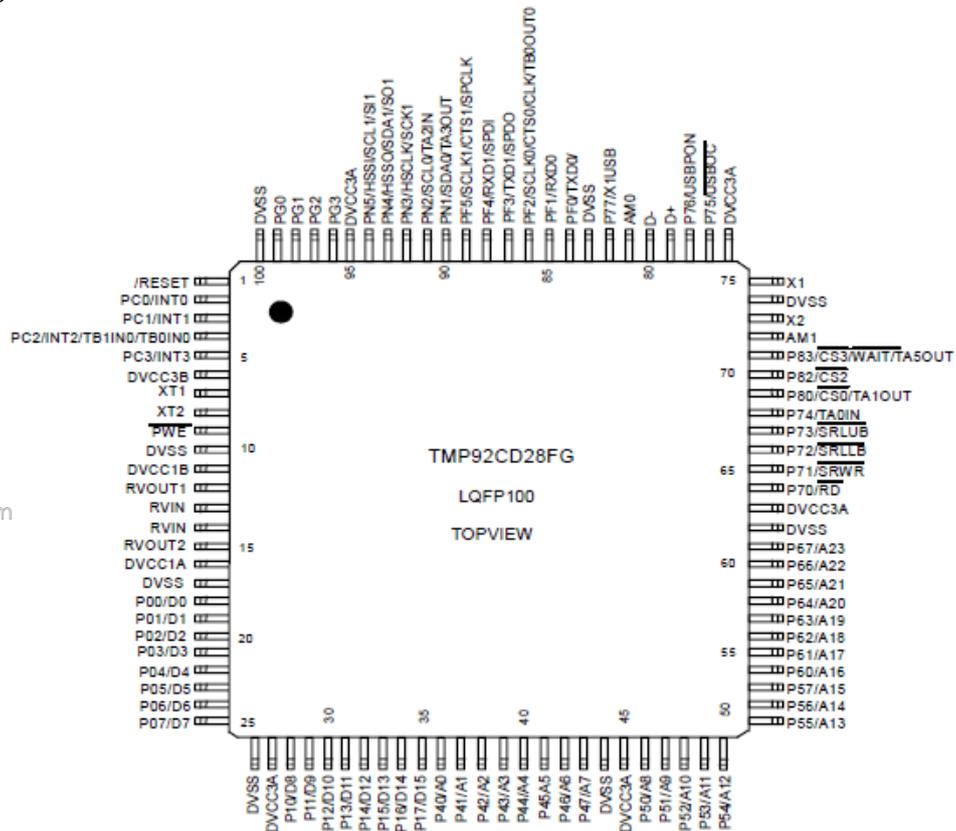
Package name FG:P-LQFP100-1414-0.50C

TMP92CD28DFG

QFP100

TOPVIEW
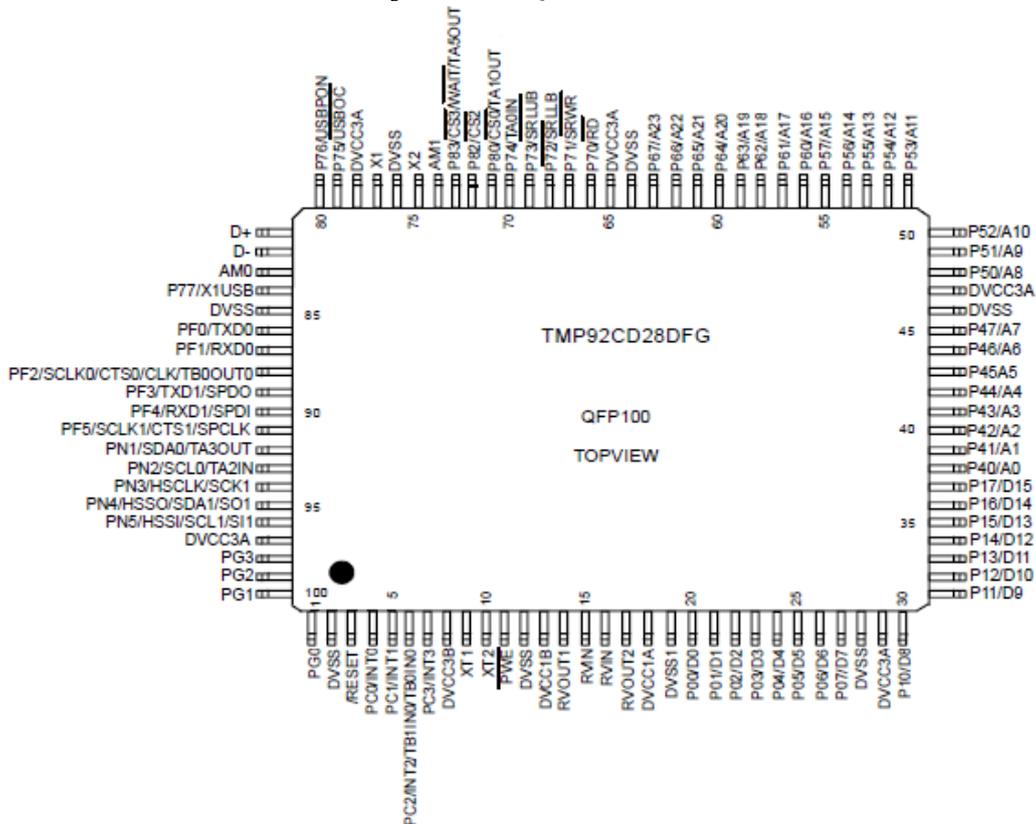
Package Name DFG: P-QFP100-1420-0.65Q