CMOS 4-Bit Microcontroller

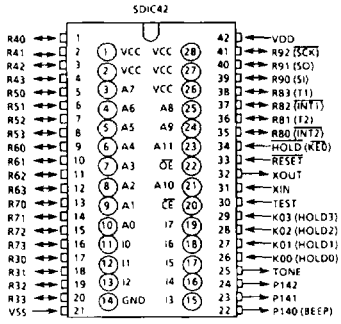
TMP47C952AE, TMP47C952AG

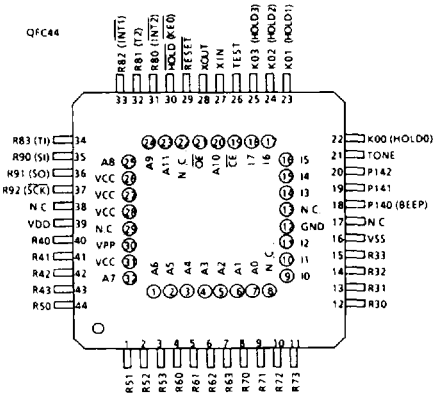
The 47C952A, which is equipped with an EPROM as program memory, is a piggyback type evaluator chip used for development and operational confirmation of the 47C452A application systems (programs).

The 47C952A is pin compatible with the 47C452A which are mask-programmed ROM devices.

Also, pin compatibility with the 47C451A is possible by using the 42- to 30-pin conversion adapter socket (BM1104).

Pin Assignment (Top View)





Pin Function (Top of the Package)

Pin Name	Input/Output	Functions
A11 - A0	Output	Program memory address output
I7 - I0	Input	Program memory data input
$\overline{CE}$	Output	Chip enable signal output
$\overline{OE}$		Output enable signal output
VCC	Power supply	+5V (connected with VDD)
GND		0V (connected with VSS)

AC Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Address Delay Time	t_{AD}	V_{SS} = 0V, V_{DD} =2.2~6.0V C_L = 100pF Topr = -30~60°C	-	-	150	ns
Data Setup Time	t_{IS}		150	-	-	ns
Data Hold Time	t_{IH}		50	-	-	ns

Notes for Use

(1) Program memory

The program area are as shown in Figure 1.

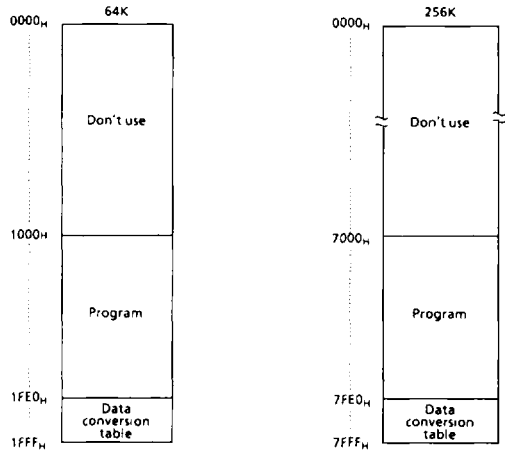


Figure 1. Program Area

(2) I/O ports

Input/Output circuitries of I/O ports in the 47C952A are similar to the code WB of the 47C452A. When this chip is used as evaluator with other I/O code, it is necessary to provide the external resistors. This is also the same when used as the 47C451A evaluator (the 47C451A does not have ports R4, R5 and R9).

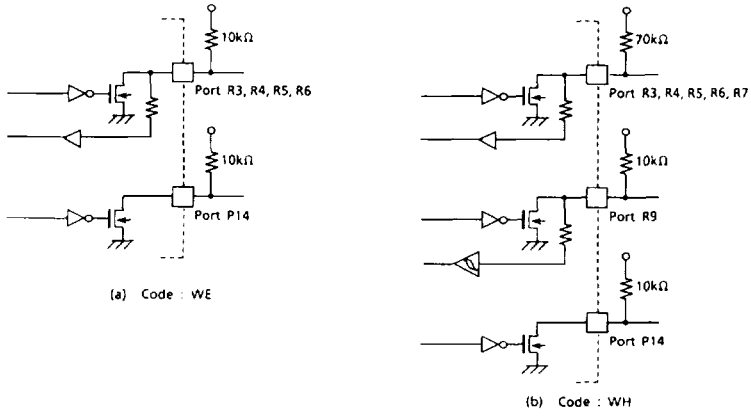


Figure 2. Input/Output Code and External Circuitry