

CMOS 4-BIT MICROCONTROLLER

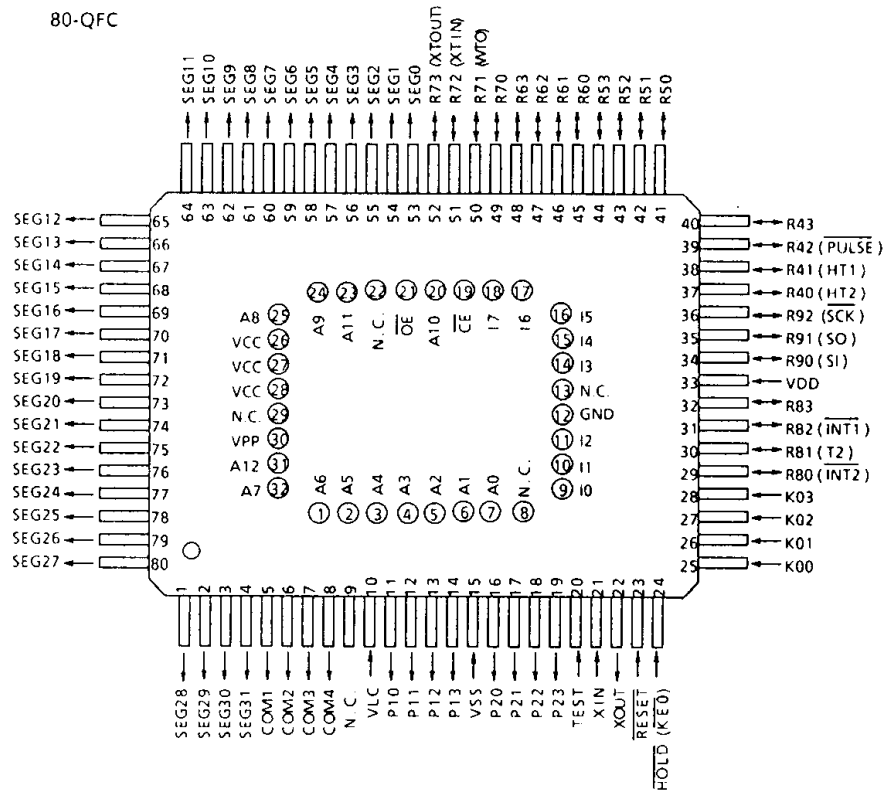
TMP47C020G

The 47C020, which is equipped with an EPROM as program memory, is a piggyback type evaluator chip used for development and operational confirmation of the 47C620/820 application systems (programs).

The 47C020 is pin compatible with the 47C620/820 which are mask-programed ROM devices.

PIN ASSIGNMENT (TOP VIEW)

80-QFC



NOTES FOR USE

(1) Program memory

The program areas are as shown in Figure 1.

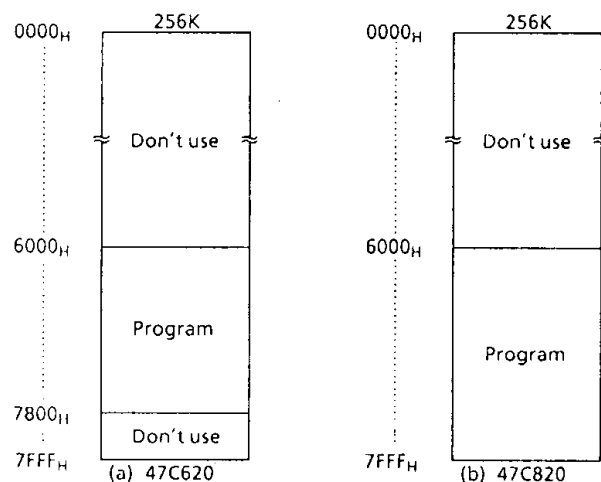


Figure 1. Capacity of EPROM and program area

(2) Data memory

47C020 contains two 256×4 bit data memory banks (DMB0, DMB1).

When 47C020 is used as the 47C620 evaluator, DMB1 has address space at addresses 00-FF_H but do not write data to 80_H and following addresses. DMB0 includes a special function common area so this need not be taken into consideration.

(3) I/O ports

Input/Output circuitries of I/O ports in the 47C020 are similar to the code GA of the 47C620/820.

When this chip is used as evaluator with other I/O code (GB-GF), it is necessary to provide the external resistors.

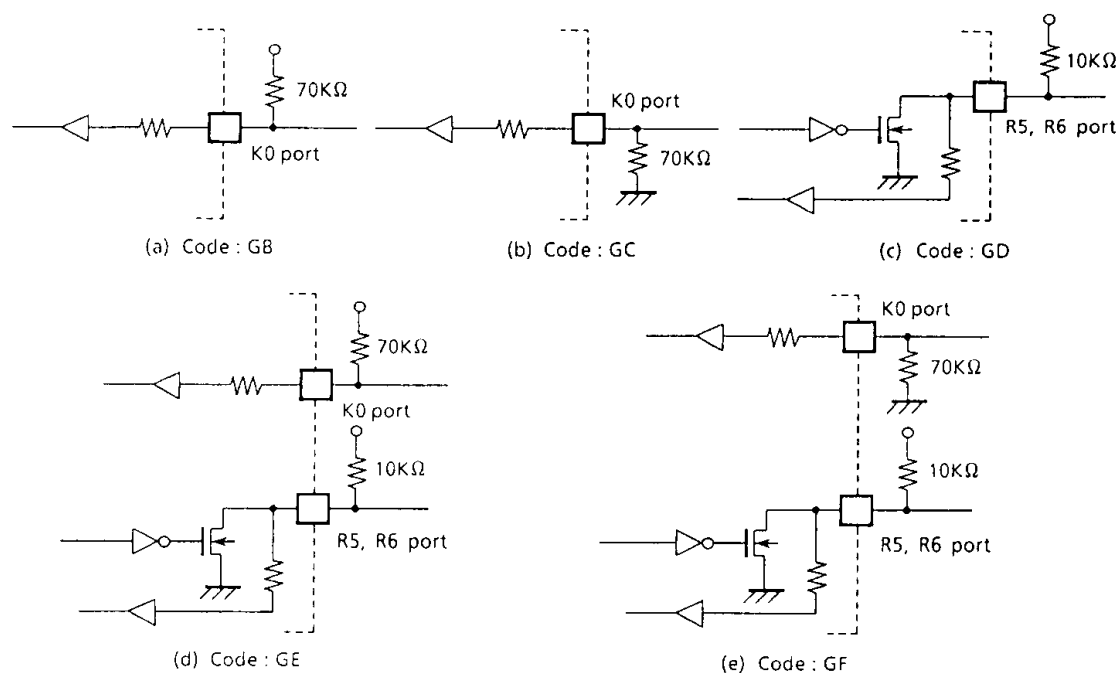


Figure2. I/O code and external circuitry