

SPECIFICATION FOR LCD MODULE

Model No. [TM320240FBCW1](#)

Prepared by:	Date:
Checked by :	Date:
Verified by :	Date:
Approved by:	Date:

TIANMA MICROELECTRONICS CO., LTD

REVISION RECORD

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

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1 Display Specifications

1.1 Display type: STN

1.2 Display color*:

Display color: Blue-Black

Background: Yellow-Green

1.3 Polarizer mode: Transflective/Positive

1.4 Viewing Angle: 6:00

1.5 Driving Duty: 1/240

1.6 Backlight: LED(24.0V)

* Color tone is slightly changed by temperature and driving voltage.

2 Mechanical Specifications

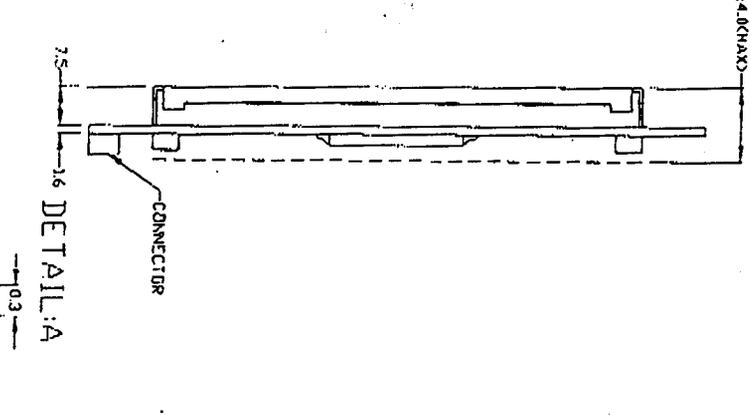
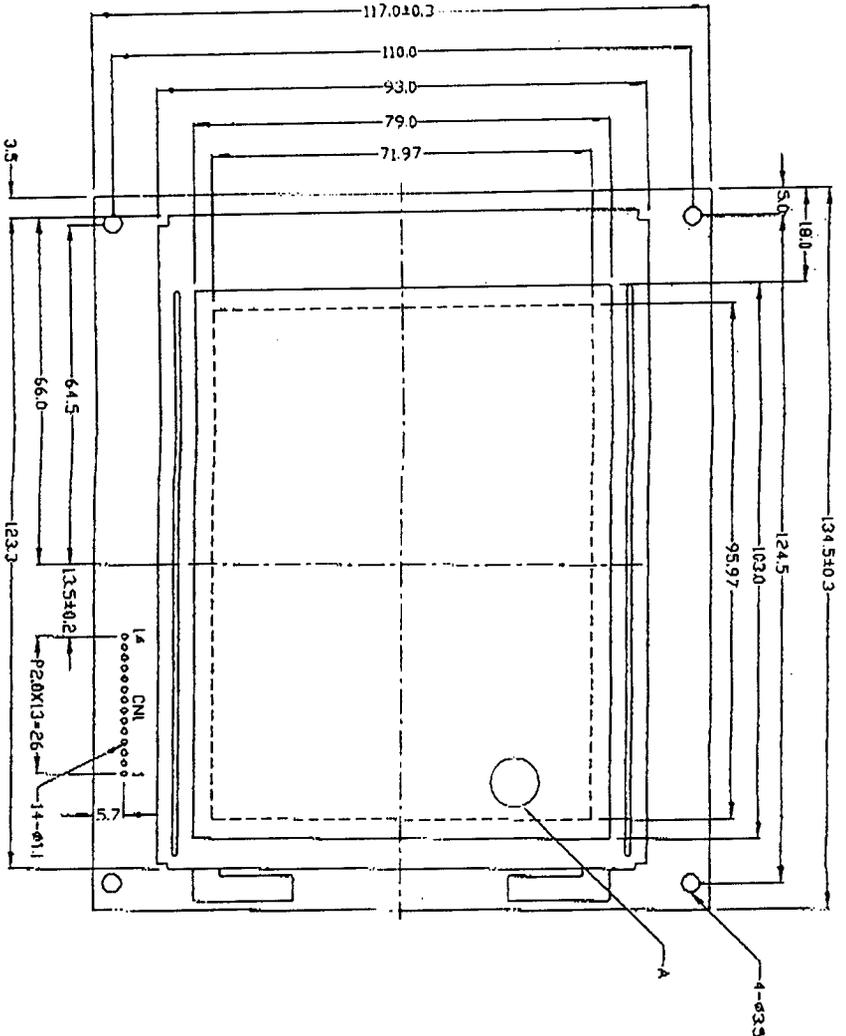
2.1 Outline Dimensions: Refer to outline drawing on page: 2

2.2 Dot Matrix: 320X240

2.3 Dot Size: 0.27X0.27(mm)

2.4 Dot Pitch: 0.30X0.30 (mm)

2.5 Weight: 220 g



- NOTES:
- 1.DISPLAY TYPE: STVYELLOW-GREEN
 - 2.VIEWING DIRECTION: 6:00
 - 3.POLARIZER MODE: TRANSPARENT/POSITIVE
 - 4.OPERATING TEMP: -20°C+70°C
 - 5.STORAGE TEMP: -30°C+80°C
 - 6.LCD OPERATING VOLTAGE: 22.7V
 - 7.DRIVE METHOD: 1/240 DUTY 1/16 BIAS
 - 8.UNMARKED TOLERANCE: ±0.3mm
 - 9.BACKLIGHT TYPE: LED (WHITE 24.0V)
 - 10.IC: KS0086TQ
 - 11.BUILT-IN HEATER

1	2	3	4	5	6	7	8	9	10	11	12	13	14
V0	Vee	D3	D2	D1	D0	Vs	Vdd	CL2	CL1	FLM	K	A	NC

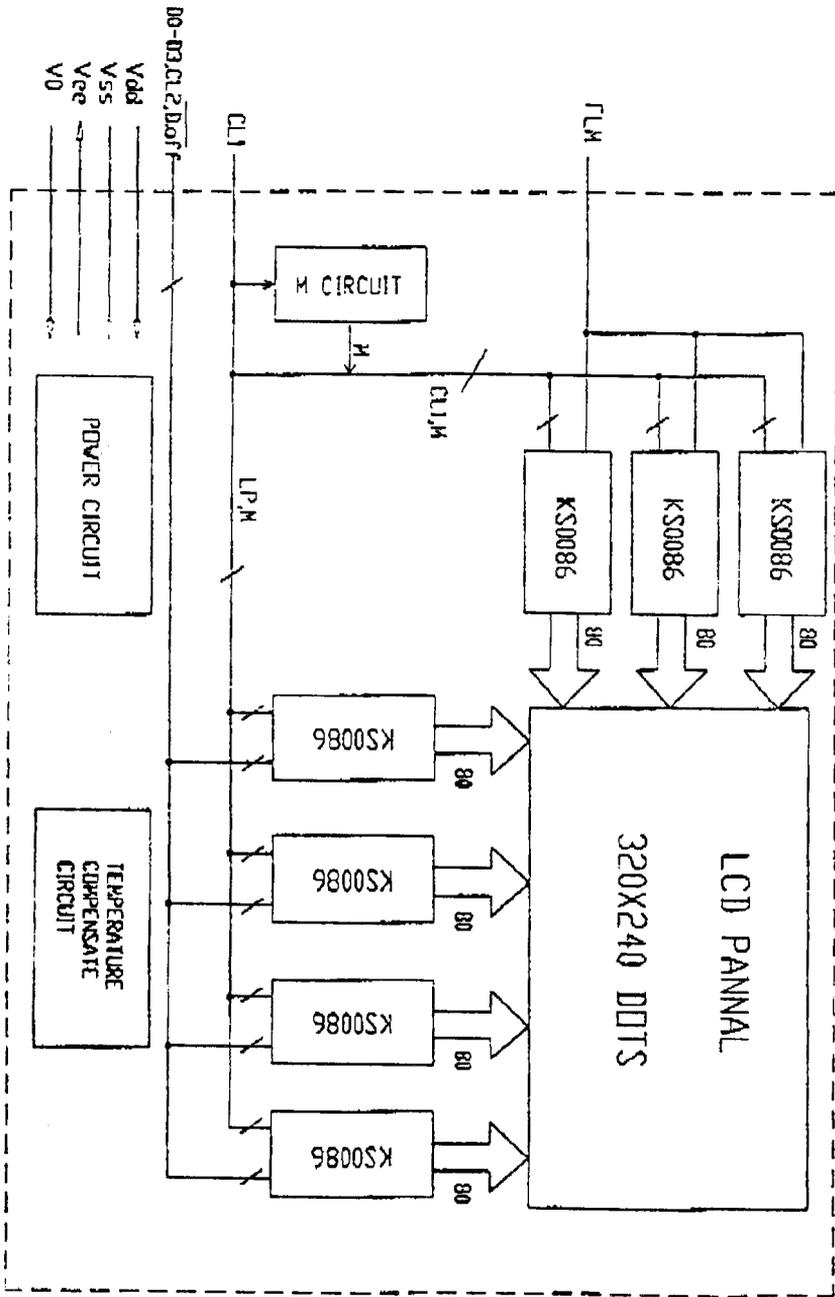


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DRAWN	BY: <i>[Signature]</i>	DATE: 1/4-03	TITLE: TM320240F BCW1	SCALE: <i>[Symbol]</i>
CHECKED	BY: <i>[Signature]</i>	DATE: 1/4-03	DWG NO: G-1	UNIT: mm
APPROVED	BY: <i>[Signature]</i>	DATE: 1/4-03	DWG NAME: <i>[Signature]</i>	SCALE: <i>[Symbol]</i>
CONFIRMED	BY: <i>[Signature]</i>	DATE: 1/4-03	DWG NAME: <i>[Signature]</i>	SCALE: <i>[Symbol]</i>
			SHEET NO:	OR

3 Circuit Block Diagram



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4 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	$V_{DD}-V_{SS}$	-0.3	6.0	V	
LCD Driving Voltage	$V_{DD}-V_{EE}$	\	28.0		
Operating Temperature Range	T_{OP}	-20	+70	°C	No Condensation
Storage Temperature Range	T_{ST}	-30	+80		

5 Electrical Specifications and Instruction Code

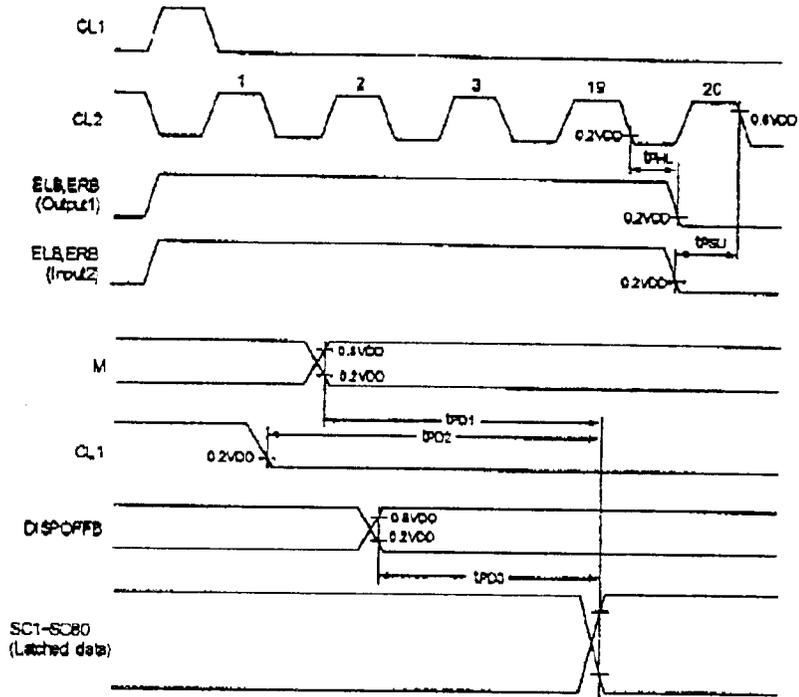
5.1 Electrical characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
Supply Voltage (Logic)	$V_{DD}-V_{SS}$	4.75	5.0	5.25	V		
Supply Voltage (LCD Drive)	$V_{DD}-V_{EE}$	-	22.7	-	V		
Input Signal Voltage	'H'Level	V_{IH}	$0.8V_{DD}$	-	$V_{DD}+0.3$	V	
	'L'Level	V_{IL}	0	-	$0.2V_{DD}$	V	
Supply current (Logic)	I_{DD}	-	-	6.0	mA		
Supply current (LCD Drive)	I_{EE}	-	-	6.0	mA		

5.2 Interface Signals

Pin No.	Symbol	Level	Description
1	V _o	22.7V	Voltage Level for LCD Contrast Adjust
2	V _{ee}	-17.7V	Power supply voltage for LCD
3	D3	H/L	Data bit3
4	D2	H/L	Data bit2
5	D1	H/L	Data bit1
6	D0	H/L	Data bit0
7	V _{ss}	0V	Ground
8	V _{dd}	5.0V	Power supply voltage for logic (+5V)
9	CL1	H/L	Data latch clock
10	CL2	H/L	Data shift clock
11	FLM	H/L	Indicate the beginning of each frame
12	K	0V	Power supply for LED(-)
13	A	24.0V	Power supply for LED(+)
14	NC		

5.3 Interface Timing Chart:

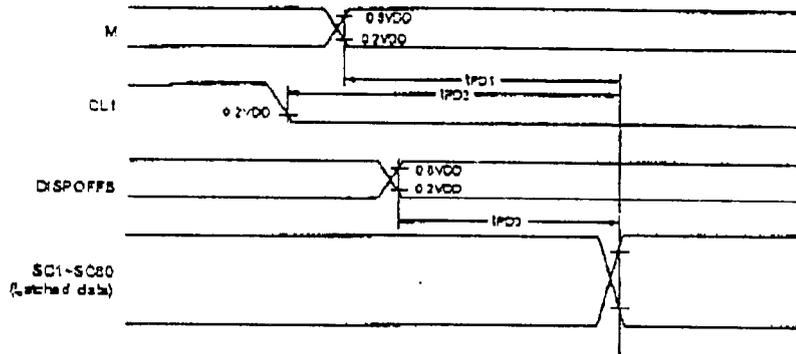


SEGMENT DRIVER APPLICATION

(V_{SS} = 0 V, T_a = -30 ~ +85°C)

Characteristic	Symbol	Test Condition	(1) VDD=5 V ± 10%			(2) VDD=3 V ± 10%			Unit
			MIN	TYP	MAX	MIN	TYP	MAX	
Clock cycle time	t _{cy}	Duty=50%	125	-	-	250	-	-	ns
Clock pulse width	t _{wck}	-	45	-	-	95	-	-	
Clock rise/fall time	t _{r/f}	-	-	-	-	-	-	30	
Data set-up time	t _{ds}	-	30	-	-	65	-	-	
Data hold time	t _{dh}	-	30	-	-	65	-	-	
Clock set-up time	t _{cs}	-	60	-	-	120	-	-	
Clock hold time	t _{ch}	-	80	-	-	120	-	-	
Propagation delay time	t _{PHL}	ELB Output	-	-	60	-	-	125	
		ERB Output	-	-	60	-	-	125	
ELB,ERB set-up time	t _{psu}	ELB Input	30	-	-	65	-	-	
		ERB Input	30	-	-	65	-	-	
DISPOFFB low pulse width	t _{wol}	-	1.2	-	-	1.2	-	-	μs
DISPOFFB clear time	t _{co}	-	100	-	-	100	-	-	ns
M - OUT propagation delay time	t _{po1}	CL=15 pF	-	-	1.0	-	-	1.2	μs
CL1 - OUT propagation delay time	t _{po2}		-	-	1.0	-	-	1.2	
DISPOFFB - OUT propagation delay time	t _{po3}		-	-	1.0	-	-	-	

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COMMON DRIVER APPLICATION

(V_{ss} = 0 V, T_a = -30 - +85°C)

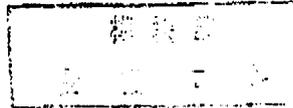
Characteristic	Symbol	Test Condition	(1) VDD=5V ± 10%			(2) VDD=3V ± 10%			Unit
			MIN	TYP	MAX	MIN	TYP	MAX	
Clock cycle time	t _{CY}	Duty=50%	250	-	-	500	-	-	ns
Clock pulse width	t _{WCK}	-	45	-	-	95	-	-	
Clock rise/fall time	t _{RAF}	-	-	-	50	-	-	50	
Data set-up time	t _{DS}	-	30	-	-	65	-	-	
Data hold time	t _{DH}	-	30	-	-	65	-	-	
DISPOFFB low pulse width	t _{WDL}	-	1.2	-	-	1.2	-	-	μs
DISPOFFB clear time	t _{CD}	-	100	-	-	100	-	-	ns
Output delay time	t _{DL}	CL=15 pF	-	-	200	-	-	250	μs
M - OUT propagation delay time	t _{PD1}		-	-	1.0	-	-	1.2	
CL1 - OUT propagation delay time	t _{PD2}		-	-	1.0	-	-	1.2	
DISPOFFB - OUT propagation delay time	t _{PD3}		-	-	1.0	-	-	1.2	

6. Optical Characteristics

6.1 Optical Characteristics

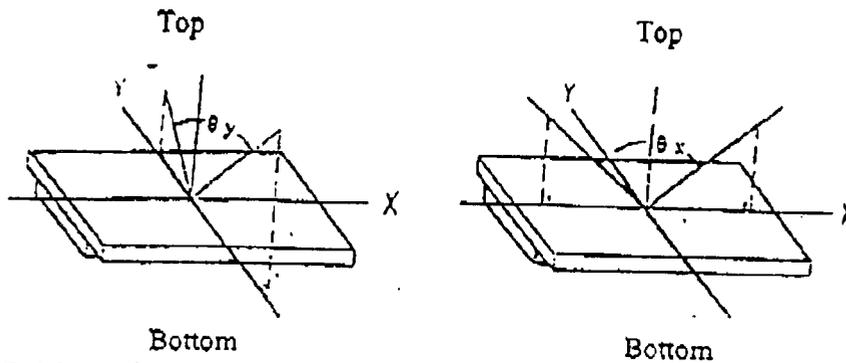
Top=25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle	θ_x	$Cr \geq 2$	-30	--	20	Deg	
	θ_y						
Contrast Ratio	Cr	$\theta_x = 0^\circ$	3.0				
		$\theta_y = 0^\circ$					
Response Time	Turn on	$\theta_x = 0^\circ$ $\theta_y = 0^\circ$			350	ms	
	Turn off						

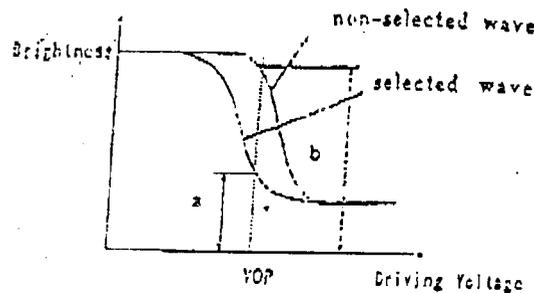


6.2 Definition of optical characteristics

6.2.1 Definition of viewing Angle(see fig. as follow)



6.2.2 Definition of Contrast Ratio(see fig. as follow)

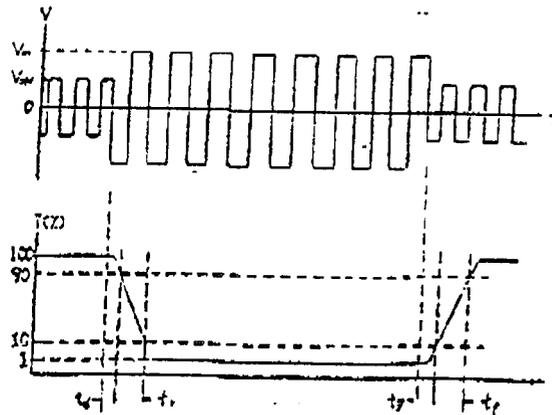


$$\text{Contrast Ratio} = b / a = \frac{\text{non-selected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

- 1) Ambient Temperature: 25 °C ;
- 2) Frame frequency: 64Hz

6.2.3 Definition of Response time(see fig. as follow)



Turn-on time: $t_{on} = t_d + t_r$

Turn-off time: $t_{off} = t_d + t_f$

Measuring Condition:

- 1) Operating Voltage: 22.7V;
- 2) Frame frequency: 64Hz

7. Reliability

7.1 Content of Reliability Test

($T_{OP}=25^{\circ}C$)

No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	80°C 96H
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-30°C 96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	70°C 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	-20°C 96H
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	60°C 90%RH 96H
6	Temperature Cycle	Endurance test applying the low and high temperature cycle $-20^{\circ}C \xleftarrow{30min} 25^{\circ}C \xleftarrow{5min} 60^{\circ}C \xrightarrow{30min} 20^{\circ}C \xrightarrow{5min}$ 1 cycle	-30°C/80°C 10 cycles
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 50m/s ² , 40min
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half-sinewave, 100m/s ² , 1ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	25Kpa 16H

7.2 Failure Judgment Criterion

Criterion Item	Test Item No.									Failure Judgement Criterion
	1	2	3	4	5	6	7	8	9	
Basic Specification	0	0	0	0	0	0	0	0	0	Out of the basic Specification
Electrical specification	0	0	0	0	0					Out of the electrical specification
Mechanical Specification							0	0		Out of the mechanical specification
Optical Characteristic	0	0	0	0	0	0			0	Out of the optical specification -
Remark	Basic specification = Optical specification + Mechanical specification									

8 Precautions for use of LCD Modules

8.1 Handling Precautions

- 8.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 8.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 8.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 8.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 8.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcoholSolvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
 - Water
 - Ketone
 - Aromatic solvents
- 8.1.6 Do not attempt to disassemble the LCD Module.
- 8.1.7 If the logic circuit power is off, do not apply the input signals.
- 8.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - a. Be sure to ground the body when handling the LCD Modules.
 - b. Tools required for assembly, such as soldering irons, must be properly ground.
 - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

8.2 Storage precautions

8.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

8.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

relatively humidity: $\leq 80\%$

8.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

8.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.