# SPECIFICATION FOR LCD MODULE

Model No. TM320240EGGW

Prepared by: Date: Checked by: Date: Verified by: Date: Approved by: Date:

TIANMA MICROELECTRONICS CO., LTD

## **REVISION RECORD**

Date	Ref. Page	Revision No.	Revision Items	Check & Approval

## 1 General Specifications:

1.1 Display type: FSTN

1.2 Display color\*:

Display color: Blue-Black

Background: White

1.3 Polarizer mode: Transmissive/Negative

1.4 Viewing Angle: 6:00

1.5 Driving Method: 1/240 Duty 1/16 Bias

1.6 Backlight: CCFL

\* Color tone is slightly changed by temperature and driving voltage.

1.7 Controller: SED1335F

1.8 Data Transfer: 8 Bit Parallel

1.9 Operating Temperature:  $-20 \text{ to } +70 ^{\circ}\text{C}$ 

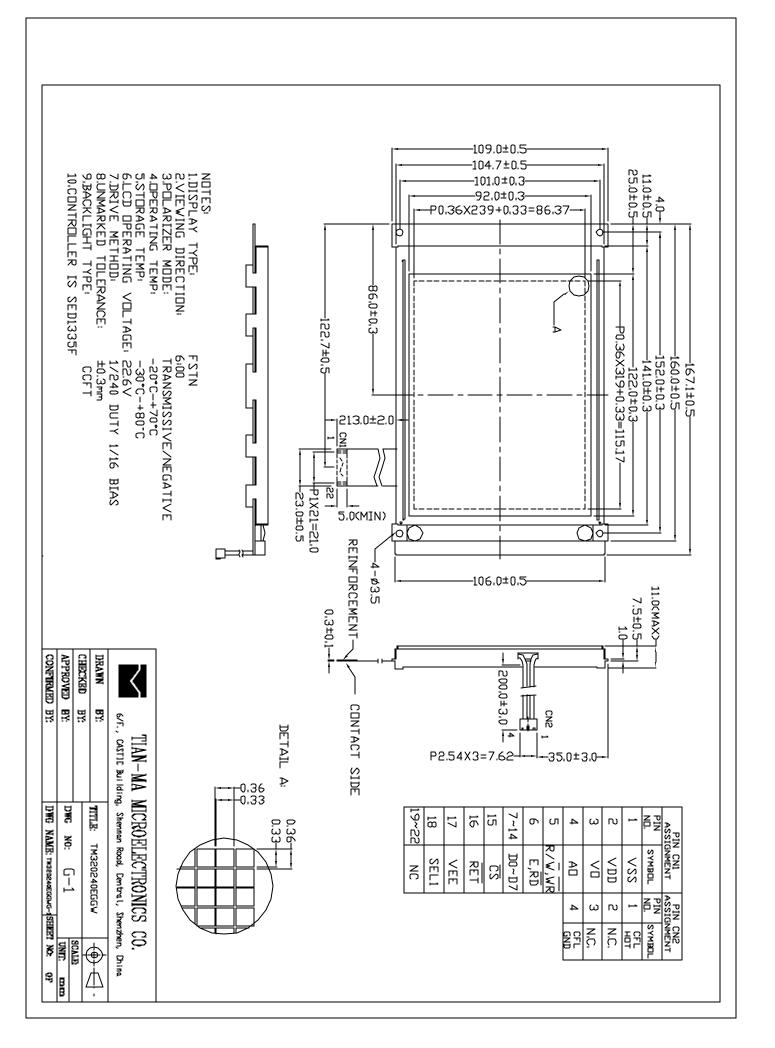
Storage Temperature:  $-30 \text{ to} + 80 ^{\circ}\text{C}$ 

1.10 Outline Dimensions: Refer to outline drawing on next page

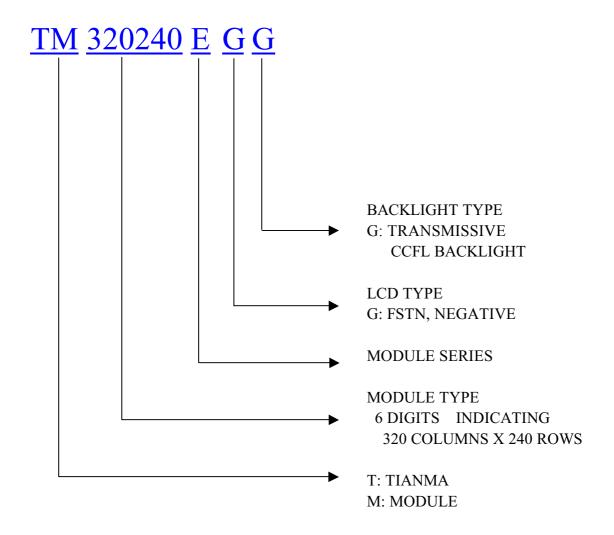
1.11 Dot Matrix: 320 X 240 Dots 1.12 Dot Size: 0.33X0.33(mm)

1.13 Dot Pitch: 0.36X0.36 (mm)

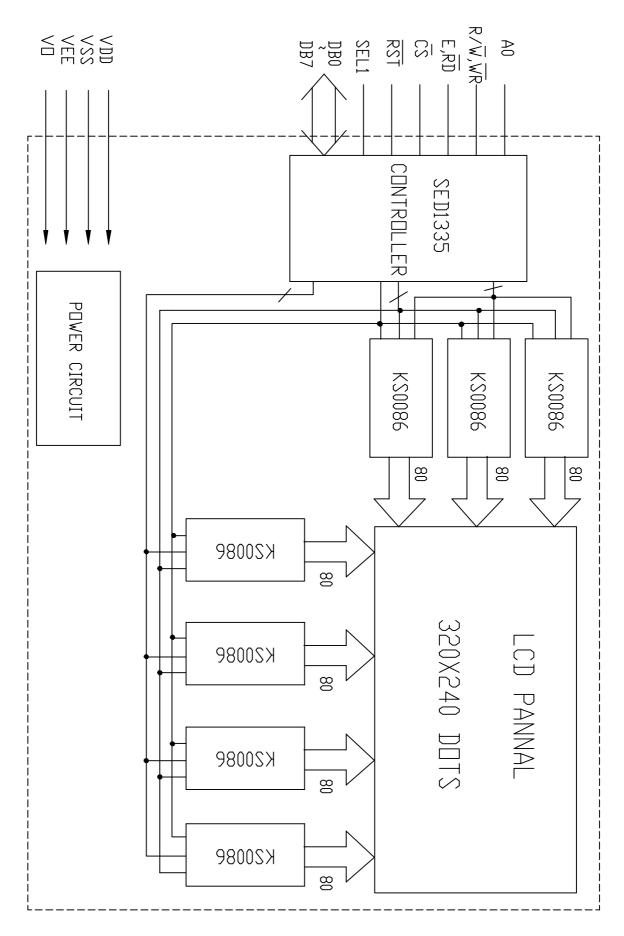
1.14 Weight: 220g



## 3 LCD Module Part Numbering System



## 4 Circuit Block Diagram



# **5 Absolute Maximum Ratings**

Item	Symbol	Min.	Max.	Unit	Remark	
Power Supply Voltage	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	6.0	V		
LCD Driving Voltage	Vlcd -		28.0	v		
Operating Temperature Range	Тор	-20	+70	°C	No Condensation	
Storage Temperature Range	Тѕт	-30	+80			

# **6 Electrical Specifications and Instruction Code**

## 6.1 Electrical characteristics

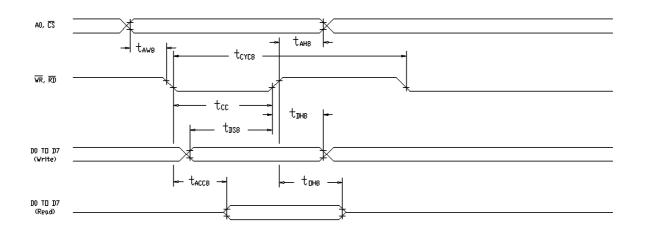
Iten	n	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage (Logic)		V <sub>DD</sub> -V <sub>SS</sub>	4.75	5.0	5.25	V
Supply Voltage (LCD Drive)		Vlcd	1	22.6	-	V
Input	High	$V_{\text{IH}}$ $(V_{DD}=5.0)$	$0.8\mathrm{V}_\mathrm{DD}$	-	V <sub>DD</sub> +0.3	V
Signal Voltage	Low	$V_{\text{IL}}$ $(V_{\text{DD}}=5.0)$	0	-	0.2 V <sub>DD</sub>	V
Supply c (Log		$I_{DD}$ (V <sub>DD</sub> - V <sub>SS</sub> =5.0V)	-	-	25.0	mA
Supply current (LCD Drive)		${ m I}_{\scriptscriptstyle  m EE}$	-	-	5.0	mA
Supply c (CCF		Iccfl	-	5.0	-	mA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Power supply voltage for logic and LCD(+)
3	V0	-17.6V	Power supply voltage for LCD(-)
4	A0	H/L	Data type selection
5	$R/\overline{W}, \overline{WR}$	H/L	68 series R/W signal
			80 series Write strobe signal
6	$E,\overline{RD}$	H/L	68 series E clock
			80 series Read strobe signal
7	DB0	H/L	Data bit0
8	DB1	H/L	Data bit1
9	DB2	H/L	Data bit2
10	DB3	H/L	Data bit3
11	DB4	H/L	Data bit4
12	DB5	H/L	Data bit5
13	DB6	H/L	Data bit6
14	DB7	H/L	Data bit7
15	$\overline{CS}$	L	Chip select signal
16	RET	L	Reset signal
17	VEE	-19.7V	DC-DC voltage Output
18	SEL1	H/L	MPU interface format selection
19~22	NC		
-	CFL HOT	200V~ 820 V	CCFL Backlight power supply(+)
-	NC		
_	NC		
-	CFL GND	0V	CCFL Backlight power supply(-)

## 6.3 Interface Timing Chart

## 8080 family interface timing



Ta=-20 to 75 deg. C

G:1	C11	D	V <sub>DD</sub> =4.5	to 5.5V	V <sub>DD</sub> =2.7	to 5.5V	T I:4	Condition	
Signal Symbol		Parameter	min	min max		max	Unit	Condition	
40 <del>C</del> C	$t_{AH8}$	Address hold time	10	-	10	-	ns		
Au,CS	$A0,\overline{CS}$ $t_{AW8}$	Address setup time	0	ı	0	-	ns		
<u></u>	$t_{CYC8}$	System cycle time	See note.	ı	See note.	ı	ns		
WR,RD	$t_{CC}$	Strobe pulsewidth	120	-	150	-	ns	CL=100pF	
	$t_{ m DS8}$	Data setup time	120	-	120	-	ns		
D0 to D7	$t_{\mathrm{DH8}}$	Data hold time	5	ı	5	-	ns		
ם וטוט די	$t_{ACC8}$	RD access time	ı	50	-	80	ns		
	$t_{OH8}$	Output disable time	10	50	10	55	ns		

Note

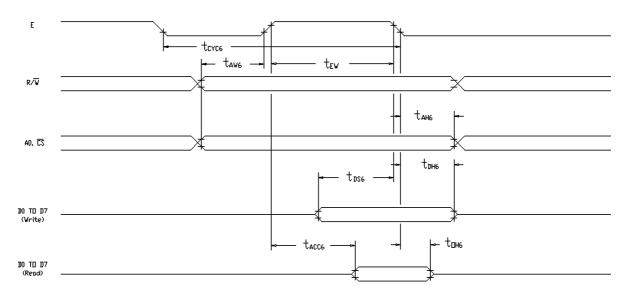
For memory control and system control commands:

$$t_{CYC8} = 2 t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$$

for all other commands:

$$t_{CYC8} = 4 t_C + t_{CC} + 30$$

## 6800 family interface timing



#### Note

tors indicates the interval during which CS is LOW and E is HIGH

Ta=-20 to 75 deg. C

G: 1	G 1 1	D	V <sub>DD</sub> =4.5	to 5.5V	V <sub>DD</sub> =2.7	to 5.5V	TT '4	Condition	
Signal	Symbol	Parameter	min	Max	min	max	Unit	Condition	
A0,CS	$t_{CYC6}$	System cycle time	See note.	ı	See note.	ı	ns		
Au,CS	$t_{AW6}$	Address setup time	0	-	10	-	ns		
R/W	$t_{AH6}$	Address hold time	0	-	0	-	ns		
	$t_{DS6}$	Data setup time	100	-	120	-	ns	CL=100pF	
D0 to D7	$t_{\rm DH6}$	Data hold time	0	ı	0	-	ns		
Do to D7	t <sub>OH6</sub>	Output disable time	10	50	10	75	ns		
t	t <sub>ACC6</sub>	Access time	-	85	-	130	ns		
Е	$t_{\rm EW}$	Enable pulsewidth	120	-	150	-	ns		

### Note

For memory control and system control commands:

$$t_{CYC8} = 2 t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$$

for all other commands:

$$t_{CYC8} = 4 t_C + t_{EW} + 30$$

## 6.4 Instruction Code

Class	Command						Code					•	Command description	Number of
		RD	WR	A0	D7	D6	D5	D4	D3	D2	D1	D0		Bytes
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	Initialize device and display	8
cotrol	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	Enter standby mode	0
	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	Enable and disable display and	1
Display control	SCROLL	1	0	1	0	1	0	0	0	1	0	0	display flashing  Set display start address and  display regions	10
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	Set cursor type	2
	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	Set start address of character	2
	CSRDIR	1	0	1	0	1	0	0	1	1	CD	CD	Set direction of cursor movement	0
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	Set horizontal scroll position	1
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	Set display overlay format	1
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	Set cursor address	2
control	CSRR	0	1	1	0	1	0	0	0	1	1	1	Read cursor address	2
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	Write to display memory	-
control	MREAD	0	1	1	0	1	0	0	0	0	1	1	Read from display memory	-

#### Notes

- 1. In general, the internal registers of the SED1335F/1336F are modified as each command parameter is input. However, the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters have been input. The internal registers for the parameters that have been input will have been changed but the remaining parameter registers are unchanged.
  - 2-byte parameters (where two bytes are treated as 1 data item) are handled as follows:
  - a.CSRW,CSRR:Each byte is processed individuallly. The microprocessor may read or write just the low byte of the cursor address.
  - b.SYSTEM SET,SCROLL,CGRAM ADR:Both parameter bytes are processed together.If the command is changed after half of the parameters has been input, the single byte is ignored.
- 2. APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.

## 6.5 Character Code Table



# 7 Optical Characteristics

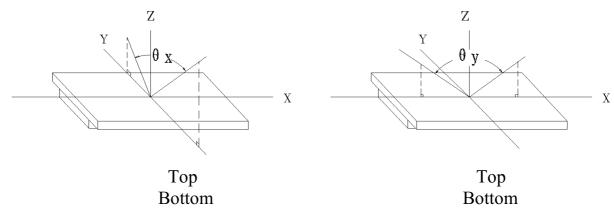
7.1 Optical Characteristics

$T_{\alpha}$	-25	$^{\circ}$
ra-	-23	$\cup$

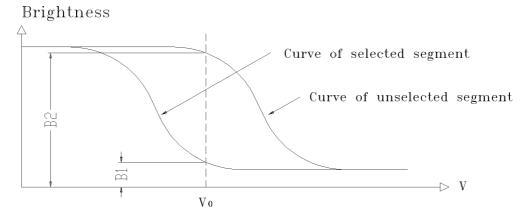
Item	Item		Condition		Min.	Тур.	Max.	Unit
Viewing Angle		$\theta_{\!\mathbf{x}}$	Cr≥2	θ <sub>y</sub> =0°	-30		20	Dag
		θу	Cr <u>~</u> 2	θ <sub>x</sub> =0°	-30		30	Deg
Contrast 1	Ratio	Cr	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		3.0	-	-	
Response	Turn on	Ton	$ heta_{\!\scriptscriptstyle \mathbf{x}} = \! 0^{\circ}  hinspace$		-	-	350	tmg.
Time	Turn off	Toff	$\theta_{y}$ =	=0°	-	-	350	ms

## 7.2 Definition of Optical Characteristics

## 7.2.1 Definition of Viewing Angle



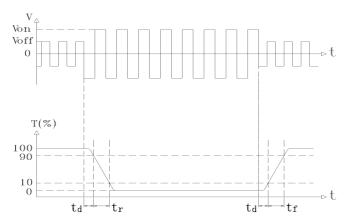
### 7.2.2 Definition of Contrast Ratio



Contrast Ratio = 
$$B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

1) Ambient Temperature:  $25^{\circ}$ C; 2) Frame frequency: 64Hz 7.2.3 Definition of Response time



Turn on time:  $t_{on} = t_d + t_r$  Turn off time:  $t_{off} = t_d + t_f$  Measuring Condition:

1) Operating Voltage: 22.6V; 2) Frame frequency: 64Hz

# 8 Reliability

## 8.1 Content of Reliability Test

Ta=25 ℃

No.	Test Item	Content of Test	Test condition
1	High Temperature	Endurance test applying the high	80°C
	Storage	storage temperature for a long time	96H
	Low Temperature	Endurance test applying the low	-30°C
2	Storage	storage temperature for a long time	96H
	High Temperature	Endurance test applying the	
3	Operation	electric stress (voltage & current)	70℃
		and the thermal stress to the	96H
		element for a long time	
	Low Temperature	Endurance test applying the	-20°C
4	Operation	electric stress under low	96H
		temperature for a long time	
	High Temperature	Endurance test applying the high	40℃
5	/Humidity Storage	temperature and high humidity	90%RH
		storage for a long time	96H
	Temperature	Endurance test applying the low	-30°C/80°C
	Cycle	and high temperature cycle	
6		$-30^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C} \longleftrightarrow 80^{\circ}\text{C} \longleftrightarrow 25^{\circ}\text{C}$	10 cycles
		30min 5min 30min 5min →	
		1 cycle	
	Vibration Test	Endurance test applying the	10Hz~150Hz,
7	(package state)	vibration during transportation	$50\text{m/s}^2$ ,
			40min
	Shock Test	Endurance test applying the shock	Half-sinewave,
8	(package state)	during transportation	$100 \text{m/s}^2$ ,
			11ms
	Atmospheric	Endurance test applying the	40kPa
9	Pressure Test	atmospheric pressure during	16H
		transportation by air	

# 8.2 Failure Judgment Criterion

Criterion			To	est l	Iten	n N	0.			Failura Judgament Critorian
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion
Basic Specification	1	1	1	1	1	1	1	<b>V</b>	<b>V</b>	Out of the basic Specification
Electrical specification	1	1	V	1	V					Out of the electrical specification
Mechanical Specification							1	<b>V</b>		Out of the mechanical specification
Optical Characteristic	<b>V</b>	1	<b>V</b>	1	<b>V</b>	<b>V</b>			<b>V</b>	Out of the optical specification
Note	For test item refer to 8.1									
Remark	Basic specification = Optical specification + Mechanical specification									

# 9 QUALITY LEVEL

Examination	At T <sub>a</sub> =25°C		Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL		
External Visual Inspection	Under normal illumi-nation and eyesight condition, the dis-tance between eyes and LCD is 25cm.	See Ap	See Appendix A			Major 1.0 Minor 2.5		
Display Defects	Under normal illumi-nation and eyesight condition, display on inspection.	I See An	pendix B		II	Major 1.0 Minor 2.5		

Note: Major defects: Open segment or common, Short, Serious damages, Leakage

Miner defects: Others

Sampling standard conforms to GB2828

#### 10 Precautions for Use of LCD Modules

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

- 10.2 Storage precautions
- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature:  $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$ 

Relatively humidity: ≤80%

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

**Appendix A**Inspection items and criteria for appearance defects

Items	Contents	Criteria				
Leakage		Not permitted				
Rainbow		According to the limit specimen				
Polarizer	Wrong polarizer attachment	Not permitted				
	Bubble between polarizer and glass	Not counted		Max. 3 defects allowed		
		ф<0.3mm	0.3mm≤¢≤0.5r		nm	
	Scratches of polarizer	According to the limit specimen				
Black spot (in viewing area)		Not counted	Max. 3 spots allowed		Max. 3	
		X<0.2mm	0.2mm≤X≤0.5mm			
		X=(a+b)/2			spots (lines)	
Black line (in viewing area)	t b	Not counted	Max. 3 lines allowed		allowed	
		a<0.02mm	0.02mm≤a≤0.05mm b≤2.0mm			
Progressive cracks		Not permitted				

Appendix B

Inspection items and criteria for display defects

Items		Contents	Critera			
Open segment or open common			Not permitted			
Short			Not permitted			
Wrong viewing angle			Not permitted			
Contrast radio uneven			According to the limit specimen			
Crosstalk			According to the limit specimen			
	+ 0	+ + a	Not counted	Max.3 dots allowed		
Pin holes and cracks in segment (DOT)		X<0.1mm	0.1mm≤X≤0.2mm			
		X=(a+b)/2		Max.3 dots		
	- D	Not counted	Max.2 dots allowed	allowed		
		A<0.1mm	0.1mm≤A≤0.2mm D<0.25mm			
Black spot (in viewing area)	Q Q	Not counted	Max.3 spots allowed			
		X<0.1mm	0.1mm≤X≤0.2mm			
		X=(a+b)/2	Max.3 spots (lines)			
Black line (in viewing area)	b b	Not counted	Max.3 lines allowed	allowed		
		a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm			

Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Critera			
Transfor- mation of segment	1	Not counted	Max. 2 defects allowed		
		x<0.1mm	0.1mm≤x≤0.2mm		
		x=(a+b)/2			
				Max.3	
	D-111-a	Not counted	Max. 1 defects allowed	defects	
		a<0.1mm	0.1mm≤a≤0.2mm D>0		
		Max.2 defects 0.8W≤a≤1.2 a=measured va W=nominal va			
	-1 F-w -1 F-a				