# SPECIFICATION FOR LCD MODULE

Model No. TM24064FDA

Prepared by:	Date:
Checked by :	Date:
Verified by :	Date:
Approved by:	Date:

TIANMA MICROELECTRONICS CO., LTDeethu.com

# **REVISION RECORD**

Date	Ref. Page	Revision No.	<b>Revision Items</b>	Check & Approval

# 1 General Specifications:

1.1 Display type:	STN
1.2 Display color*:	
Display color:	Blue-Black
Background:	Grey
1.3 Polarizer mode:	Reflective/Positive
1.4 Viewing Angle:	6:00
1.5 Driving Method:	1/64 Duty 1/9 Bias
1.6 Backlight:	None
* Color tone is sli	ghtly changed by temperature and driving voltage.
1.7 Controller:	SED1335F0A
1.8 Data Transfer:	8 Bit Parallel
1.9 Operating Temper	cature: $0 + 50 \degree C$
Storage Tempera	ture: $-20+60^{\circ}C$
1.10 Outline Dimensi	ons: Refer to outline drawing on next page
1.11 Dot Matrix:	240 X 64 Dots
1.12 Dot Size:	0.58X0.82(mm)
1.13 Dot Pitch:	0.62X0.86 (mm)
1.14 Weight:	200g

# **2** Outline Drawing





# DBO ~DB7 -AO -RESET -VR2 YR1 ମା କା ସୁ କୁ କୁ ମାହା କା ସୁ କୁ SEL -LCD CONTROLLER Power on/off sequence Circuit SED1335 Power Supply Circuit LOL TX VAO~VA12 DO~D7 VRD VWR 5 ଞା SRAM IC1 1 64 IC8 80 ١ 240 X 64 DOTS LCD PANEL Λ 80 IC9 V Λ IC10 80

# 4 Circuit Block Diagram

# **5** Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	Vdd-Vss	-0.3	6.0	v	
LCD Driving Voltage	VLCD	-	25.0	v	
Operating Temperature Range	Тор	0	+50	°C	No
Storage Temperature Range	Тѕт	-20	+6 0		Condensation

# **6 Electrical Specifications and Instruction Code**

6.1 Electrical characteristics

Iter	n	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage (Logic)		Vdd-Vss	4.75	5.0	5.25	V
Supply Voltage (LCD Drive)		Vlcd	-	13.2	-	V
Input	High	$V_{IH}$ ( $V_{DD}=5.0$ )	$0.8 \mathrm{V_{DD}}$	-	V <sub>DD</sub> +0.3	V
Signal Voltage Low		$V_{IL}$ ( $V_{DD}=5.0$ )	0	-	$0.2 \ V_{DD}$	V
Supply current (Logic)		$I_{DD}$ (V <sub>DD</sub> - V <sub>SS</sub> =5.0V)	-	20	-	mA
Supply c (LCD D		$I_{LCD}$	-	3.0	-	mA

6.2 Interface Signals

Pin No.	Symbol	Level	Description
1	VDD	5.0V	Power supply voltage for logic and LCD(+)
2	VSS	0V	Ground
3	NC	-	No Signal
4	RES	L	Reset signal
5	CS	L	Chip select signal
6	A0	H/L	Data type Selection
7	$\overline{WR}(R/W)$	H/L	68 series R/W signal
			80 series Write strobe signal
8	RD(E)	H/L	68 series E clock
			80 series Read strobe signal
9	DB0	H/L	Data bit0
10	DB1	H/L	Data bit1
11	DB2	H/L	Data bit2
12	DB3	H/L	Data bit3
13	DB4	H/L	Data bit4
14	DB5	H/L	Data bit5
15	DB6	H/L	Data bit6
16	DB7	H/L	Data bit7
17	SEL	H/L	MPU interface format selection
18	VR1	-	Terminal for Contrast A directorent
19	VR2	-	Terminal for Contrast Adjustment
20	NC	-	No Signal

# 6.3 Interface Timing Chart

# 8080 family interface timing



# Ta=-20 to 75 deg. C

Signal	Sumbol	Parameter	V <sub>DD</sub> =4.5	to 5.5V	V <sub>DD</sub> =2.7	to 5.5V	Unit	Condition
Signal	Symbol	Parameter	min	max	min	max	Unit	Condition
$A0,\overline{CS}$	t <sub>AH8</sub>	Address hold time	10	-	10	-	ns	
A0,CS	t <sub>AW8</sub>	Address setup time	0	-	0	-	ns	
	t <sub>CYC8</sub>	System cycle time	See note.	-	See note.	-	ns	
WR,RD	t <sub>CC</sub>	Strobe pulsewidth	120	-	150	-	ns	CL=100pF
	$t_{\rm DS8}$	Data setup time	120	-	120	-	ns	
D0 to D7	t <sub>DH8</sub>	Data hold time	5	-	5	-	ns	
D010D7	t <sub>ACC8</sub>	$\overline{\text{RD}}$ access time	-	50	-	80	ns	
	t <sub>OH8</sub>	Output disable time	10	50	10	55	ns	

Note

For memory control and system control commands:

 $t_{CYC8} = 2 t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$ 

for all other commands:

 $t_{\rm CYC8} = 4 t_{\rm C} + t_{\rm CC} + 30$ 

# 6800 family interface timing





 $t_{\rm crcs}$  indicates the interval during which CS is LOW and E is HIGH

Ta=-20 to 75 deg. C
---------------------

Circul	Correcto e 1	Demonster	V <sub>DD</sub> =4.5	to 5.5V	V <sub>DD</sub> =2.7	to 5.5V	I I.a.i.4	Condition
Signal	Symbol	Parameter	min	Max	min	max	Unit	Condition
A0, CS	t <sub>CYC6</sub>	System cycle time	See note.	-	See note.	-	ns	
A0,C5	t <sub>AW6</sub>	Address setup time	0	-	10	-	ns	
$R/\overline{W}$	$t_{\rm AH6}$	Address hold time	0	-	0	-	ns	
	t <sub>DS6</sub>	Data setup time	100	-	120	-	ns	CL=100pF
D0 to D7	t <sub>DH6</sub>	Data hold time	0	-	0	-	ns	
D0 10 D7	t <sub>OH6</sub>	t <sub>OH6</sub> Output disable time		50	10	75	ns	
	t <sub>ACC6</sub>	Access time	-	85	-	130	ns	
Е	$t_{\rm EW}$	Enable pulsewidth	120	-	150	-	ns	

Note

For memory control and system control commands:

 $t_{CYC8} = 2 t_C + t_{CC} + t_{CEA} + 75 > t_{ACV} + 245$ 

for all other commands:

 $t_{\rm CYC8}\!=\!4~t_{\rm C}+t_{\rm EW}\!+30$ 

## 6.4 Instruction Code

Class	Command		Code						Command description	Number				
		RD	WR	A0	D7	D6	D5	D4	D3	D2	D1	D0		of
														Bytes
System	SYSTEM SET	1	0	1	0	1	0	0	0	0	0	0	Initialize device and display	8
cotrol	SLEEP IN	1	0	1	0	1	0	1	0	0	1	1	Enter standby mode	0
	DISP ON/OFF	1	0	1	0	1	0	1	1	0	0	D	Enable and disable display and	1
													display flashing	
Display	SCROLL	1	0	1	0	1	0	0	0	1	0	0	Set display start address and	10
control													display regions	
	CSRFORM	1	0	1	0	1	0	1	1	1	0	1	Set cursor type	2
	CGRAM ADR	1	0	1	0	1	0	1	1	1	0	0	Set start address of character	2
													generator RAM	
	CSRDIR	1	0	1	0	1	0	0	1	1	CD	CD	Set direction of cursor	0
													movement	
	HDOT SCR	1	0	1	0	1	0	1	1	0	1	0	Set horizontal scroll position	1
	OVLAY	1	0	1	0	1	0	1	1	0	1	1	Set display overlay format	1
Drawing	CSRW	1	0	1	0	1	0	0	0	1	1	0	Set cursor address	2
control	CSRR	0	1	1	0	1	0	0	0	1	1	1	Read cursor address	2
Memory	MWRITE	1	0	1	0	1	0	0	0	0	1	0	Write to display memory	-
control	MREAD	0	1	1	0	1	0	0	0	0	1	1	Read from display memory	-

### Notes

1. In general, the internal registers of the SED1335F/1336F are modified as each command parameter is input. However, the microprocessor does not have to set all the parameters of a command and may send a new command before all parameters have been input. The internal registers for the parameters that have been input will have been changed but the remaining parameter registers are unchanged.

2-byte parameters (where two bytes are treated as 1 data item) are handled as follows:

a.CSRW,CSRR:Each byte is processed individually. The microprocessor may read or write just the low byte of the cursor address.

b.SYSTEM SET,SCROLL,CGRAM ADR:Both parameter bytes are processed together.If the command is changed after half of the parameters has been input, the single byte is ignored.

2. APL and APH are 2-byte parameters, but are treated as two 1-byte parameters.

# 6.5 Character Code Table



# **7** Optical Characteristics

7.1 Optical Characteristics

Ta=25℃

	Charac			1a-25 C				
Item		Symbol	Cone	dition	Min.	Тур.	Max.	Unit
Viewing	Angla	θx	C >2	θy=0°	-30	)	20	Dog
Viewing A	Angle	θγ	$Cr \ge 2$ $\theta_x = 0^{\circ}$		-30 30		Deg	
Contrast ]	Ratio	Cr	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		3.0	-	-	
Response	Turn on	Ton	$\theta_{x}=0^{\circ}$ $\theta_{y}=0^{\circ}$		-	-	300	ma
Time	Turn off	Toff			-	-	300	ms

# 7.2 Definition of Optical Characteristics7.2.1 Definition of Viewing Angle





Turn on time:  $t_{on} = t_d + t_r$  Turn off time:  $t_{off} = t_d + t_f$ Measuring Condition:

1) Operating Voltage: 13.2V;

2) Frame frequency: 64Hz

# 8 Reliability

8.1 0	Content of Reliability	Ta=25℃	
No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	60℃ 96H
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-20℃ 96H
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	50℃ 96H
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	0°C 96H
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	40℃ 90%RH 96H
6	Temperature Cycle	Endurance test applying the low and high temperature cycle $-20^{\circ}C \leftrightarrow 25^{\circ}C \leftrightarrow 60^{\circ}C \leftrightarrow 25^{\circ}C$ 30min 5min 30min 5min 1 cycle	-20°C/60°C 10 cycles
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 50m/s <sup>2</sup> , 40min
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 100m/s <sup>2</sup> , 11ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	40kPa 16H

# 8.2 Failure Judgment Criterion

Criterion	Test Item No.									Failura Judgament Criterian	
Item	1	2	3	4	5	6	7	8	9	Failure Judgement Criterion	
Basic Specification			$\checkmark$					$\checkmark$	$\checkmark$	Out of the basic Specification	
Electrical specification										Out of the electrical specification	
Mechanical Specification										Out of the mechanical specification	
Optical Characteristic									$\checkmark$	Out of the optical specification	
Note	For test item refer to 8.1										
Remark	Basic specification = Optical specification + Mechanical specification										

# 9 QUALITY LEVEL

Examination	At T <sub>a</sub> =25°C	Inspection					
or Test	(unless otherwise stated)	Min.	Max.	Unit	IL	AQL	
External Visual Inspection	Under normal illumi-nation and eyesight condition, the dis-tance between eyes and LCD is 25cm.	See Appendix A			II	Major 1.0 Minor 2.5	
Display Defects	Undernormalillumi-nationandeyesightcondition,display on inspection.	See Ap	pendix B	II	Major 1.0 Minor 2.5		
Note: Major defects: Open segment or common, Short, Serious damages, Leakage Miner defects: Others Sampling standard conforms to GB2828							

# **10 Precautions for Use of LCD Modules**

- 10.1 Handling Precautions
- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
  - c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range.

If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}$ C $\sim 40^{\circ}$ CRelatively humidity: $\leq 80\%$ 

- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

# Appendix A

Inspection items and criteria for appearance defects

Items	Contents	Criteria					
Leakage		Not permitted					
Rainbow		According to the limit specimen					
	Wrong polarizer attachment	Not permitted					
	Bubble between	Not counted		Max. 3 defects allowed			
Polarizer	polarizer and glass	ф<0.3mm		0.3mm≤ø≤0.5mm			
	Scratches of polarizer	According to the limit specimen		nit specimen			
Black spot (in viewing area)		Not counted	Max. 3 spots allowed		Max. 3		
		X<0.2mm	0.2mm≤X≤0.5mm				
	α	X=(a+b)/2			spots (lines)		
Black line (in viewing area)		Not counted	Max. 3 lines allowed		allowed		
	¢b	a<0.02mm	0.021	mm≪a≪0.05mm b≪2.0mm	-		
Progressive cracks		Not permitted					

# Appendix B

Inspection items and criteria for display defects

Items	Items Contents		Critera				
Open segment or open common			Not permitted				
Short			Not permitted				
Wrong viewing angle			Not permitted				
Contrast radio uneven			According to the limit specimen				
Crosstalk			According to the limit specimen				
	-		Not counted	Max.3 dots allowed			
			X<0.1mm	0.1mm≤X≤0.2mm			
Pin holes and cracks in segment (DOT)			X=(a+b)/2	Max.3 dots			
	D	2	Not counted	Max.2 dots allowed	allowed		
			A<0.1mm	0.1mm≪A≪0.2mm D<0.25mm			
Black spot (in viewing area)	-		Not counted	Max.3 spots allowed	_		
			X<0.1mm	0.1mm≪X≪0.2mm			
			X=(a+b)/2	Max.3 spots			
Black line (in viewing area)			Not counted	Max.3 lines allowed	- (lines) allowed		
			a<0.02mm	0.02mm≤a≤0.05mm b≤0.5mm			

# Appendix B

Inspection items and criteria for display defects (continued)

Items	Content	Critera			
		Not counted	Max. 2 defects allowed		
	q -	x<0.1mm	0.1mm≤x≤0.2mm		
		x=(a+b)/2			
				Max.3	
	D-+1 + 1=- a	Not counted	Max. 1 defects allowed	defects allowed	
Transfor- mation of segment		a<0.1mm	0.1mm≤a≤0.2mm D>0		
		Max.2 defects 0.8W ≤ a ≤ 1.2 a=measured va W=nominal va			