SPECIFICATION FOR LCD MODULE

Model No. TM128160EKFWG1

Prepared by:	**Date:**
Checked by :	**Date:**
Verified by :	**Date:**
Approved by:	**Date:**

TIANMA MICROELECTRONICS CO., LTD

Ver. 1.0

REVISION RECORD

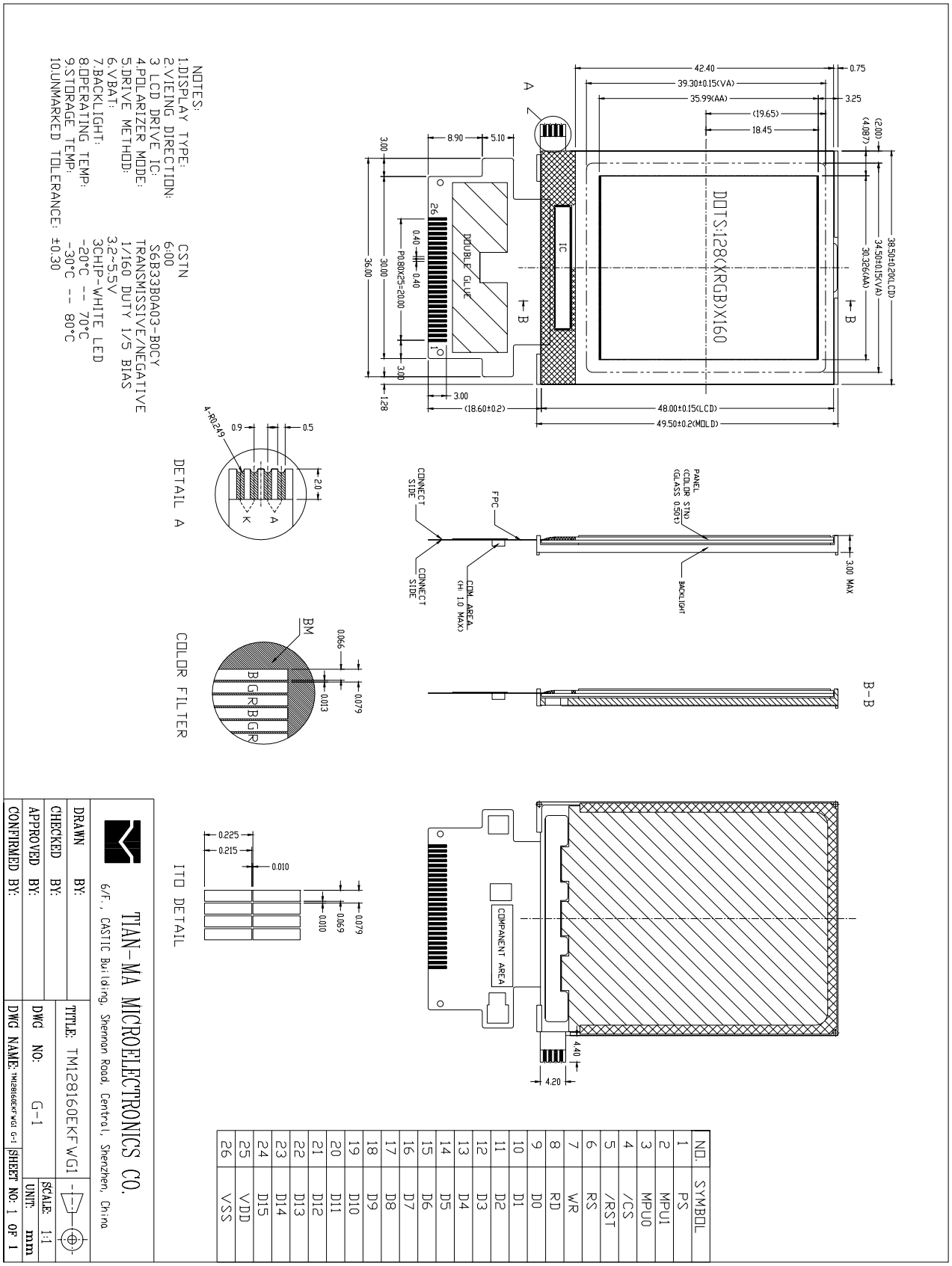
Date	Ver.	Ref. Page	Revision No.	Revision Items

1. General Specifications:

1.1 Display type: COLOR STN
1.2 Display color*[1]:
Display color: 65K COLOR
Background*[2]: Black (Red, Green, Blue dots are off state)
1.3 Polarizer mode: Transmissive/Negative
1.4 Viewing Angle: 6:00
1.5 Driving Method: 1/160 Duty 1/5 Bias
1.6 Backlight Type: LED (3 LAMPS)
Backlight Color: WHITE
1.7 Controller: S6B33B0A03-B0CY
1.8 Data Transfer: 8/16 Bits Parallel or 3/4-PIN Serial Interface
1.9 Operating Temperature: -20～+70℃
Storage Temperature: -30～+80℃
1.10 Power Supply Voltage: VDD=3.0V
1.11 LCD Operating Voltage: VLCD=16.8V
1.12 Outline Dimensions: Refer to outline drawing on next page
1.13 Dot Matrix: 128×3 (RGB)×160 Dots
1.14 Dot Size: 0.227(R+G+B)×0.215(mm^2)
1.15 Dot Pitch: 0.237×0.225 (mm^2)
1.16 Weight: TBD*[3]

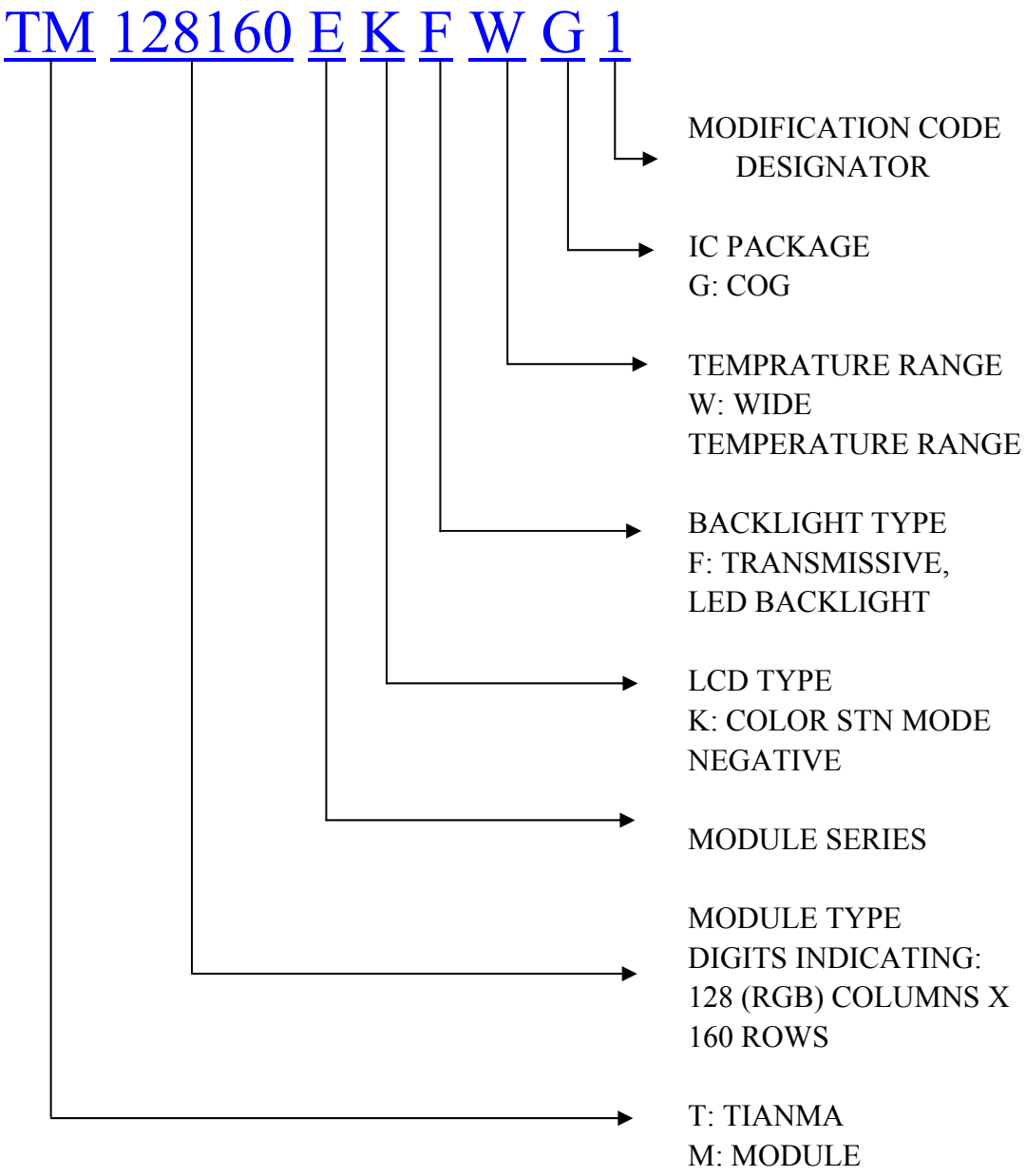
*[1] Color tone is slightly changed by temperature and driving voltage.
*[2] Color tone will be changed by backlight.
*[3] TBD: To Be Determined.

2. Outline Drawing

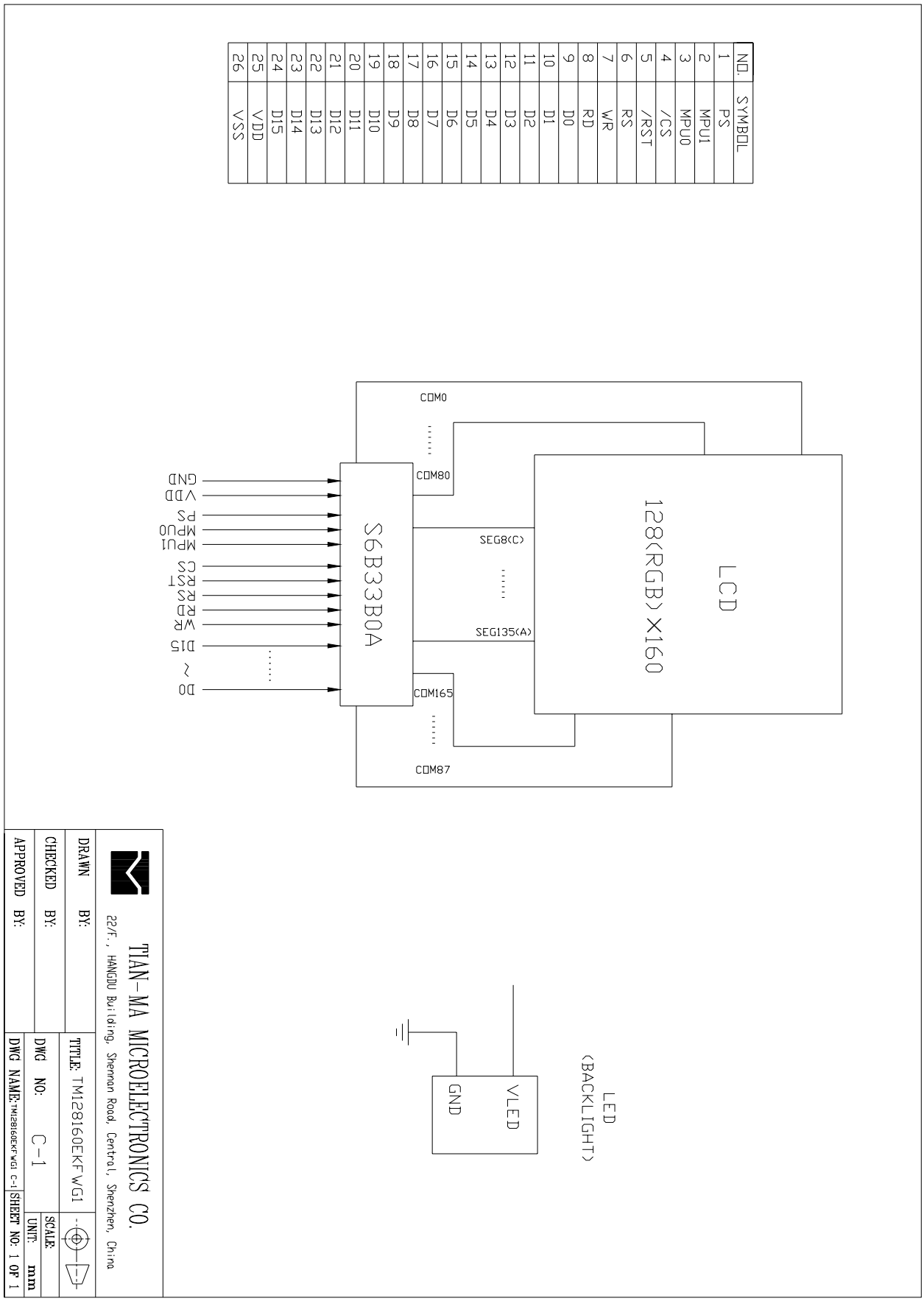


NO.	SYMBOL
1	PS
2	MPU1
3	MPU0
4	/CS
5	/RST
6	RS
7	WR
8	RD
9	D0
10	D1
11	D2
12	D3
13	D4
14	D5
15	D6
16	D7
17	D8
18	D9
19	D10
20	D11
21	D12
22	D13
23	D14
24	D15
25	VDD
26	VSS

3. LCD Module Part Numbering System



4. Circuit Block Diagram



5. Absolute Maximum Ratings

Ta=25℃

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	$V_{DD}-V_{SS}$	-0.3	+4.6	V	
LCD Driving Voltage	V_{LCD}	-0.3	+20.0		
Operating Temperature Range	T_{OP}	-20	+70	℃	No Condensation
Storage Temperature Range	T_{ST}	-30	+80		

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics Vss=0V, Ta=25℃

Item		Symbol	Min.	Typ.	Max.	Unit
Supply Voltage (Logic)		V_{DD}-Vss	+3.1	+3.6	+4.5	V
Supply Voltage (LCD Drive)		V_{LCD}	-	16.8	-	V
Input Signal Voltage	High	V_{IH} (V_{DD}=3.0)	0.8V_{DD}	-	V_{DD}	V
	Low	V_{IL} (V_{DD}=3.0)	0	-	0.2V_{DD}	V
Supply current (Logic)		I_{DD} (V_{DD}- V_{SS}=3.0V)	-	-	2.5	mA
Oscillator frequency range		f_{osc}	220	-	330	KHz
Supply Voltage (LED)		V_{LED}	-	10.0	-	V
Supply current (LED)		I_{LED}		15.0	20.0	mA

6.2 Interface Signals

6.2.1 CN1（FPC）

<table>
<tr><th>Pin No.</th><th>Symbol</th><th>Level</th><th>Description</th></tr>
<tr><td>1</td><td>PS</td><td>H/L</td><td rowspan="7">
<table>
<tr><td>PS</td><td>MPU1</td><td>MPU0</td><td>MPU inter-face select</td></tr>
<tr><td>H</td><td>L</td><td>L</td><td>8080-series 8bit interface</td></tr>
<tr><td>H</td><td>L</td><td>H</td><td>8080-series 16bit interface</td></tr>
<tr><td>H</td><td>H</td><td>L</td><td>6800-series 8bit interface</td></tr>
<tr><td>H</td><td>H</td><td>H</td><td>6800-series 16bit interface</td></tr>
<tr><td>L</td><td>L</td><td>X</td><td>3 pin SPI(Write only)</td></tr>
<tr><td>L</td><td>H</td><td>X</td><td>4 pin SPI(Write only)</td></tr>
</table>
</td></tr>
<tr><td>2</td><td>MPU1</td><td>H/L</td></tr>
<tr><td>3</td><td>MPU0</td><td>H/L</td></tr>
<tr><td>4</td><td>CS</td><td>H/L</td><td colspan="2">Chip select: Low active</td></tr>
<tr><td>5</td><td>RST</td><td>H/L</td><td colspan="2">Reset pin: Low active</td></tr>
<tr><td>6</td><td>RS</td><td>H/L</td><td colspan="2">Index register / Data command select</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">WR(R/W)</td><td rowspan="2">H/L</td><td>6800-series</td><td>ReadWRBite control input pin</td></tr>
<tr><td>8080-series</td><td>Write enable clock input pin</td></tr>
<tr><td rowspan="2">8</td><td rowspan="2">RD(E)</td><td rowspan="2">H/L</td><td>6800-series</td><td>Read / Write control input pin</td></tr>
<tr><td>8080-series</td><td>Read enable clock input pin</td></tr>
<tr><td>9</td><td>DB0</td><td>H/L</td><td colspan="2" rowspan="8">Data bus bit 0-7.
Only DB[7:6] are valid in serial interface mode (DB[5:0]: high impedance, DB[6]: serial clock, DB[7]: serial data).</td></tr>
<tr><td>10</td><td>DB1</td><td>H/L</td></tr>
<tr><td>11</td><td>DB2</td><td>H/L</td></tr>
<tr><td>12</td><td>DB3</td><td>H/L</td></tr>
<tr><td>13</td><td>DB4</td><td>H/L</td></tr>
<tr><td>14</td><td>DB5</td><td>H/L</td></tr>
<tr><td>15</td><td>DB6</td><td>H/L</td></tr>
<tr><td>16</td><td>DB7</td><td>H/L</td></tr>
<tr><td>17</td><td>DB8</td><td>H/L</td><td colspan="2" rowspan="8">Data bus bit 8-15.
Connect DB[15:8] “low” only in 8-series parallel interface mode.</td></tr>
<tr><td>18</td><td>DB9</td><td>H/L</td></tr>
<tr><td>19</td><td>DB10</td><td>H/L</td></tr>
<tr><td>20</td><td>DB11</td><td>H/L</td></tr>
<tr><td>21</td><td>DB12</td><td>H/L</td></tr>
<tr><td>22</td><td>DB13</td><td>H/L</td></tr>
<tr><td>23</td><td>DB14</td><td>H/L</td></tr>
<tr><td>24</td><td>DB15</td><td>H/L</td></tr>
</table>

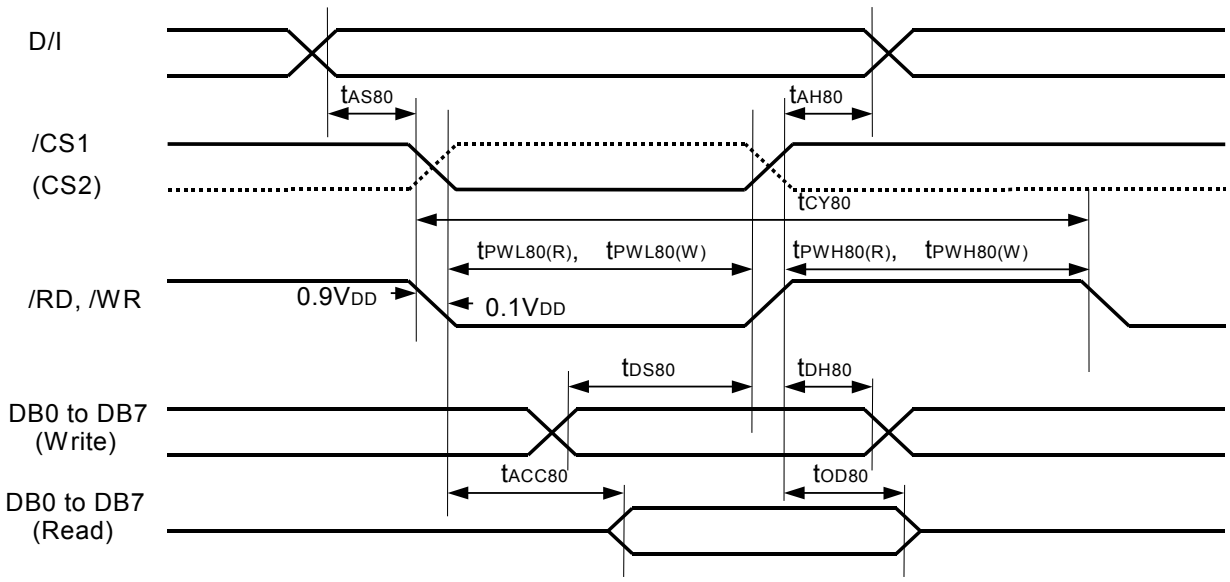
25	VDD	3.0V	Main power supply, 3VDC
26	VSS	0V	Ground

6.2.2 CN2（LED）

Pin No.	Symbol	Level	Description
1, 2	CATHODE	0V	LED CATHODE
3, 4	ANODE	10.0V	LED ANODE

6.3 Interface Timing Chart

Read / Write Characteristics (8080-series MPU)



** $t_{PWL80(W)}$ and $t_{PWL80(R)}$ is specified in the overlapped period when CS1B is low (CS2 is high) and /WR(/RD) is low.

Parallel Interface (8080-series MPU) Timing Diagram

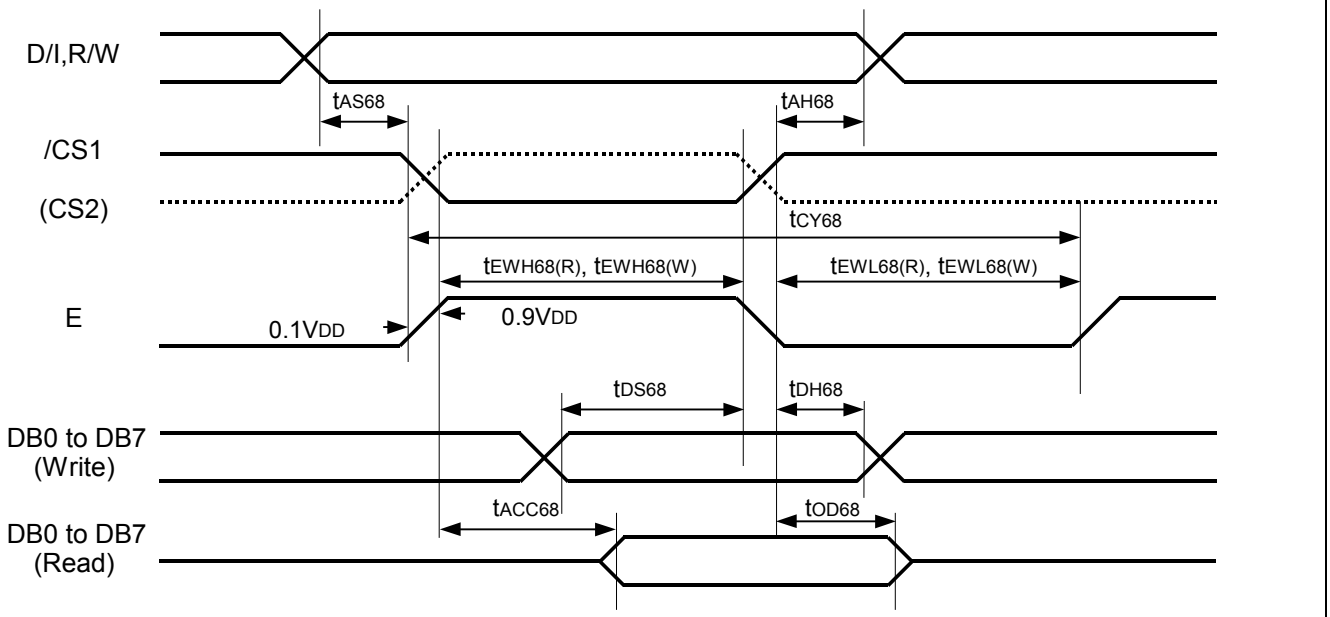
AC Characteristics (8080-series Parallel Mode)

(VDD3 = 1.8 to 3.3V, Ta = -30 to +70°C)

Item	Signal	Symbol	Condition	Min.		Max. (3.3V/1.8V)	Unit
				3.3V	1.8V		
Address setup time Address hold time	D/I	t_{AS80} t_{AH80}		0 0	0 0	- -	ns
System cycle time		t_{CY80}		150	360	-	ns
Pulse width low for write Pulse width High for write	WRB (WRB)	t_{PWLW} t_{PWHW}		50 30	100 75	- -	ns
Pulse width low for read Pulse width high for read	RDB (RDB)	t_{PWLR} t_{PWHR}		50 30	100 75	- -	ns
Data setup time Data hold time	DB0 to DB15	t_{DS80} t_{DH80}		5 8	10 14	- -	ns
Read access time Output disable time		t_{ACC80} t_{OD80}	CL = 100 pF	-		60 / 120	ns
				tEWHR			

NOTE: *1. The input signal rise time and fall time (tr, tf) is specified at 10 ns or less.
(tr + tf) < (t_{CY80} - t_{PWLW} - t_{PWHW}) for write, (tr + tf) < (t_{CY80} - t_{PWLR} - t_{PWHR}) for read

Read / Write Characteristics (6800-series Microprocessor)



** $t_{EWH68(W)}$ and $t_{EWH68(R)}$ is specified in the overlapped period when /CS1 is low (CS2 is high) and E is high.

Parallel Interface (6800-series MPU) Timing Diagram

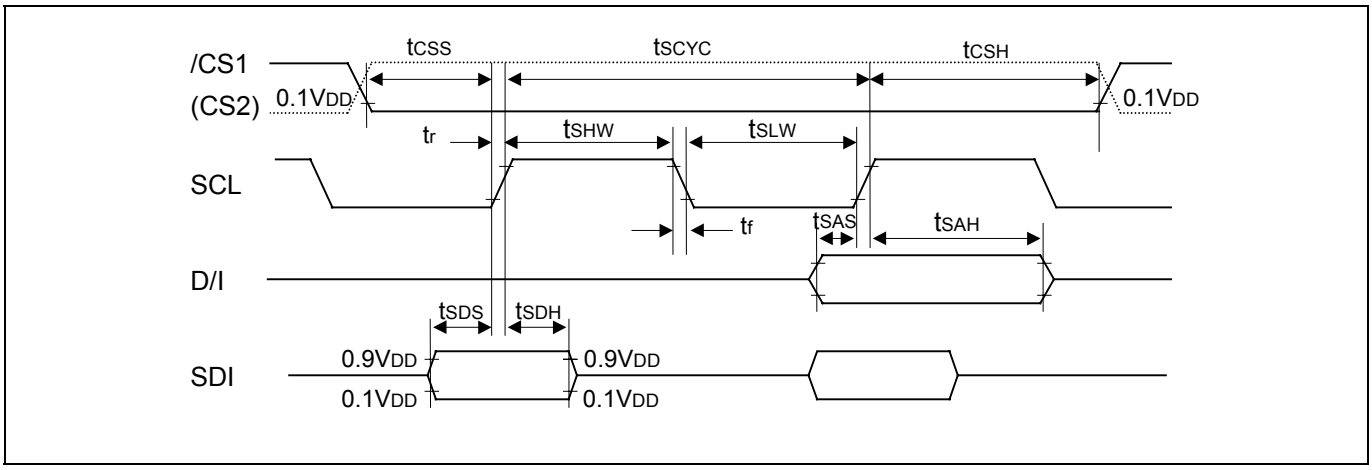
AC Characteristics (6800-series Parallel Mode)

(V_{DD3} = 1.8 to 3.3V, Ta = -30 to +70°C)

Item	Signal	Symbol	Condition	Min.		Max. (3.3V/1.8V)	Unit
				3.3V	1.8V		
Address setup time Address hold time	D/I R/W	t_{AS68} t_{AH68}		0 0	0 0	- -	ns
System cycle time		t_{CY68}		150	360	-	ns
Enable width high for write Enable width low for write	RDB (E)	t_{EWHW} t_{EWLW}		50 30	100 75	- -	ns
Enable width high for read Enable width low for read	RDB (E)	t_{EWHR} t_{EWLR}		50 30	100 75	- -	ns
Data setup time Data hold time	DB0 to DB15	t_{DS68} t_{DH68}		5 8	10 14	- -	ns
Read access time		T_{ACC68}	C_L = 100 pF	-		60 / 120	ns
Output disable time		t_{OD68}		tEWLR			

NOTE: *1. The input signal rise time and fall time (tr, tf) is specified at 10 ns or less.
(tr + tf) < (t_{CY68} - t_{EWHW} - t_{EWLW}) for write, (tr + tf) < (t_{CY68} - t_{EWHR} - t_{EWLR}) for read

Serial Data Interface Timing



Serial Data Interface Timing

(V_{DD3} = 1.8 to 3.3V, Ta = -30 to +70°C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
SCL Cycle Time	SCL	t_{CSC}		50	-	ns
SCL High Pulse Width	SCL	t_{SHW}		20	-	ns
SCL Low Pulse Width	SCL	t_{SLW}		20	-	ns
SDI Setup time	SDI	t_{SDS}		20	-	ns
SDI Hold time	SDI	t_{SDH}		20	-	ns
D/I Setup time	D/I	t_{SAS}		20	-	ns
D/I Hold time	D/I	t_{SAH}		20	-	ns
Chip Select Setup time	CS1B(CS2)	t_{CSS}		20	-	ns
Chip Select Hold time	CS1B(CS2)	t_{CHS}		20	-	ns

6.4 Instruction code

Instruction Table

Instruction Name	D/I	WRB	RDB	DB15 ~ DB8	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Hex.	Parameter
Non Operation	0	0	1	*	0	0	0	0	0	0	0	0	00	
Oscillation Mode Set	0	0	1	*	0	0	0	0	0	0	1	0	02	1Byte
Driver Output Mode Set	0	0	1	*	0	0	0	1	0	0	0	0	10	1Byte
DC-DC Select	0	0	1	*	0	0	1	0	0	0	0	0	20	1Byte
Bias Set	0	0	1	*	0	0	1	0	0	0	1	0	22	1Byte
DCDC Clock Division Set	0	0	1	*	0	0	1	0	0	1	0	0	24	1Byte
DCDC and AMP ON/OFF set	0	0	1	*	0	0	1	0	0	1	1	0	26	1Byte
Temperature Compensation Set	0	0	1	*	0	0	1	0	1	0	0	0	28	1Byte
Contrast Control(1)	0	0	1	*	0	0	1	0	1	0	1	0	2A	1Byte
Contrast Control(2)	0	0	1	*	0	0	1	0	1	0	1	1	2B	1Byte
Standby Mode OFF	0	0	1	*	0	0	1	0	1	1	0	0	2C	-
Standby Mode ON	0	0	1	*	0	0	1	0	1	1	0	1	2D	-
DDRAM Burst Mode OFF	0	0	1	*	0	0	1	0	1	1	1	0	2E	-
DDRAM Burst Mode ON	0	0	1	*	0	0	1	0	1	1	1	1	2F	-
Addressing Mode Set	0	0	1	*	0	0	1	1	0	0	0	0	30	1Byte
ROW Vector Mode Set	0	0	1	*	0	0	1	1	0	0	1	0	32	1Byte
N-line Inversion Set	0	0	1	*	0	0	1	1	0	1	0	0	34	1Byte
Entry Mode Set	0	0	1	*	0	1	0	0	0	0	0	0	40	1Byte
X-address Area Set	0	0	1	*	0	1	0	0	0	0	1	0	42	2Byte
Y-address Area Set	0	0	1	*	0	1	0	0	0	0	1	1	43	2Byte
RAM Skip Area Set	0	0	1	*	0	1	0	0	0	1	0	1	45	1Byte
Display OFF	0	0	1	*	0	1	0	1	0	0	0	0	50	-
Display ON	0	0	1	*	0	1	0	1	0	0	0	1	51	-
Specified Display Pattern Set	0	0	1	*	0	1	0	1	0	0	1	1	53	1Byte
Partial Display Mode Set	0	0	1	*	0	1	0	1	0	1	0	1	55	1Byte
Partial Display Start Line Set	0	0	1	*	0	1	0	1	0	1	1	0	56	1Byte
Partial Display End Line Set	0	0	1	*	0	1	0	1	0	1	1	1	57	1Byte
Area Scroll Mode Set	0	0	1	*	0	1	0	1	1	0	0	1	59	4Byte
Scroll Start Line Set	0	0	1	*	0	1	0	1	1	0	1	0	5A	1Byte
Set Display Data Length	X	X	X	*	1	1	1	1	1	1	0	0	FC	1Byte
Display Data Write	1	0	1	Display Data Write									-	-
Display Data Read	1	1	0	Display Data Read									-	-
Status Read	0	1	0	0	Status Data Read								-	-
Test Mode1	0	0	1	*	1	1	1	1	1	1	1	1	FF	-
Test Mode2	0	0	1	*	1	1	1	1	1	1	1	0	FE	-
Test Mode3	0	0	1	*	1	1	1	1	1	1	0	1	FD	-
Test Mode4	0	0	1	*	1	1	1	1	1	0	1	1	FB	-
Test Mode5	0	0	1	*	1	1	1	1	1	0	1	0	FA	-
Test Mode6	0	0	1	*	1	1	1	1	1	0	0	1	F9	-

*: Don't care

Parameter: The number of parameter bytes that follows instruction data.

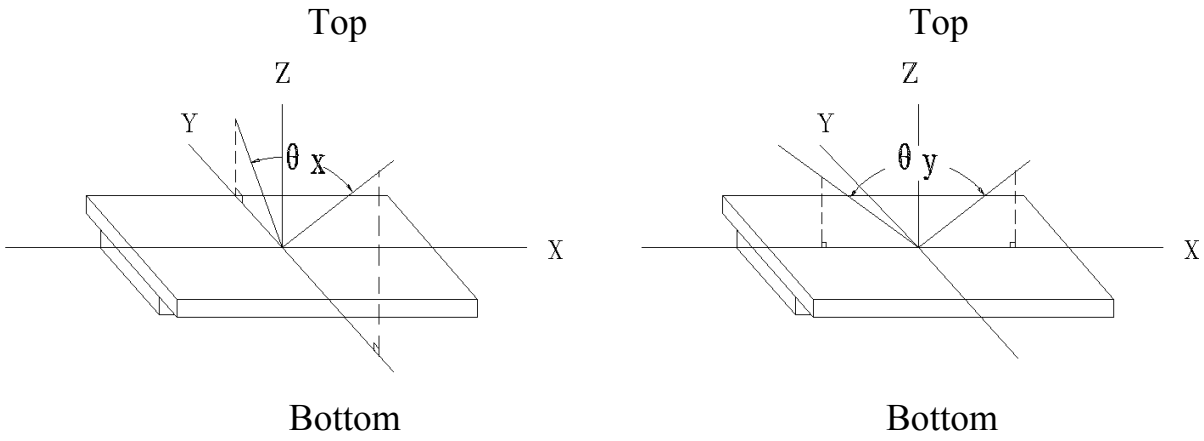
7. Optical Characteristics

7.1 Optical Characteristics V_{LCD}=16.8V Ta=25℃

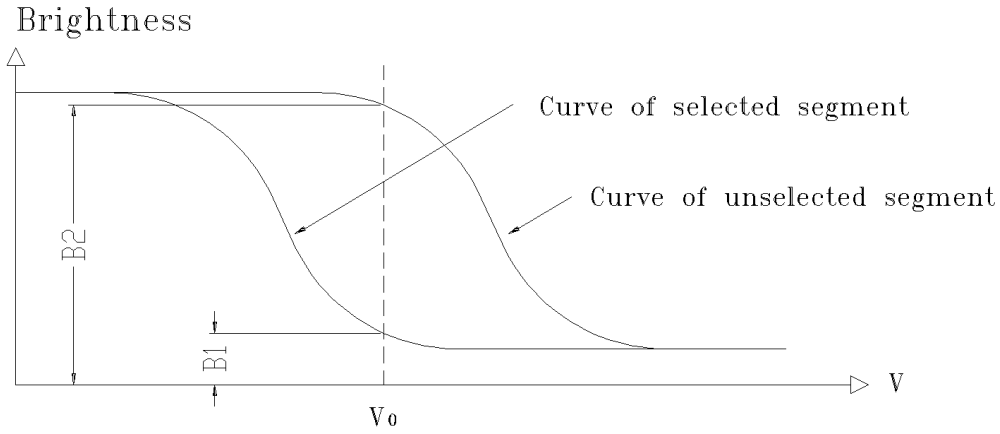
Item		Symbol	Condition		Min.	Typ.	Max.	Unit
Viewing Angle		θ_x	$C_r \geq 2$	$\theta_y=0°$	-45 -- +30			Deg
		θ_y		$\theta_x=0°$	-50 -- +50			
Contrast Ratio		Cr	$\theta_x=0°$ $\theta_y=0°$		-	30	-	
Response Time	Turn on	Ton	$\theta_x=0°$ $\theta_y=0°$		-	-	300	ms
	Turn off	Toff			-	-	200	
Color Of CIE Coord-Inate	Red	x	$\theta_x=0°$ $\theta_y=0°$		-	0.53	-	cd/m^2
		y			-	0.37	-	
	Green	x	$\theta_x=0°$ $\theta_y=0°$		-	0.31	-	cd/m^2
		y			-	0.51	-	
	Blue	x	$\theta_x=0°$ $\theta_y=0°$		-	0.16	-	cd/m^2
		y			-	0.18	-	

7.2 Definition of Optical Characteristics

7.2.1 Definition of Viewing Angle



7.2.2 Definition of Contrast Ratio

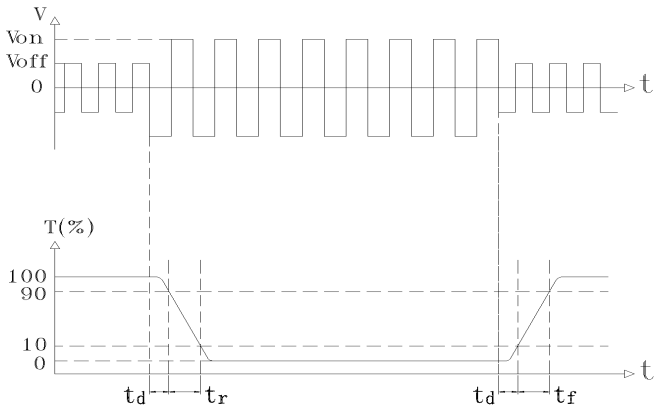


$$\text{Contrast Ratio} = B2/B1 = \frac{\text{unselected state brightness}}{\text{selected state brightness}}$$

Measuring Conditions:

1) Ambient Temperature: 25℃ ; 2) Frame frequency: 70.0Hz

7.2.3 Definition of Response time



Turn on time: $t_{on} = t_d + t_r$ Turn off time: $t_{off} = t_d + t_f$

Measuring Condition:

1) Operating Voltage:16.8V 2) Frame frequency: 70.0Hz

7.3 Brightness Characteristic

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Brightness	Bp	Ta=25℃±3℃ 30-80%RH	100	150	-	cd/m^2
Uniformity	△Bp		70	-	-	%

Note:

1. The data is measured after LEDs are turned on for 5 minutes.
2. Testing conditions LED: V_{LED} = 10.0 V (DC)
   LCD: All dots are on (White color)
3. Brightness in the center of the LCD panel.
4. Definition of Uniformity (△Bp)
   △Bp = Bp (Min.) / Bp (Max.) ×100 (%)
   Bp (Max.) = Maximum brightness in 9 measurement spots
   Bp (Min.) = Minimum brightness in 9 measurement spots

8. Reliability

8.1 Content of Reliability Test Ta=25℃

No.	Test Item	Content of Test	Test condition
1	High Temperature Storage	Endurance test applying the high storage temperature for a long time	80℃±2℃ 240H Restore 4H at 25℃
2	Low Temperature Storage	Endurance test applying the low storage temperature for a long time	-30℃±2℃ 240H Restore 4H at 25℃
3	High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the thermal stress to the element for a long time	70℃±2℃ 240H Restore 4H at 25℃
4	Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time	-20℃±2℃ Restore 4H at 25℃
5	High Temperature /Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time	70℃±2℃ 90%RH 240H Restore 4H at 25℃
6	Temperature Cycle	Endurance test applying the low and high temperature cycle -30℃←→25℃←→80℃←→25℃ 30min 5min 30min 5min ←——————————→ 1 cycle	-30℃/80℃ 10 cycles Restore 4H at 25℃
7	Vibration Test (package state)	Endurance test applying the vibration during transportation	10Hz~150Hz, 100m/s^2, 120min
8	Shock Test (package state)	Endurance test applying the shock during transportation	Half- sine wave, 300m/s^2, 18ms
9	Atmospheric Pressure Test	Endurance test applying the atmospheric pressure during transportation by air	25kPa 16H Restore 2H

8.2 Failure Judgment Criterion

Criterion Item	Test Item No.									Failure Judgment Criterion
	1	2	3	4	5	6	7	8	9	
Basic Specification	√	√	√	√	√	√	√	√	√	Out of the basic Specification
Electrical specification	√	√	√	√	√					Out of the electrical specification
Mechanical Specification							√	√		Out of the mechanical specification
Optical Characteristic	√	√	√	√	√	√			√	Out of the optical specification
Note	For test item refer to 8.1									
Remark	Basic specification = Optical specification + Mechanical specification									

9. Quality Level

Examination or Test	At T_a=25℃ (unless otherwise stated)	Inspection				
		Min.	Max.	Unit	IL	AQL
External Visual Inspection	Under normal illumination and eyesight condition, the distance between eyes and LCD is 25cm.	See Appendix A			II	Major 1.0 Minor 2.5
Display Defects	Under normal illumination and eyesight condition, display on inspection.	See Appendix B			II	Major 1.0 Minor 2.5
Note: Major defects: Open segment or common, Short, Serious damages, Leakage Minor defects: Others Sampling standard conforms to GB2828						

10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol
— Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

— Water
— Ketone
— Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

a. Be sure to ground the body when handling the LCD Modules.
b. Tools required for assembly, such as soldering irons, must be properly ground.
c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃

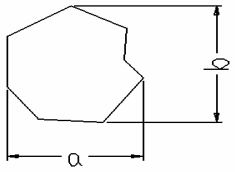
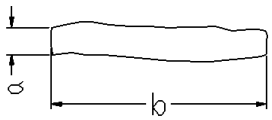
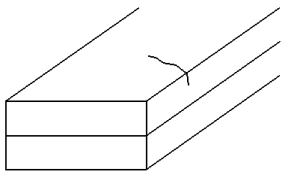
Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

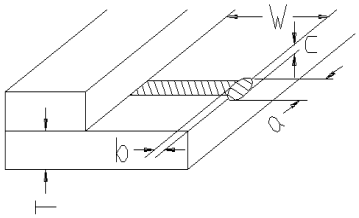
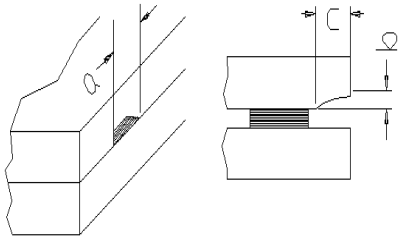
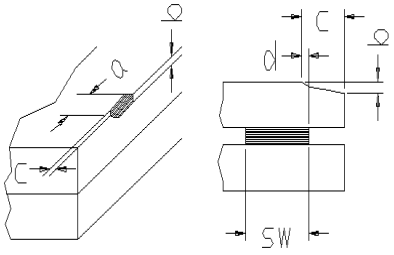
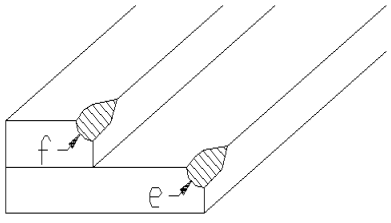
Appendix A

Inspection items and criteria for appearance defects

<table>
<tr><td>Items</td><td>Contents</td><td colspan="3">Criteria</td></tr>
<tr><td>Leakage</td><td></td><td colspan="3">Not permitted</td></tr>
<tr><td>Rainbow</td><td></td><td colspan="3">According to the limit specimen</td></tr>
<tr><td rowspan="3">Polarizer</td><td>Wrong polarizer attachment</td><td colspan="3">Not permitted</td></tr>
<tr><td rowspan="2">Bubble between polarizer and glass</td><td>Not counted</td><td colspan="2">Max. 3 defects allowed</td></tr>
<tr><td>ϕ<0.3mm</td><td colspan="2">0.3mm≤ϕ≤0.5mm</td></tr>
<tr><td>Scratches of polarizer</td><td colspan="3">According to the limit specimen</td></tr>
<tr><td rowspan="3">Black spot (in viewing area)</td><td rowspan="3">  </td><td>Not counted</td><td>Max. 3 spots allowed</td><td rowspan="4">Max. 3 spots (lines) allowed</td></tr>
<tr><td>X<0.2mm</td><td>0.2mm≤X≤0.5mm</td></tr>
<tr><td colspan="2">X=(a+b)/2</td></tr>
<tr><td rowspan="2">Black line (in viewing area)</td><td rowspan="2">  </td><td>Not counted</td><td>Max. 3 lines allowed</td></tr>
<tr><td>a<0.02mm</td><td>0.02mm≤a≤0.05mm
b≤2.0mm</td></tr>
<tr><td>Progressive cracks</td><td>  </td><td colspan="3">Not permitted</td></tr>
</table>

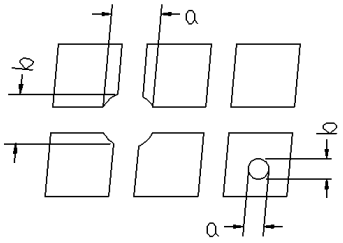
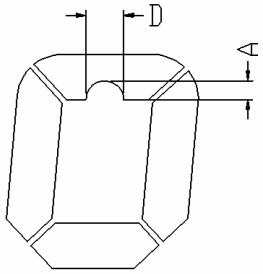
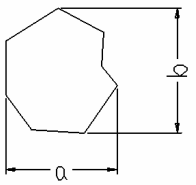
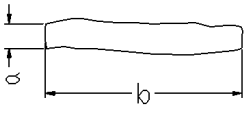
Appendix A

Inspection item and criteria for appearance defects (continued)

<table>
<tr><th>Items</th><th>Contents</th><th colspan="4">Criteria</th></tr>
<tr><td rowspan="4">Glass Cracks</td><td>Cracks on pads
 
</td><td>a
≦3mm
≦2mm</td><td>b
≦W/5
≦W/5</td><td>c
≦T/2
T/2<C<T</td><td>Max. 2 cracks allowed</td><td rowspan="4">Max. 5 cracks allowed</td></tr>
<tr><td>Cracks on contact side
 
</td><td colspan="2">a
≦3mm
≦2mm
C shall be not reach the seal area</td><td>b
≦T/2
T/2<b<T</td><td rowspan="2">Max. 2 cracks allowed</td></tr>
<tr><td>Cracks on non-contact side
 
</td><td colspan="2">a
≦3mm
≦2mm
C≦0.5mm
d≦SW/3</td><td>b
≦T/2
T/2<b<T</td></tr>
<tr><td>Corner cracks
 
</td><td colspan="3">e<2.0mm^2
f<2.0mm^2</td><td>Max. 3 cracks allowed</td></tr>
</table>

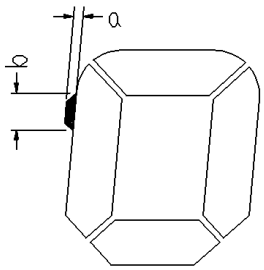
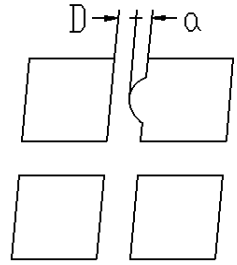
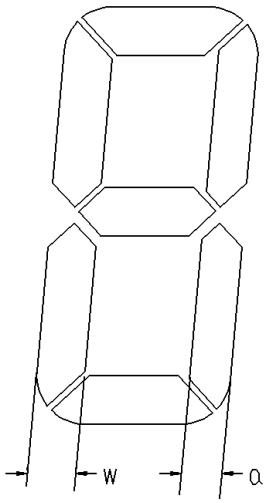
Appendix B

Inspection items and criteria for display defects

<table>
<tr><td colspan="2">Items</td><td>Contents</td><td colspan="3">Criteria</td></tr>
<tr><td colspan="3">Open segment or open common</td><td colspan="3">Not permitted</td></tr>
<tr><td colspan="3">Short</td><td colspan="3">Not permitted</td></tr>
<tr><td colspan="3">Wrong viewing angle</td><td colspan="3">Not permitted</td></tr>
<tr><td colspan="3">Contrast radio uneven</td><td colspan="3">According to the limit specimen</td></tr>
<tr><td colspan="3">Crosstalk</td><td colspan="3">According to the limit specimen</td></tr>
<tr><td rowspan="4">Pin holes and cracks in segment (DOT)</td><td rowspan="2">  </td><td>Not counted</td><td>Max.3 dots allowed</td><td rowspan="4">Max.3 dots allowed</td></tr>
<tr><td>X<0.1mm</td><td>0.1mm≤X≤0.2mm</td></tr>
<tr><td colspan="2">X=(a+b)/2</td></tr>
<tr><td rowspan="2">  </td><td>Not counted</td><td>Max.2 dots allowed</td></tr>
<tr><td>A<0.1mm</td><td>0.1mm≤A≤0.2mm
D<0.25mm</td></tr>
<tr><td rowspan="3">Black spot (in viewing area)</td><td rowspan="3">  </td><td>Not counted</td><td>Max.3 spots allowed</td><td rowspan="4">Max.3 spots (lines) allowed</td></tr>
<tr><td>X<0.1mm</td><td>0.1mm≤X≤0.2mm</td></tr>
<tr><td colspan="2">X=(a+b)/2</td></tr>
<tr><td rowspan="2">Black line (in viewing area)</td><td rowspan="2">  </td><td>Not counted</td><td>Max.3 lines allowed</td></tr>
<tr><td>a<0.02mm</td><td>0.02mm≤a≤0.05mm
b≤0.5mm</td></tr>
</table>

Appendix B

Inspection items and criteria for display defects (continued)

<table>
<tr><td>Items</td><td>Content</td><td colspan="3">Criteria</td></tr>
<tr><td rowspan="4">Transfor-
mation
of segment</td><td rowspan="2">  </td><td>Not counted</td><td>Max. 2 defects allowed</td><td rowspan="3">Max.3
defects
allowed</td></tr>
<tr><td>x＜0.1mm</td><td>0.1mm≤x≤0.2mm</td></tr>
<tr><td colspan="2">x=(a+b)/2</td></tr>
<tr><td>  </td><td>Not counted</td><td>Max. 1 defects allowed</td><td rowspan="2">Max.3
defects
allowed</td></tr>
<tr><td>a＜0.1mm</td><td>0.1mm≤a≤0.2mm
D>0</td></tr>
<tr><td>  </td><td colspan="3">Max.2 defects allowed
0.8W≤a≤1.2W

a=measured value of width
W=nominal value of width</td></tr>
</table>