

# TLP250F(INV)

Transistor Inverter  
Inverters for Air Conditioner  
IGBT Gate Drive  
Power MOS FET Gate Drive

The TOSHIBA TLP250F(INV) consists of an infrared emitting diode and a integrated photodetector.

This unit is 8-lead DIP.

TLP250F(INV) is suitable for gate driving circuit of IGBT or power MOS FET.

- Input Threshold Current :  $I_F=5\text{mA}(\text{max})$
- Supply Current :  $11\text{mA}(\text{max})$
- Supply Voltage : 10 to 35V
- Output Current :  $\pm 1.5\text{A}(\text{max})$
- Switching Time( $t_{pLH}/t_{pHL}$ ) :  $0.5\mu\text{s}(\text{max})$
- Isolation Voltage :  $2500\text{Vrms}(\text{min})$
- UL-recognized : UL 1577, File No.E67349
- cUL-recognized : CSA Component Acceptance Service  
No.5A File No.E67349
- VDE-Approved : EN 60747-5-5 (Note 1)

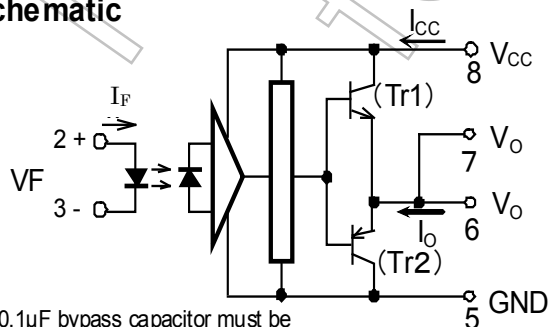
Note 1:When a VDE approved type is needed, please designate the **Option(D4)**.

- Creepage Distance : 8.0mm(min)
- Clearance : 8.0mm(min)

## Truth Table

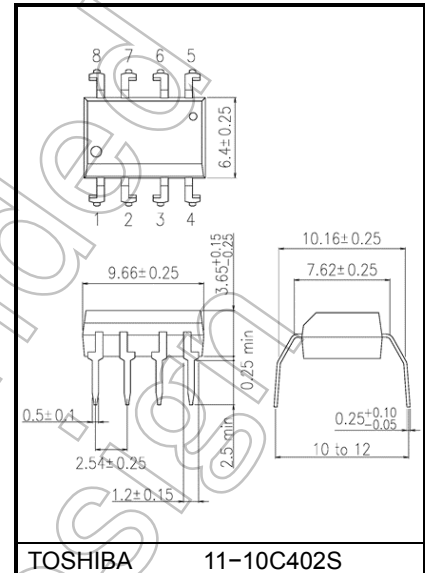
		Tr 1	Tr 2
Input LED	ON	ON	OFF
	OFF	OFF	ON

## Schematic



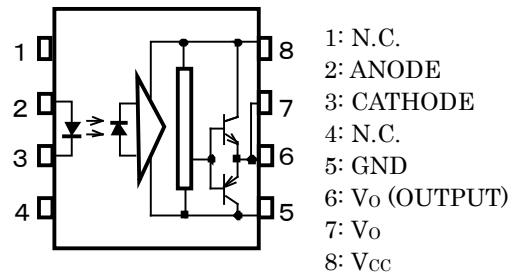
Note: A 0.1 $\mu\text{F}$  bypass capacitor must be  
Connected between pin 8 and 5(See Note 5).

Unit : mm



Weight: 0.54 g (typ.)

## Pin Configuration (top view)



Start of commercial production  
1998-07

## Absolute Maximum Ratings (Ta=25°C)

Characteristics				Symbol	Rating	Unit
LED	Forward Current			I <sub>F</sub>	20	mA
	Forward Current Derating (Ta≥70°C)			ΔI <sub>F</sub> / ΔTa	-0.36	mA / °C
	Peak Transient Forward Current (Note 1)			I <sub>FPT</sub>	1	A
	Reverse Voltage			V <sub>R</sub>	5	V
	Diode power dissipation			P <sub>D</sub>	40	mW
	Diode power dissipation derating (Ta≥70 °C)			ΔP <sub>D</sub> /°C	-0.72	mW/°C
	Junction Temperature			T <sub>j</sub>	125	°C
Detector	“H” Peak Output Current	PW ≤2.5μs , f≤15 kHz	(Note 2)	I <sub>OPH</sub>	-1.5	A
		PW≤1.0μs , f≤15 kHz			-2.0	
	“L” Peak Output Current	PW≤2.5μs , f≤15 kHz		I <sub>OPL</sub>	+1.5	A
		PW ≤1.0μs , f≤15 kHz			+2.0	
	Output Voltage		(Ta≤70°C)	V <sub>O</sub>	35	V
			(Ta≤85°C)		24	
	Supply Voltage		(Ta≤70°C)	V <sub>CC</sub>	35	V
			(Ta≤85°C)		24	
	Output Voltage Derating (Ta≥70°C)			ΔV <sub>O</sub> / ΔTa	-0.73	V / °C
	Supply Voltage Derating (Ta≥70°C)			ΔV <sub>CC</sub> / ΔTa	-0.73	V / °C
	Output Power dissipation			P <sub>O</sub>	800	mW
	Output Power dissipation derating (Ta ≥70°C )			ΔP <sub>O</sub> / °C	-14.5	mW/°C
	Junction Temperature			T <sub>j</sub>	125	°C
Operating Frequency (Note 3)			f	25	kHz	
Operating Temperature Range			T <sub>opr</sub>	-20 to 85	°C	
Storage Temperature Range			T <sub>stg</sub>	-55 to 125	°C	
Lead Soldering Temperature(10 s)			T <sub>sol</sub>	260	°C	
Isolation Voltage (AC,60 s., R.H. ≤60 %) (Note 4)			BV <sub>s</sub>	2500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) : Pulse width PW≤1 μs,300 pps

(Note 2) : Exponential Waveform

(Note 3) : Exponential Waveform  $I_{OPH} \leq -1.0 \text{ A } (\leq 2.5 \mu s)$  ,  $I_{OPL} \leq +1.0 \text{ A } (\leq 2.5 \mu s)$

(Note 4) : Device considered a two terminal device : pins 1,2,3 and 4 shorted together and pins 5,6,7 and 8 shorted together.

(Note 5) : A ceramic capacitor(0.1 μF) should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier.Failure to provide the bypassing may impair the switching property.The total lead length between capacitor and coupler should not exceed 1cm.

## Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Input Current, ON	$I_{F(ON)}$	7	8	10	mA
Input Voltage, OFF	$V_{F(OFF)}$	0	—	0.8	V
Supply Voltage	$V_{CC}$	15	—	30	V
Peak Output Current	$I_{OPH} / I_{OPL}$	—	—	±0.5	A
Operating Temperature	$T_{opr}$	-20	25	70	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Note 6: Input signal rise time(fall time)<0.5μs.

## Electrical Characteristics (Ta = -20~70°C, Unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Condition		Min	Typ.	Max	Unit
Input Forward Voltage		V <sub>F</sub>	—	I <sub>F</sub> = 10 mA, T <sub>a</sub> = 25 °C		—	1.6	1.8	V
Temperature Coefficient of Forward Voltage		ΔV <sub>F</sub> / ΔT <sub>a</sub>	—	I <sub>F</sub> = 10 mA		—	-2.0	—	mV / °C
Input Reverse Current		I <sub>R</sub>	—	V <sub>R</sub> = 5 V, T <sub>a</sub> = 25 °C		—	—	10	μA
Input Capacitance		C <sub>T</sub>	—	V = 0 V, f = 1 MHz, T <sub>a</sub> = 25 °C		—	45	250	pF
Output Current	“H” Level	I <sub>OPH</sub>	2	V <sub>CC</sub> = 30 V (*1)	I <sub>F</sub> = 10 mA V <sub>8-6</sub> = 4 V	-1.0	-1.5	—	A
	“L” Level	I <sub>OPL</sub>	1		I <sub>F</sub> = 0 mA V <sub>6-5</sub> = 2.5 V	1.0	2	—	
Output Voltage	“H” Level	V <sub>OH</sub>	3	V <sub>CC1</sub> = +15 V V <sub>EE1</sub> = -15 V R <sub>L</sub> = 200 Ω, I <sub>F</sub> = 5 mA		11	12.8	—	V
	“L” Level	V <sub>OL</sub>	4	V <sub>CC1</sub> = +15 V V <sub>EE1</sub> = -15 V R <sub>L</sub> = 200 Ω, V <sub>F</sub> = 0.8 V		—	-14.2	-12.5	
Supply Current	“H” Level	I <sub>CCH</sub>	—	V <sub>CC</sub> = 30 V	I <sub>F</sub> = 10 mA T <sub>a</sub> = 25 °C	—	7	—	mA
					I <sub>F</sub> = 10 mA	—	—	11	
	“L” Level	I <sub>CCL</sub>			I <sub>F</sub> = 0 mA T <sub>a</sub> = 25 °C	—	7.5	—	mA
					I <sub>F</sub> = 0 mA	—	—	11	
Threshold Input Current	L→H	I <sub>FLH</sub>	—	V <sub>CC1</sub> = +15 V V <sub>EE1</sub> = -15 V R <sub>L</sub> = 200 Ω, V <sub>O</sub> > 0 V		—	1.2	5	mA
Threshold Input Voltage	H→L	V <sub>FHL</sub>		V <sub>CC1</sub> = +15 V V <sub>EE1</sub> = -15 V R <sub>L</sub> = 200 Ω, V <sub>O</sub> < 0 V		0.8	—	—	V
Supply Voltage		V <sub>CC</sub>	—	—		10	—	35	V
Capacitance (Input-Output)		C <sub>S</sub>	—	V <sub>S</sub> = 0 V, f = 1 MHz, T <sub>a</sub> = 25 °C		—	1.0	2.0	pF
Resistance (Input-Output)		R <sub>S</sub>	—	V <sub>S</sub> = 500 V, T <sub>a</sub> = 25 °C R.H.≤ 60 %		1×10 <sup>12</sup>	10 <sup>14</sup>	—	Ω

(\*) : All typical values are at  $T_a = 25^\circ\text{C}$

(\*1) : Duration of IO time ≤ 50μs

Switching Characteristics (Ta = -20~70°C, Unless otherwise specified)

Characteristics		Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Propagation Delay Time	L→H	$t_{pLH}$	5	$I_F = 8\text{ mA}$ , $V_{CC} = 15\text{ V}$ $R_L = 20\ \Omega$ , $C_L = 10\text{ nF}$	0.05	0.15	0.5	$\mu\text{s}$
	H→L	$t_{pHL}$			0.05	0.15	0.5	
Switching Time Dispersion between ON and OFF		$ t_{pHL}-t_{pLH} $			—	—	0.45	
Common Mode Transient Immunity at High Level Output		$CM_H$	6	$V_{CM} = 1000\text{ V}$ , $I_F = 8\text{ mA}$ $V_{CC} = 30\text{ V}$ , $T_a = 25\text{ }^\circ\text{C}$	-15000	—	—	$\text{V}/\mu\text{s}$
Common Mode Transient Immunity at Low Level Output		$CM_L$		$V_{CM} = 1000\text{ V}$ , $I_F = 0\text{ mA}$ $V_{CC} = 30\text{ V}$ , $T_a = 25\text{ }^\circ\text{C}$	15000	—	—	$\text{V}/\mu\text{s}$

Note : All typical values are at Ta=25°C

Fig.1  $I_{OPL}$  Test Circuit

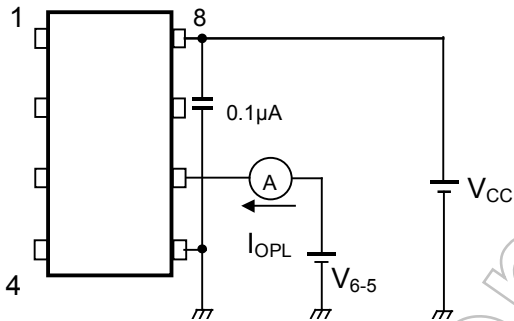


Fig.2  $I_{OPH}$  Test Circuit

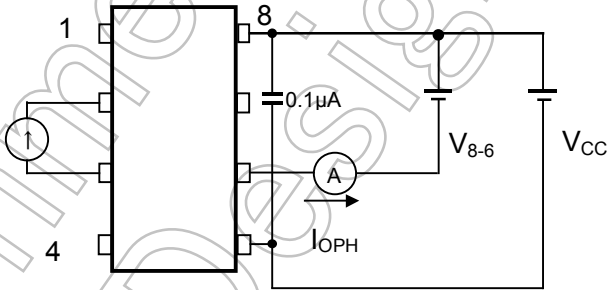


Fig.3  $V_{OH}$  Test Circuit

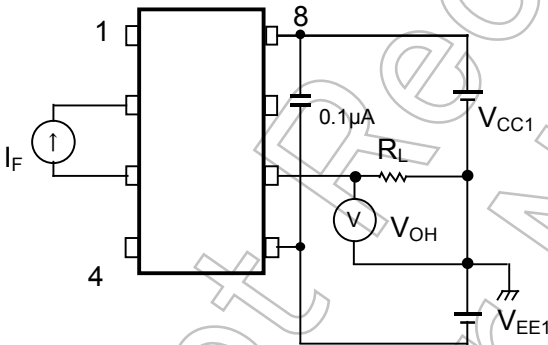
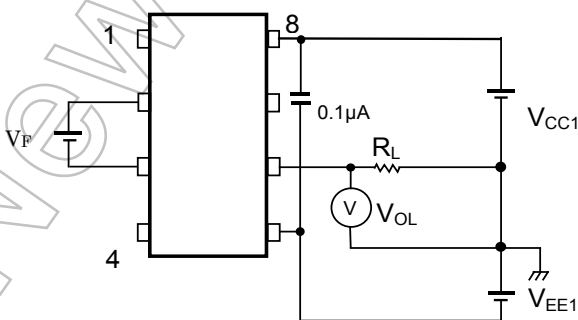
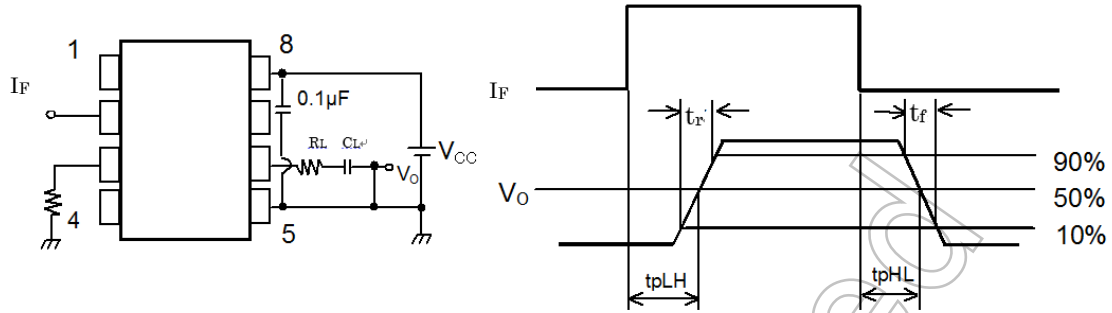


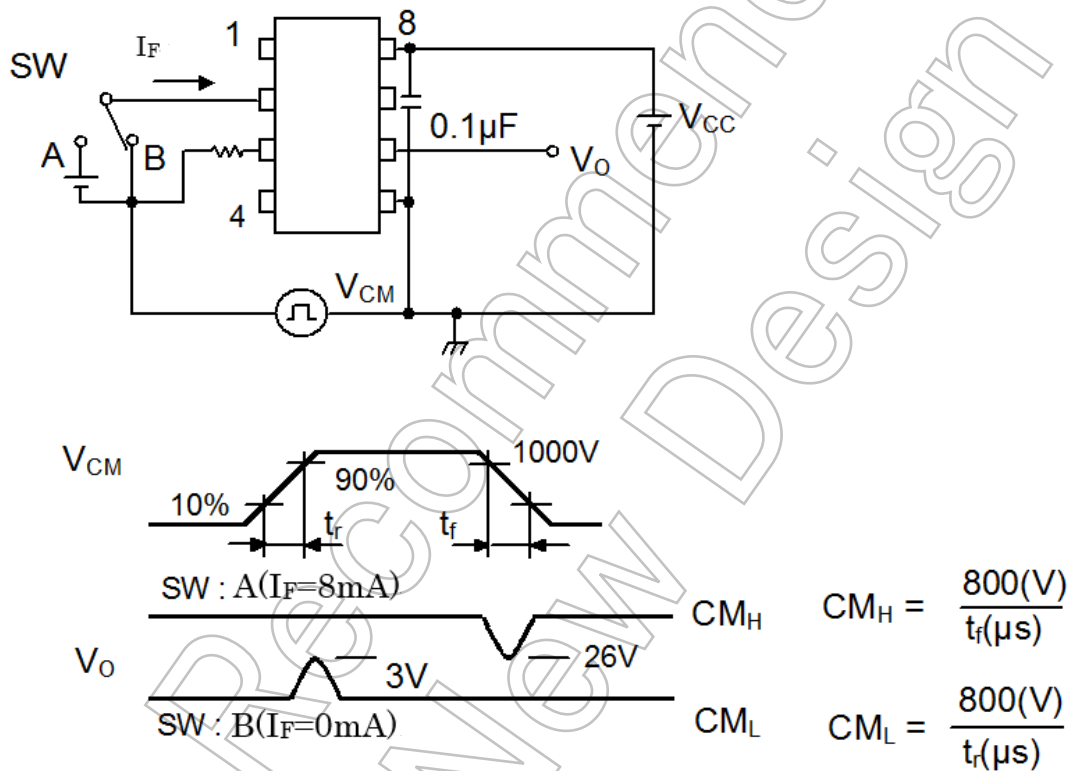
Fig.4  $V_{OL}$  Test Circuit



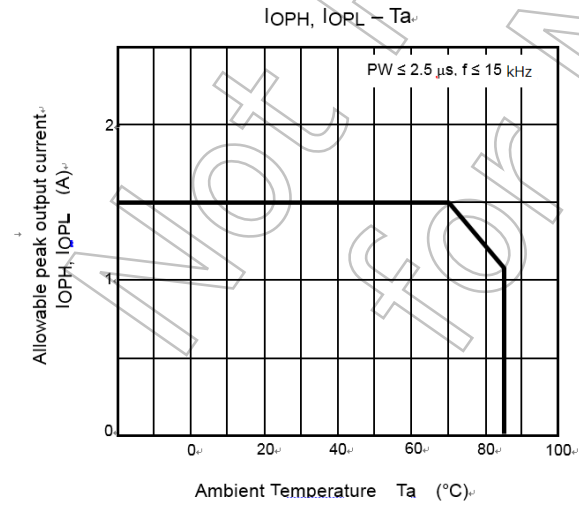
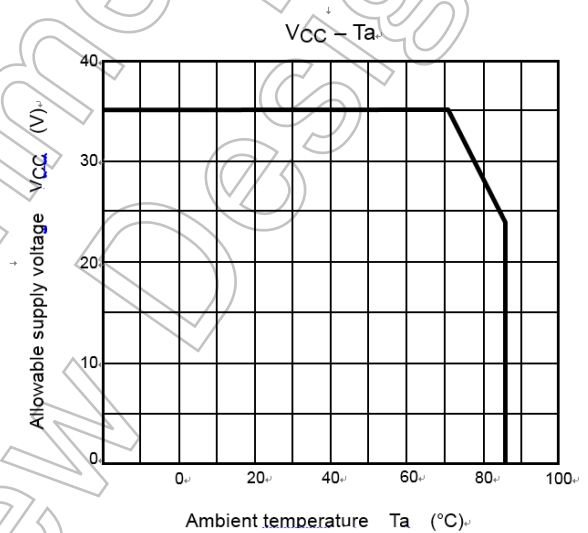
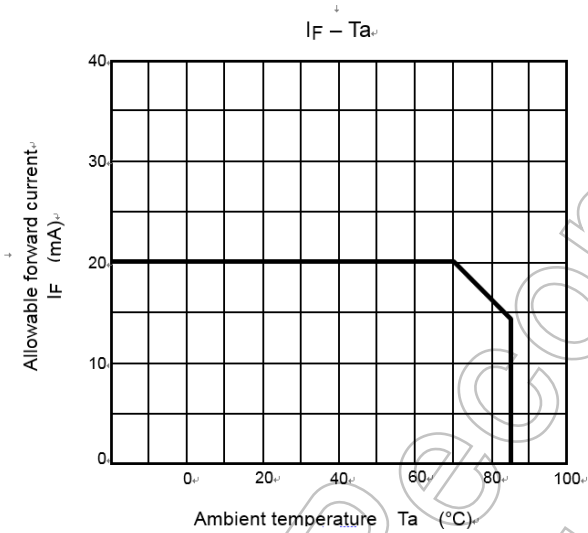
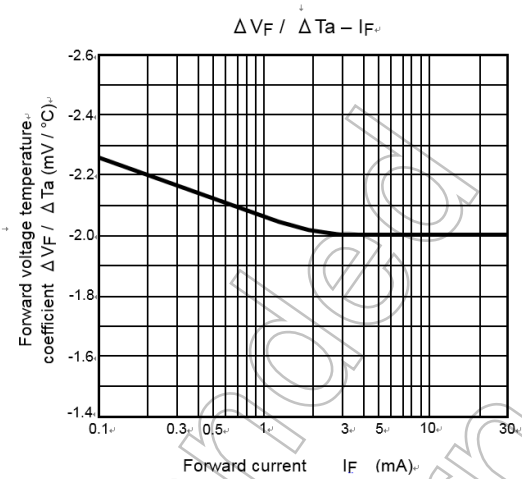
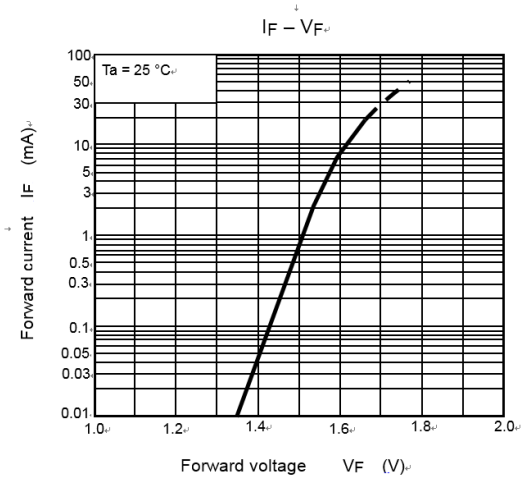
**Fig.5**  $t_{pLH}$ ,  $t_{pHL}$ ,  $t_r$ ,  $t_f$  Test Circuit



**Fig.6**  $CM_H$ ,  $CM_L$  Test Circuit



$CM_L(CM_H)$  is the maximum rate of rise(fall) of the common mode voltage that can be sustained with the output voltage in the low(high)state.



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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