

isc N-Channel MOSFET Transistor

TK62N60W5

| RDS(• Enhand Vth = • 100% a • Minimu perform • DESCF • Switchi | ain-source on-resistance: on) ≤45mΩ. cement mode: =3 to 4.5V (Vos = 10 V, Io=3.1mA) avalanche tested im Lot-to-Lot variations for robust de nance and reliable operation RITION Ing Voltage Regulators | G(1) G(1) G(1) S(3) pin 1.Gate 2.Drain 3.Source 1 2 3 TO-247 package | | | |
|--|--|---|------|---|--|
| SYMBOL | PARAMETER | VALUE | UNIT | | |
| V _{DSS} | Drain-Source Voltage | 600 | v | | |
| V_{GS} | Gate-Source Voltage | ±30 | V | | |
| I _D | Drain Current-Continuous | 61.8 | А | | |
| I _{DM} | Drain Current-Single Pulsed | 247 | A | J D G | |
| P _D | Total Dissipation @T _C =25℃ | 400 | w | DIM MIN MAX | |
| Tj | Max. Operating Junction Temperature | 150 | °C | A 19.80 20.20 B 15.40 15.80 C 4.90 5.10 | |
| Tstg | Storage Temperature | -55~150 | °C | D 0.90 1.10 E 1.40 1.60 F 1.90 2.10 | |
| THERMAL CHARACTERISTICS | | | | G10.8011.00H2.402.60 | |
| SYMBOL | PARAMETER | МАХ | UNIT | J 0.50 0.70 K 19.50 20.50 P 3.90 4.10 | |
| Rth(ch-c) | Channel-to-case thermal resistance | 0.313 | °C/W | Q 3. 30 3. 50 U 5. 20 5. 40 V 2. 90 3. 10 | |



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ELECTRICAL CHARACTERISTICS

$T_{\rm C}\text{=}25^\circ\!\!\mathrm{C}$ unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN | ТҮР | МАХ | UNIT |
|----------------------|--------------------------------|---|-----|-----|-----|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V; I _D =10mA | 600 | | | v |
| V _{GS} (th) | Gate Threshold Voltage | V _{DS} =10V; I _D =3.1mA | 3 | | 4.5 | v |
| R _{DS(on)} | Drain-Source On-Resistance | V _{GS} =10V; I _D =30.9A | | | 45 | mΩ |
| lgss | Gate-Source Leakage Current | V _{GS} = ±30V;V _{DS} = 0V | | | ±1 | μA |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =600V; V _{GS} = 0V | | | 100 | uA |
| V _{SDF} | Diode forward voltage | I _{DR} =61.8A, V _{GS} = 0 V | | | 1.7 | V |



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