

INCHANGE SEMICONDUCTOR

iscN-Channel MOSFET Transistor

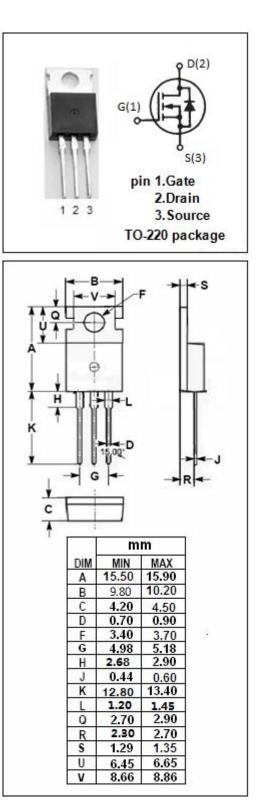
TK4P55D

ILAIO	RES							
Low drain-source on-resistance:								
RDS(DN) = 1.88Ω (MAX)							
 Enhance 	ement mode:							
Vth =	Vth = 2.4 to 4.4V (Vbs = 10 V, Ib=1.0mA)							
• 100% a	100% avalanche tested							
 Minimu 	m Lot-to-Lot variations for robust de	evice						
perform	ance and reliable operation	2						
• DESCRITION								
 Switching 	Switching Voltage Regulators							
• ABSOL	• ABSOLUTE MAXIMUM RATINGS(Ta=25°C)							
	DADAMETED	VALUE						
SYMBOL	PARAMETER	VALUE	UNIT					
VDSS	Drain-Source Voltage	550						
V _{DSS}	Drain-Source Voltage	550	V					
V _{DSS} V _{GS}	Drain-Source Voltage Gate-Source Voltage	550 ±30	V					
V _{DSS} V _{GS}	Drain-Source Voltage Gate-Source Voltage Drain Current-Continuous	550 ±30 4	V V A					
V _{DSS} V _{GS} I _D I _{DM}	Drain-Source Voltage Gate-Source Voltage Drain Current-Continuous Drain Current-Single Pulsed	550 ±30 4 16	V V A A					

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
Rth(ch-c)	Channel-to-case thermal resistance	1.56	°C/W

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ELECTRICAL CHARACTERISTICS

$T_{\texttt{C}}\text{=}25^{\circ}\!\!\!\mathbb{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	ТҮР	МАХ	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D = 10mA	550			V
V _{GS} (th)	Gate Threshold Voltage	V _{DS} = 10V; I _D =1.0mA	2.4		4.4	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =10V; I _D =2A			1.88	Ω
lgss	Gate-Source Leakage Current	V _{GS} = ±30V;V _{DS} = 0V			±1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =550V; V _{GS} = 0V			10	μA
V _{SDF}	Diode forward voltage	I _{DR} =4A, V _{GS} = 0 V			1.7	V

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