BOURNS®

- Designed for Complementary Use with the TIP42 Series
- 65 W at 25°C Case Temperature
- 6 A Continuous Collector Current
- 10 A Peak Collector Current
- Customer-Specified Selections Available

B 1 2 E 3

Pin 2 is in electrical contact with the mounting base.

MDTRACA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	TIP41		80	
Collector-base voltage ($I_E = 0$)	TIP41A	V	100	V
	TIP41B	V _{CBO}	120	٧
	TIP41C		140	
	TIP41		40	
Callegtor emitter voltage (I = 0)	TIP41A	V	60	V
Collector-emitter voltage (I _B = 0)	TIP41B	V _{CEO}	80	
	TIP41C		100	
Emitter-base voltage			5	V
Continuous collector current			6	Α
Peak collector current (see Note 1)	I _{CM}	10	Α	
Continuous base current			3	Α
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			65	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			2	W
Unclamped inductive load energy (see Note 4)			62.5	mJ
Operating junction temperature range			-65 to +150	°C
Storage temperature range			-65 to +150	°C
Lead temperature 3.2 mm from case for 10 seconds			250	°C

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$.

- 2. Derate linearly to 150°C case temperature at the rate of 0.52 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = 0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_S = 0.1 Ω , V_{CC} = 20 V.



electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C = 30 mA (see Note 5)	I _B = 0	TIP41 TIP41A TIP41B TIP41C	40 60 80 100			V
I _{CES}	Collector-emitter cut-off current	$V_{CE} = 80 \text{ V}$ $V_{CE} = 100 \text{ V}$ $V_{CE} = 120 \text{ V}$ $V_{CE} = 140 \text{ V}$	$V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$ $V_{BE} = 0$	TIP41 TIP41A TIP41B TIP41C			0.4 0.4 0.4 0.4	mA
I _{CEO}	Collector cut-off current	$V_{CE} = 30 \text{ V}$ $V_{CE} = 60 \text{ V}$	I _B = 0 I _B = 0	TIP41/41A TIP41B/41C			0.7 0.7	mA
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V	I _C = 0				1	mA
h _{FE}	Forward current transfer ratio	$V_{CE} = 4 V$ $V_{CE} = 4 V$	$I_{\rm C} = 0.3 {\rm A}$ $I_{\rm C} = 3 {\rm A}$	(see Notes 5 and 6)	30 15		75	
V _{CE(sat)}	Collector-emitter saturation voltage	I _B = 0.6 A	I _C = 6A	(see Notes 5 and 6)			1.5	V
V_{BE}	Base-emitter voltage	V _{CE} = 4 V	I _C = 6 A	(see Notes 5 and 6)			2	V
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 0.5 A	f = 1 kHz	20			
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 0.5 A	f = 1 MHz	3			

NOTES: 5. These parameters must be measured using pulse techniques, $t_p = 300 \mu s$, duty cycle $\leq 2\%$.

thermal characteristics

PARAMETER			TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1.92	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			62.5	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = 6 A	$I_{B(on)} = 0.6 A$	I _{B(off)} = -0.6 A		0.6		μs
t _{off}	Turn-off time	$V_{BE(off)} = -4 V$	$R_L = 5 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		1		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN vs **COLLECTOR CURRENT** TCS633AD 1000 V_{CE} = 4 V $T_c = 25^{\circ}C$ $t_p = 300 \mu s$, duty cycle < 2%h_{FE} - DC Current Gain 100 10 1.0 0.01 0.1 1.0 10 I_c - Collector Current - A

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE vs BASE CURRENT TCS633AE

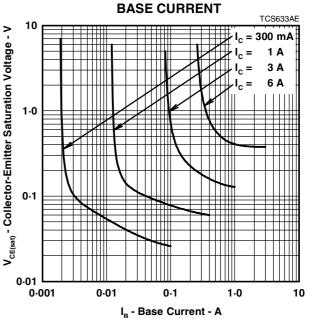


Figure 2.

BASE-EMITTER VOLTAGE

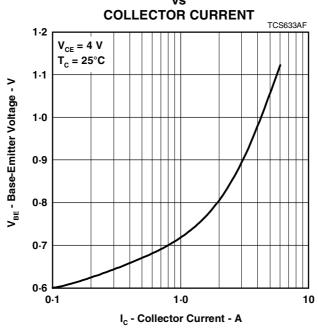
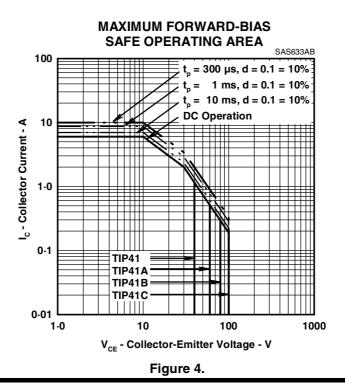


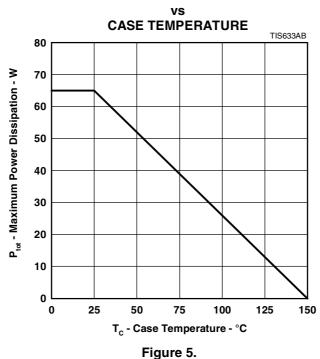
Figure 3.

MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION



PRODUCT INFORMATION