

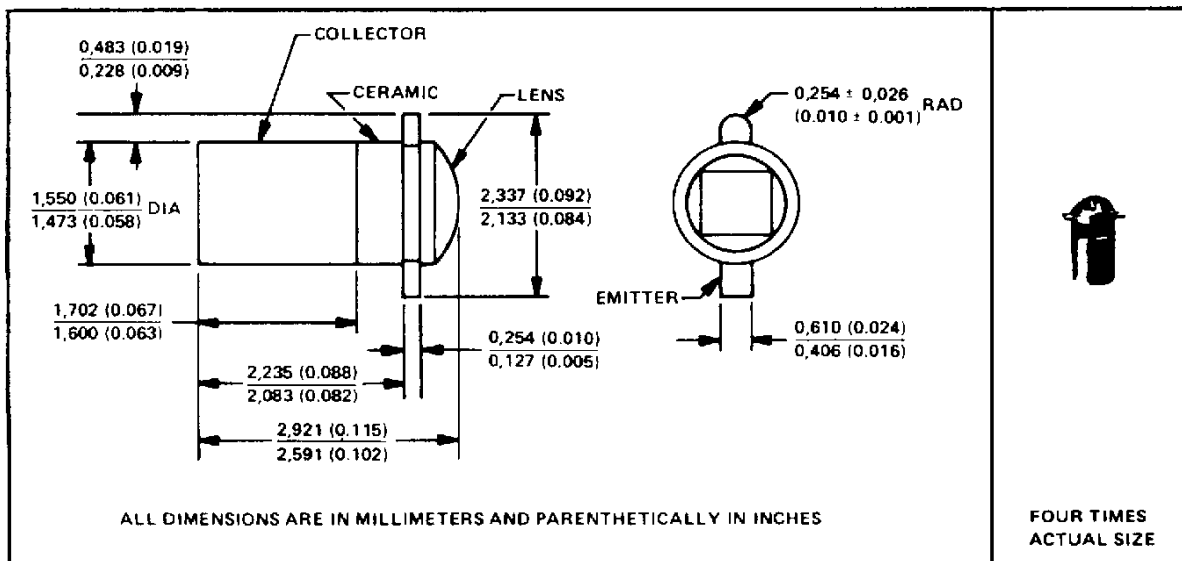
TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619 N-P-N PLANAR SILICON PHOTOTRANSISTORS

SODS026A D1971, NOVEMBER 1974—REVISED SEPTEMBER 1989

DESIGNED FOR HIGH-DENSITY READ OUT

- Hermetically-Sealed Pill Package
- Recommended for Application in Character Recognition, Tape and Card Readers, Velocity Indicators, and Encoders
- Unique Package Design Allows for Assembly into Printed Circuit Boards
- Spectrally and Mechanically Compatible with TIL23 thru TIL25
- Saturation Level Directly Compatible with Most TTL
- TIL604HR2[†] Includes High-Reliability Processing and Lot Acceptance (See TIL604HR2 for Summary of Processing)

mechanical data



[†] All electrical and mechanical specifications for the TIL24 also apply for TIL24HR2

PRODUCTION DATA documents contain information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS
INSTRUMENTS

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 1989, Texas Instruments Incorporated

TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619
N-P-N PLANAR SILICON PHOTOTRANSISTORS

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

Collector-emitter voltage	50 V
Emitter-collector voltage	7 V
Continuous device dissipation at (or below) 25°C case temperature (see Note 1)	50 mW
Operating case temperature range	-65°C to 125°C
Storage temperature range	-65°C to 150°C
Soldering temperature (10 seconds)	240°C

electrical characteristics at 25°C case temperature (unless otherwise noted)

PARAMETER	TEST CONDITIONS	TYPE	MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$ Collector-emitter breakdown voltage	$I_C = 100 \mu A$, $E_e = 0$	All	50			V
$V_{(BR)ECO}$ Emitter-collector breakdown voltage	$I_E = 100 \mu A$, $E_e = 0$	All	7			V
I_D Dark current	$V_{CE} = 30 V$, $E_e = 0$	All			25	nA
	$V_{CE} = 30 V$, $E_e = 0$, $T_C = 100^\circ C$	All		3		μA
I_L Light current	$V_{CE} = 5 V$, $E_e = 20 \text{ mW/cm}^2$, See Note 2	TIL601	0.5		3	mA
		TIL602	2		5	
		TIL603	4		8	
		TIL604	7			
		LS600	0.8			
		LS602	0.5			
		LS611	0.5	1	2	
		LS612	1	2	3	
		LS613	2	3	4	
		LS614	3	4	5	
		LS615	4	5	6	
		LS616	5	6	7	
		LS617	6	7	8	
		LS618	7	8	9	
		LS619	8	9		
$V_{CE(sat)}$ Collector-emitter saturation voltage	$I_C = 0.4 \text{ mA}$, $E_e = 20 \text{ mW/cm}^2$, See Note 2	All		0.15		V

NOTES: 1. Derate linearly to 125°C at the rate of 0.5 mW/°C.

2. Irradiance (E_e) is the radiant power per unit area incident upon a surface. For this measurement, the source is an unfiltered tungsten linear-filament lamp operating at a color temperature of 2870 K.

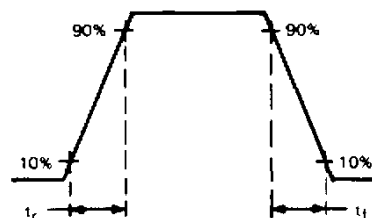
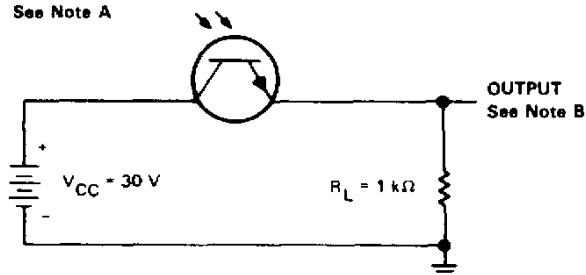
switching characteristics at 25°C case temperature

PARAMETER	TEST CONDITIONS	TYP	UNIT
t_r Rise time	$V_{CC} = 30 V$, $I_L = 800 \mu A$,	8	μs
t_f Fall time	$R_L = 1 \text{ k}\Omega$, See Figure 1	6	

TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619 N-P-N PLANAR SILICON PHOTOTRANSISTORS

PARAMETER MEASUREMENT INFORMATION

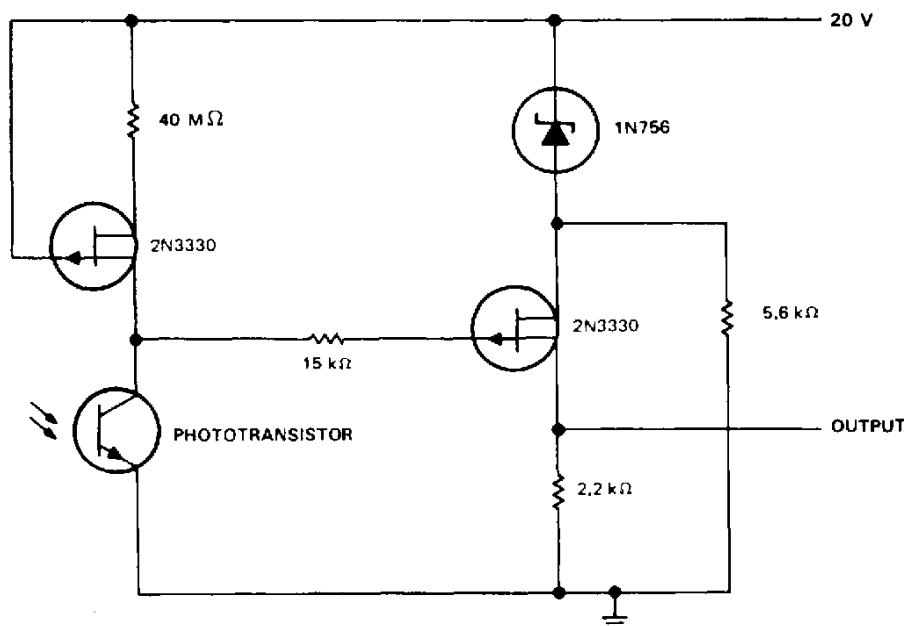
See Note A



- NOTES: A. Input irradiance is supplied by a pulsed gallium arsenide infrared emitter with rise and fall times of less than 50 ns. Incident irradiation is adjusted for $I_L = 800 \mu A$.
B. Output waveform is monitored on an oscilloscope with the following characteristics: $t_r \leq 25 \text{ ns}$, $R_{in} \geq 1 \text{ M}\Omega$, $C_{in} \leq 20 \text{ pF}$.

FIGURE 1

TYPICAL APPLICATION DATA



TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619
N·P·N PLANAR SILICON PHOTOTRANSISTORS

TYPICAL APPLICATION DATA

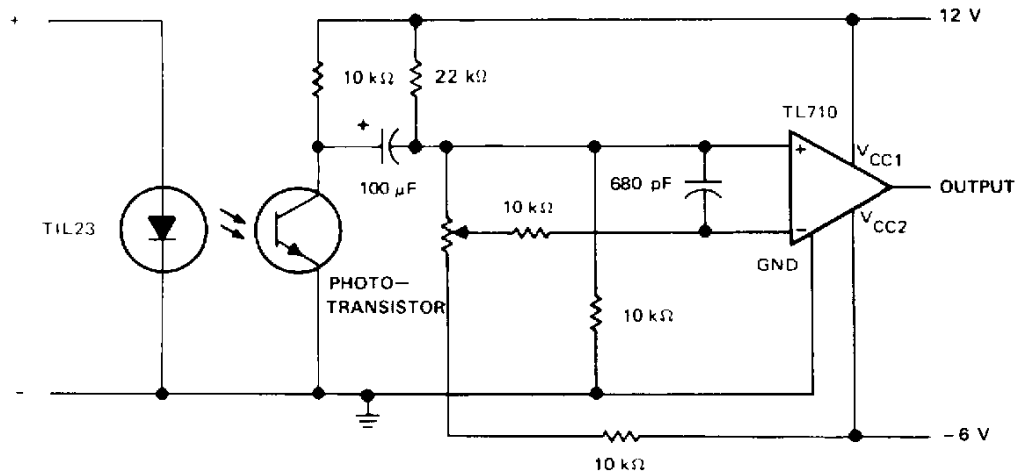


FIGURE 3. OPTICALLY COUPLED AMPLIFIER

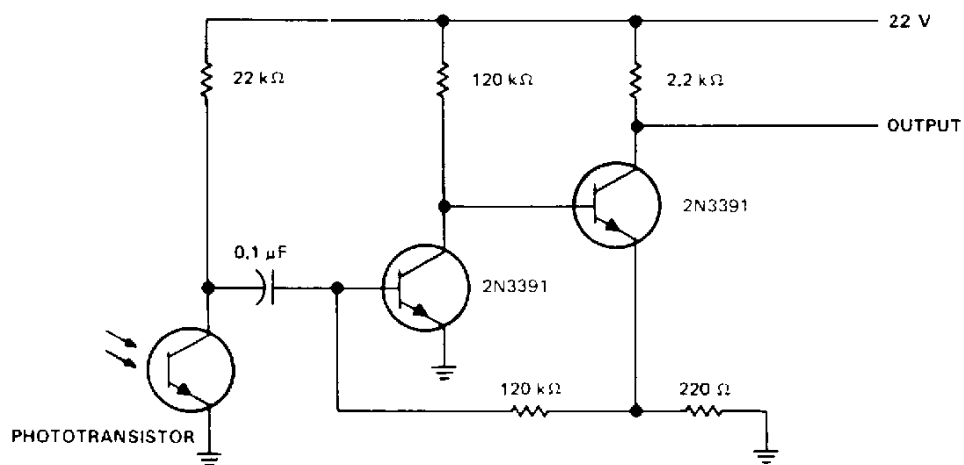


FIGURE 4. LIGHT PULSE DETECTOR

TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619 N-P-N PLANAR SILICON PHOTOTRANSISTORS

TYPICAL CHARACTERISTICS

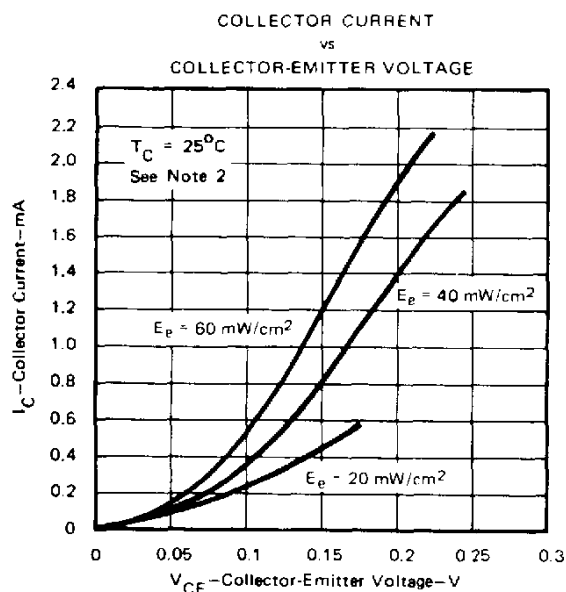


FIGURE 5

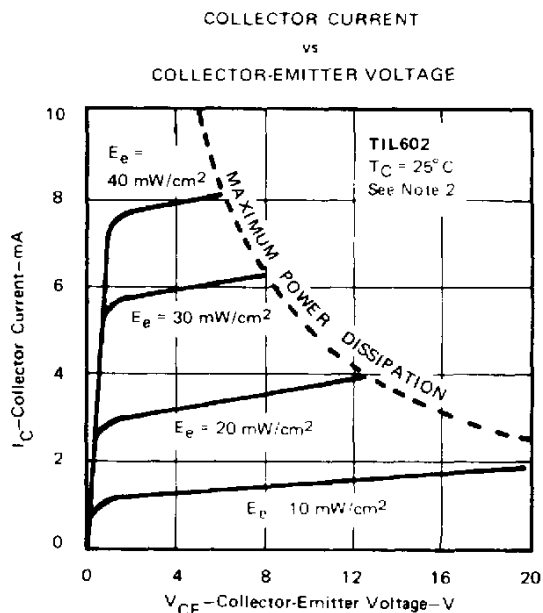


FIGURE 6

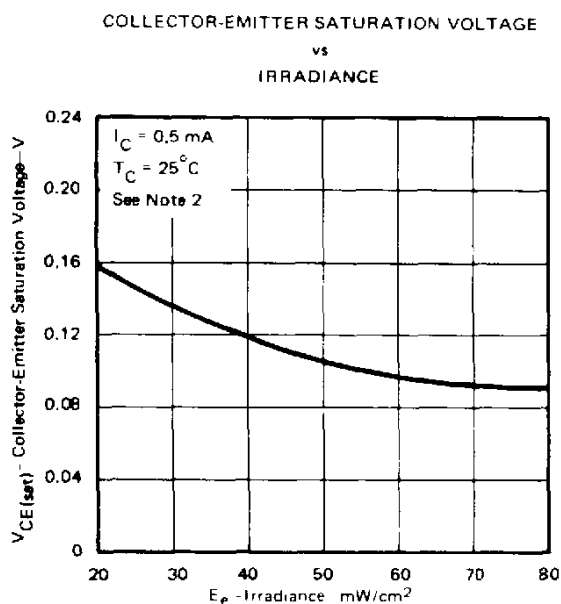


FIGURE 7

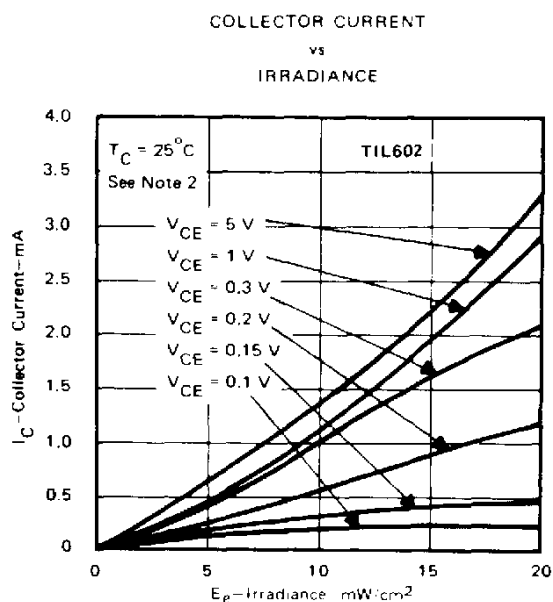
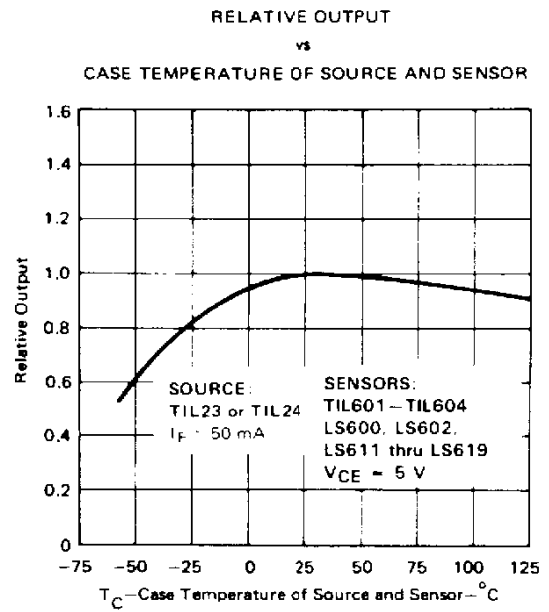
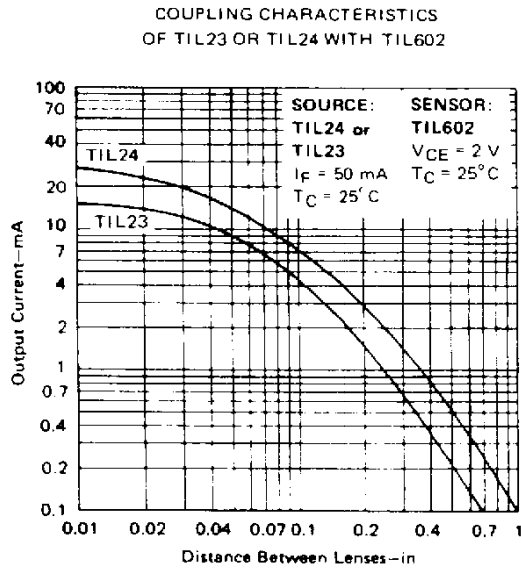
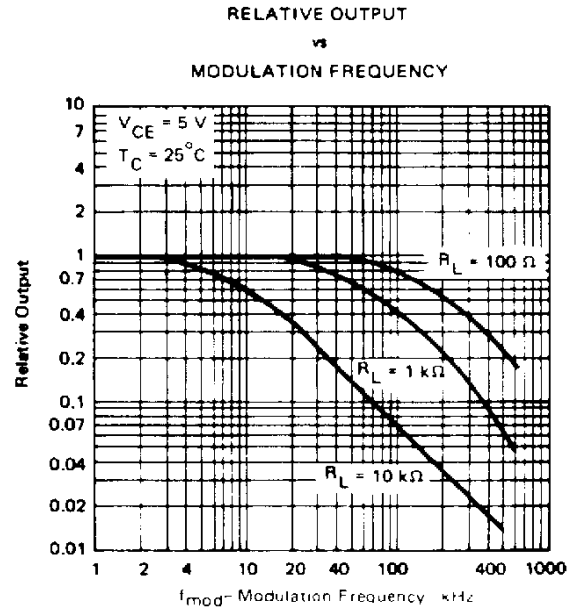
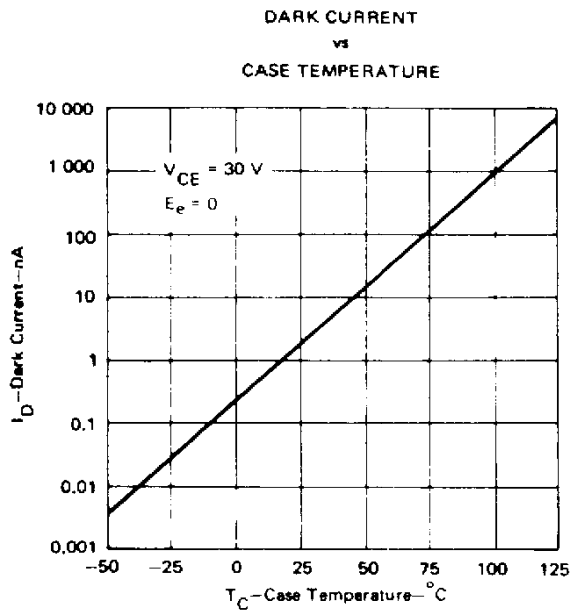


FIGURE 8

NOTE 2. Irradiance (E_e) is the radiant power unit area incident upon a surface. For this measurement, the source is an unfiltered tungsten linear-filament lamp operating at a color temperature of 2870 K.

TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619
N-P-N PLANAR SILICON PHOTOTRANSISTORS

TYPICAL CHARACTERISTICS



TIL601 THRU TIL604, LS600, LS602, LS611 THRU LS619
N-P-N PLANAR SILICON PHOTOTRANSISTORS

TYPICAL CHARACTERISTICS

NORMALIZED LIGHT CURRENT
 vs
 ANGULAR DISPLACEMENT

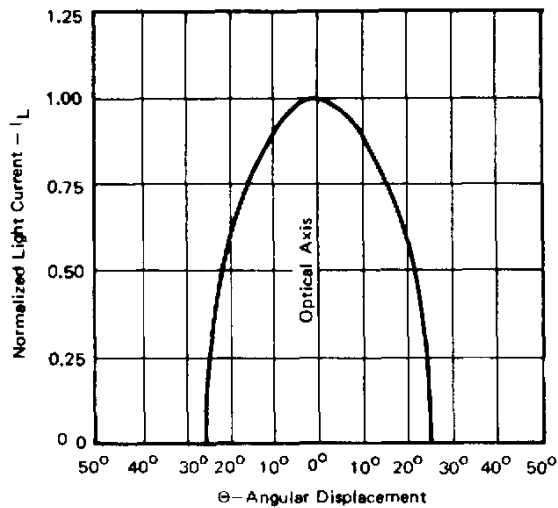


FIGURE 13

RELATIVE SPECTRAL CHARACTERISTICS

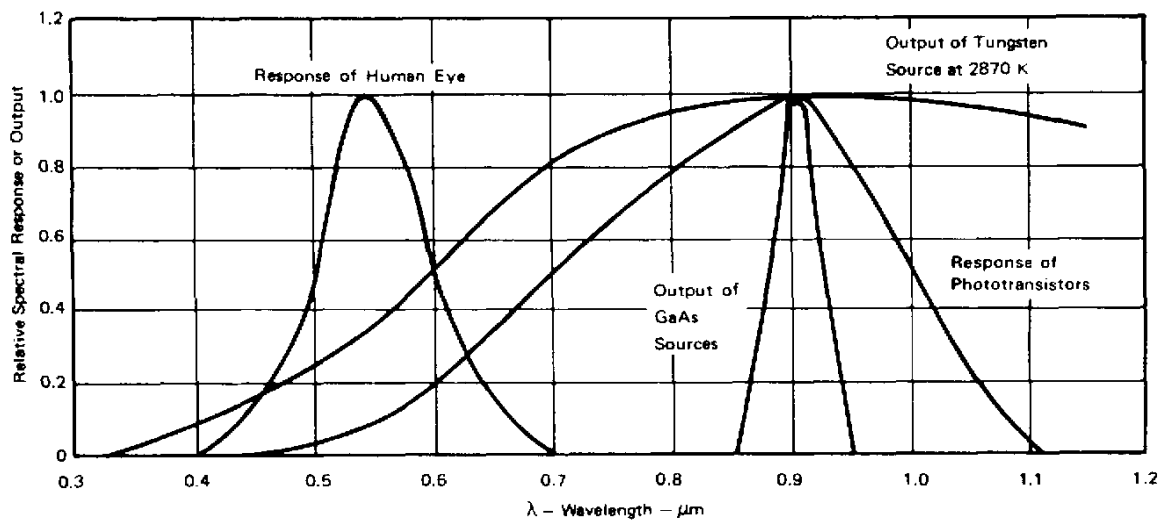


FIGURE 14

IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.