



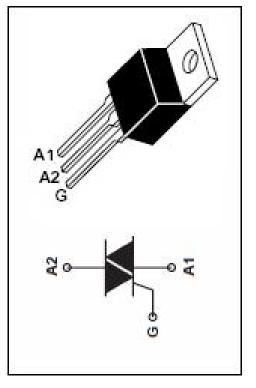
isc Triacs

TIC226series

DESCRIPTION

- 8A RMS ,70A Peak
- Glass passivated Wafer
- 400V to 800V off-state Voltage
- Max I_{GT} of 50mA(Quadrants 1-3)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)								
SYMBOL	PARAMETER		VALUE	UNIT				
V _{DRM}	Repetitive peakoff-state voltage	TIC226D	400	- V				
		TIC226M	600					
		TIC226S	700					
		TIC226N	800					
V _{RRM}		TIC226D	400					
	Repetitive peakreverse voltage	TIC226M	600	V				
		TIC226S	700					
		TIC226N	800					
I _{T(RMS)}	RMS on-state current wave)Tc=85℃	8	А					
I _{TSM}	Non-repetitive peak on-stat	70	А					
P _{GM}	Peak gate power $P_W \leqslant 200 \ \mu \ s$		2.2	W				
P _{G(AV)}	Average gate power	0.9	W					
Tj	Operating Junction tempera	110	°C					
T _{stg}	Storage temperature	-40 ~+125	°C					



1



INCHANGE SEMICONDUCTOR

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	ТҮР	МАХ	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case		1.8	°C/W
R _{th j-a}	Thermal Resistance, Junction to Ambient		62.5	°C/W

ELECTRICAL CHARACTERISTICS

$T_{c}\text{=}25^{\circ}\!\!\mathrm{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	ТҮР	MAX	UNIT
I _{RRM}	Repetitive peak reverse current	V _{RM} =V _{RRM} , V _{RM} =V _{RRM} , Tj=110℃		0.4 2.0	mA
I _{DRM}	Repetitive peak off-state current	V _{DM} =V _{DRM} , V _{DM} =V _{DRM} , Tj=110°C		0.4 2.0	mA
I _{GT}	Gate trigger current	- V _{supply} = 12 V†; R _L = 10 Ω ; t _{p(g)} >20 μ s	2	50	mA
			12	50	
			9	50	
			20		
l _Η	Holding current $V_{supply} = 12 V_{\uparrow}, I_G = 0$ initial $I_{TM} = 100 \text{mA}$			30	mA
V_{GT}	Gate trigger voltageall quadrant	V_{supply} = 12 V†; R _L = 10 Ω ; $t_{p(g)}$ >20 μ s		2	V
V _{TM}	On-state voltage	I _T = 12A; I _G = 50mA		2.1	V

2





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3