

isc Thyristors

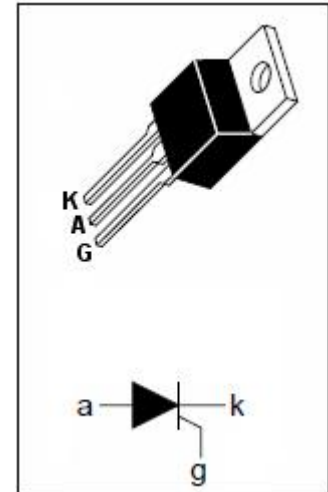
TIC116N

APPLICATIONS

- 5A continuous on-state current
- Glass passivated
- Max I_{GT} of 20uA

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	MIN	UNIT
V_{DRM}	Repetitive peak off-state voltage	800	V
V_{RRM}	Repetitive peak reverse voltage	800	V
$I_{T(AV)}$	On-state current $T_c=80^\circ\text{C}$	5	A
I_{TSM}	Surge non-repetitive on-state current $T_P=10\text{ms}$	80	A
P_{GM}	Peak gate power $P_W \leq 300 \mu\text{s}$	5	W
$P_{G(AV)}$	Average gate power	1	W
T_j	Operating Junction temperature	$-40 \sim +125$	$^\circ\text{C}$
T_{stg}	Storage temperature	$-40 \sim +150$	$^\circ\text{C}$
$R_{th(j-c)}$	Thermal resistance, junction to case	1.5	$^\circ\text{C/W}$
$R_{th(j-a)}$	Thermal resistance, junction to ambient	60	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_{RM}=V_{RRM}$, $T_j=110^\circ\text{C}$			10	μA
I_{DRM}	Repetitive peak off-state current	$V_{RM}=V_{RRM}$, $T_j=110^\circ\text{C}$			10	μA
V_{TM}	On-state voltage	$I_{TM}=8\text{A}$			1.5	V
I_{GT}	Gate-trigger current	$V_D=6\text{V}$; $R_L=100\Omega$			20	μA
V_{GT}	Gate-trigger voltage	$V_D=12\text{V}$; $R_L=100\Omega$			0.8	V
I_H	Holding current	$I_T=100\text{mA}$ Gate Open			20	mA

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