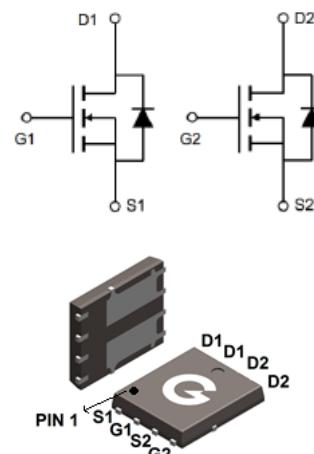


### Features

- Super low gate charge
- Green device available
- Excellent  $C_{dv}/d_t$  effect decline
- Advanced high cell density trench technology
- Halogen free
- Qualified to AEC-Q101 standards for high reliability

**HF**



**PDFN5x6-8LC**

### Mechanical Data

- Case: PDFN5x6-8LC
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208

### Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
TGBLN6601-5DL8	PDFN5x6-8LC	5000 pcs / Tape & Reel	GBLN6601

### Maximum Ratings (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	60	V
Gate-to-Source Voltage	$V_{GSS}$	$\pm 20$	V
Continuous Drain Current ( $T_c = 25^\circ\text{C}$ ) *1	$I_D$	25	A
Continuous Drain Current ( $T_c = 100^\circ\text{C}$ ) *1		18	A
Pulsed Drain Current (tp=10us)	$I_{DM}$	98	A
Single Pulse Avalanche Energy *3	$E_{AS}$	40	mJ

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation ( $T_c = 25^\circ\text{C}$ ) *4	$P_D$	33	W
Thermal Resistance Junction-to-Case *1	$R_{\theta JC}$	4.5	$^\circ\text{C/W}$
Thermal Resistance Junction-to-Air *5	$R_{\theta JA}$	100	$^\circ\text{C/W}$
Operating Junction Temperature Range	$T_J$	-55 ~ +175	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55 ~ +175	$^\circ\text{C}$

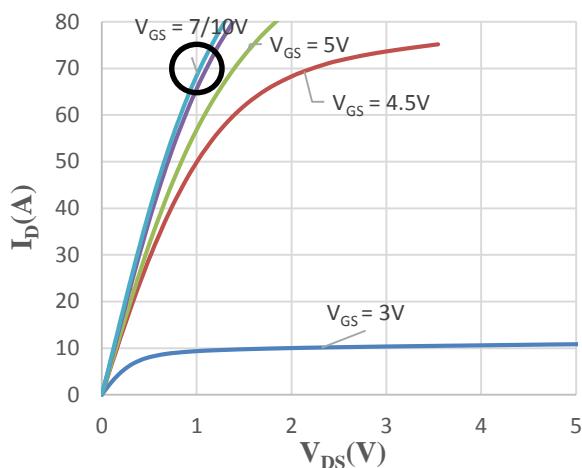
**Electrical Characteristics** (@  $T_A = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
$V_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu\text{A}$	60	-	-	V
$I_{DS(0)}$	Zero Gate Voltage Drain Current	$V_{DS} = 48V, V_{GS} = 0V$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$R_{DS(ON)}$	Static Drain-Source On-resistance <sup>*2</sup>	$V_{GS} = 10V, I_D = 20A$	-	12	20	$\text{m}\Omega$
		$V_{GS} = 4.5V, I_D = 15A$	-	16	24	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1	1.7	2.5	V
<b>Dynamic Characteristics</b>						
$C_{ISS}$	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 15V$ $f = 1.0\text{MHz}$	-	1880	-	pF
$C_{OSS}$	Output Capacitance		-	170	-	
$C_{RSS}$	Reverse Transfer Capacitance		-	140	-	
<b>Switching Characteristics</b>						
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD} = 30V$ $V_{GS} = 10V$ $R_G = 3\Omega$ $I_D = 15A$	-	7.4	-	ns
$t_r$	Turn-on Rise Time		-	5.1	-	
$t_{d(OFF)}$	Turn-Off Delay Time		-	28.3	-	
$t_f$	Turn-Off Fall Time		-	5.5	-	
$Q_G$	Total Gate-Charge	$V_{DD} = 48V$ $V_{GS} = 10V$ $I_D = 15A$	-	36	-	nC
$Q_{GS}$	Gate to Source Charge		-	4.7	-	
$Q_{GD}$	Gate to Drain (Miller) Charge		-	9	-	
<b>Source-Drain Diode Characteristics</b>						
$V_{SD}$	Diode Forward Voltage <sup>*2</sup>	$I_{SD} = 5A, V_{GS} = 0V, T_J = 25^\circ\text{C}$	-	-	1.2	V
$I_S$	Diode Continuous Forward Current *1,4		-	-	25	A
$t_{rr}$	Reverse Recovery Time	$V_R = 50V, I_F = 15A$ $di/dt = 100A/\mu\text{s}$	-	26	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	18	-	nC

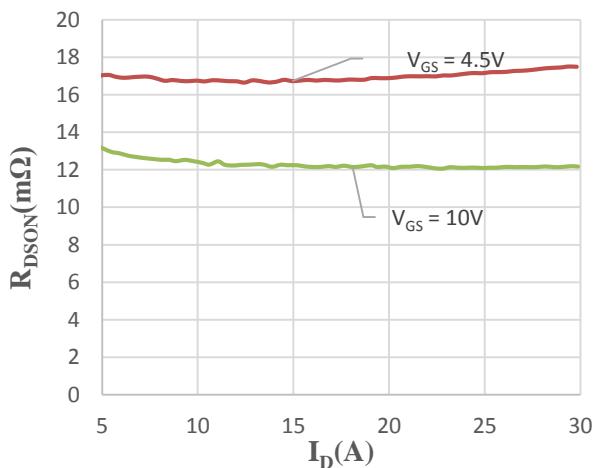
Notes:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper
2. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$
3. The  $E_{AS}$  data shows Max. rating. The test condition is  $V_{DD} = 25V, V_{GS} = 10V, L = 0.1\text{mH}$
4. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation
5. The data is test on minimal footprint

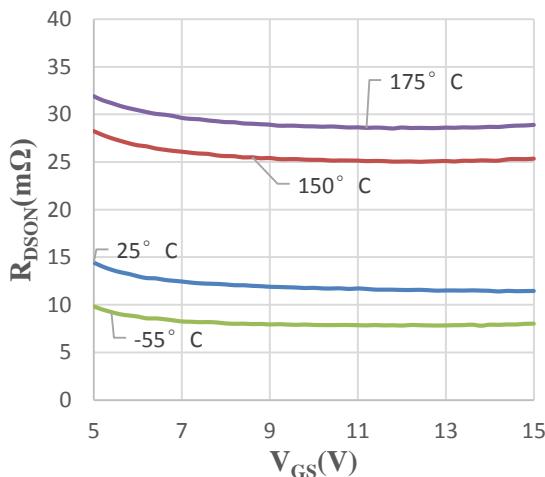
### Ratings and Characteristics Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)



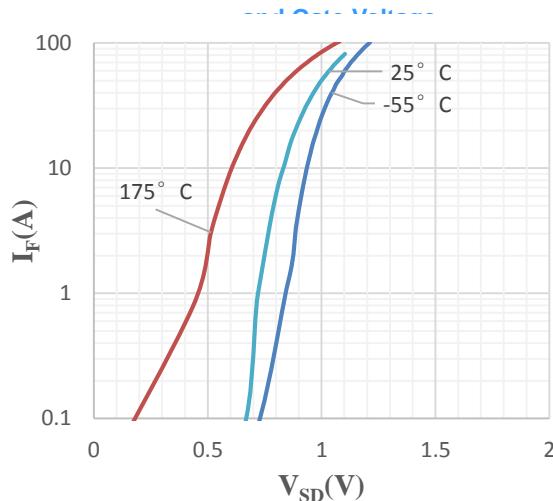
**Fig 1 Output Characteristics**



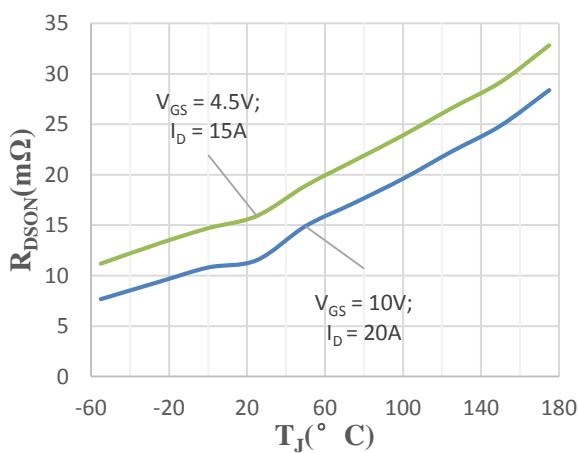
**Fig 2 On-Resistance vs. Drain Current**



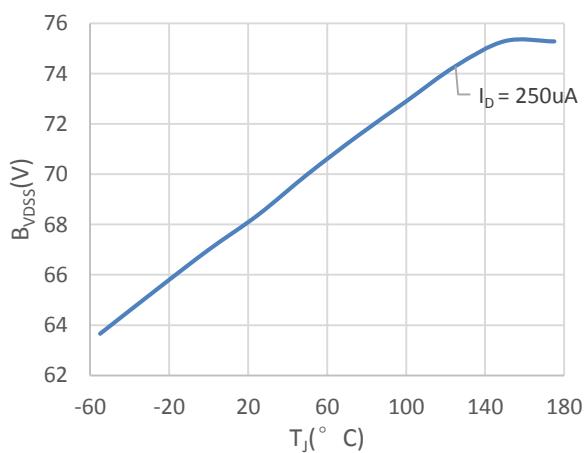
**Fig 3 On-Resistance vs. Gate-Source Voltage**



**Fig 4 Body-Diode Characteristics**



**Fig 5 On-Resistance vs. Junction Temperature**



**Fig 6 Drain-Source vs. Junction Temperature**

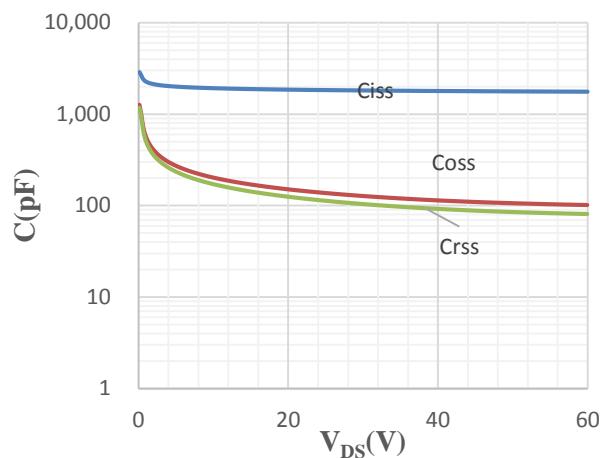


Fig 7 Capacitance Characteristics

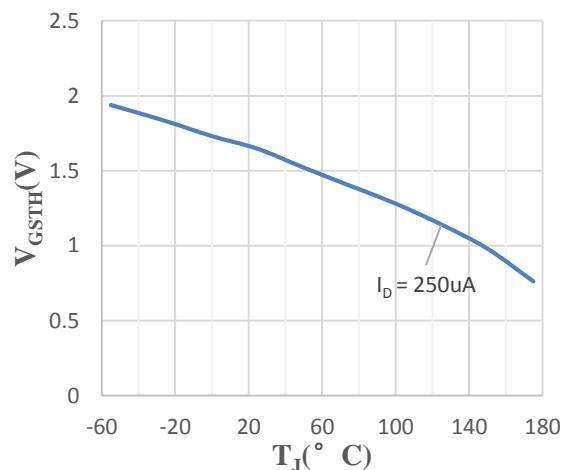


Fig 8 Gate Voltage vs. Junction Temperature

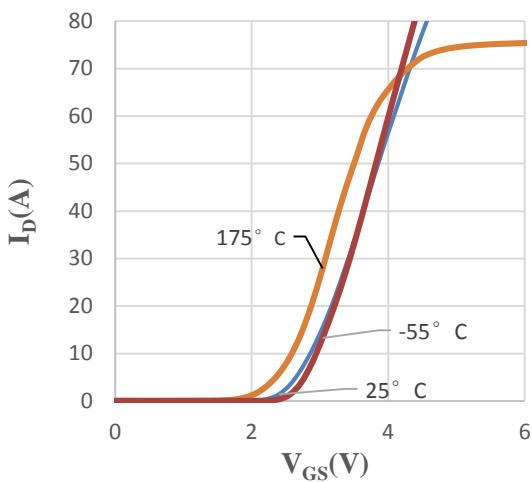


Fig 9 Transfer Characteristics

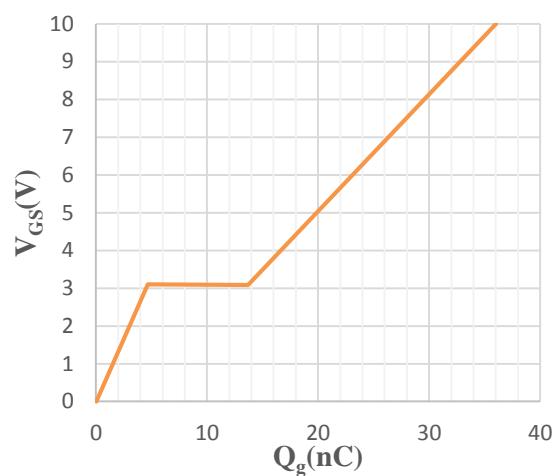
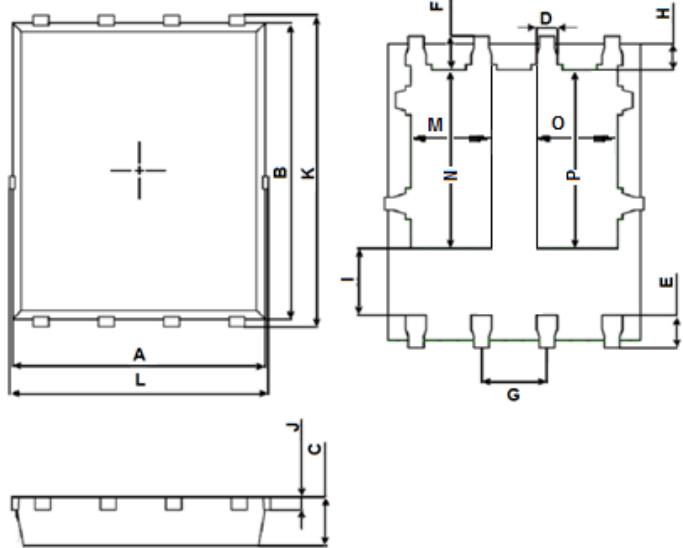


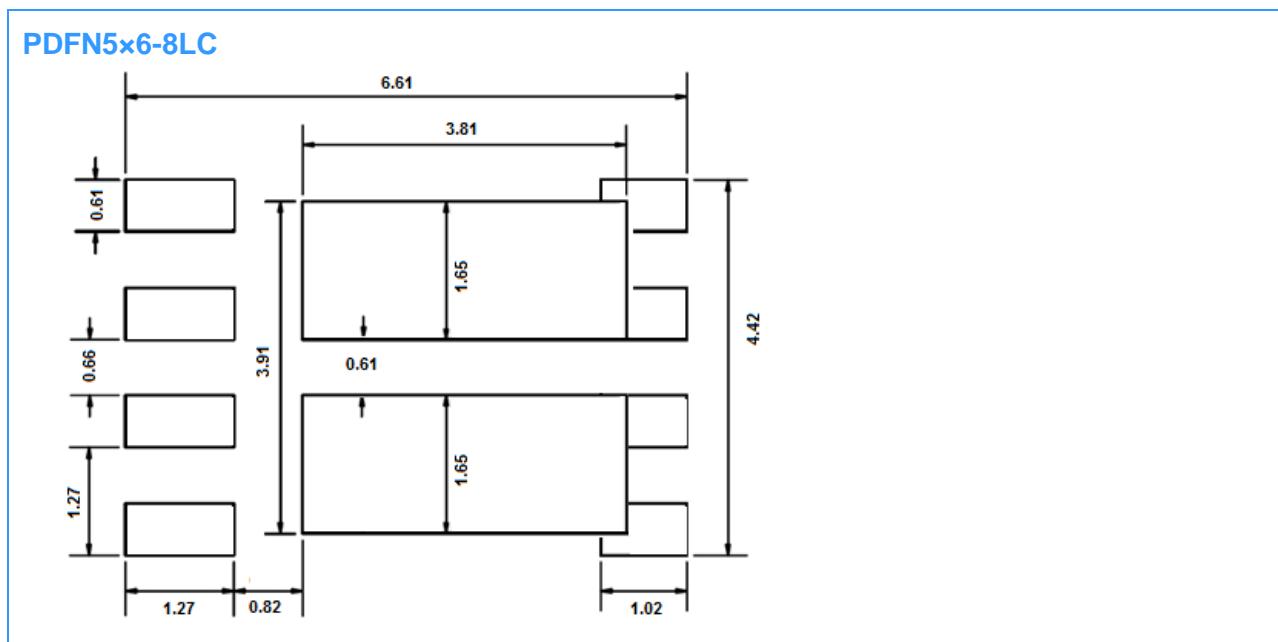
Fig 10 Gate-Charge Characteristics

## Package Outline Dimensions (Unit: mm)



PDFN5x6-8LC		
Dimension	Min.	Max.
A	4.824	4.976
B	5.674	5.826
C	0.900	1.000
D	0.350	0.450
E	0.559	0.711
F	0.574	0.726
G	1.250	1.290
H	0.424	0.576
I	1.190	1.390
J	0.154	0.354
K	5.974	6.126
L	4.944	5.096
M/O	1.595	1.795
N/P	3.375	3.575

## Mounting Pad Layout (Unit: mm)



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