DATA IMAGE CORPORATION

LCD Module Specification

ITEM NO.: TG243210FFEBB-T1

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Rev	Date	Item	Page	Comment
В	FEB/24/03	12.2.3, 12.3	22-23	1.Change: 11.2.3 Inspection Parameters. 2.Add:11.3 Sampling Condition.
	FEB/24/03	14	24	New model numbering system updated from old P/N# TG24325FEBH-TP

2. RECORD OF REVISION

3. GENERAL SPECIFICATION

Display Format :	240 (\	N) ×	320 (H)	dots
Dots Size :	0.225 (\	N) × 0	.225 (H)	mm
View Area :	61.8 (\	N) ×	80.3 (H)	mm
General Dimensions :	70.6 (\	N) ×	89.6 (H) ×	7.2 (T) mm Max.
Weight :	g max.			
LCD Type :	STN Blue	STN	Yellow V	FSTN
Polarizer mode :	Reflective	VTrans	flective	
	Transmissive	Nega	tive	
View Angle :	V6 O'clock	12 O'	clock	Others
Backlight :	LED	VEL		CCFL
Backlight Color :	Yellow green	Ambe	er V	Blue Green
	White	Othe	rs	
Controller / Driver :	NT7701/NT7702			
Temperature Range :	Normal Operating 0 t Storage -20] o 50°C) to 70°C	V Wide Te Operatir Storage	ng -20 to 60°C

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

			Vss=	$0V, Ta = 25^{\circ}$
Item	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	VDD-VSS	-0.3	6.5	V
Supply Voltage (LCD Driver)	VEE-VSS	0	45	V
Input Voltage	Vi	Vss	Vdd	V
Operating Temperature	Тор	-20	60	°C
Storage Temperature	Tstg	-30	80	°C

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

Item	Operating		Sto	rage	Comment		
item	(Min.)	Max.)	(Min.)	(Max.)	Comment		
Ambient Temp	-20	60	-30 80		Note (1)		
Humidity	Note	e (2)	Note(2)		Note(2)		Without Condensation
Vibration		4.9M/S ²		19.6M/S ²	XYZ Direction		
Shock		29.4M/S ²	490M/S ²		XYZ Direction		

Note(1) Ta = $0^{\circ}C$: 50Hr Max.

Note(2) Ta $\leq 40^{\circ}$ C : 90% RH Max.

Ta $\geq 40^{\circ}$ C :

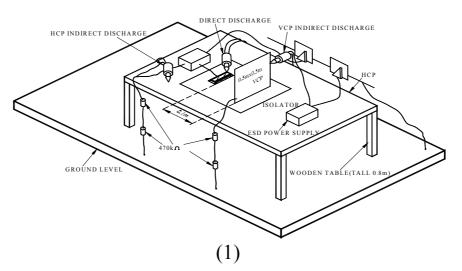
Absolute humidity must be lower than the humidity of 90% RH at 40°C.

4.3 Electronic Static Discharge maximum rating

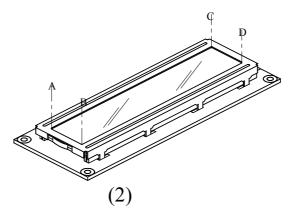
Item	Description			
Testing environment	Ambient tempe	erature :15°C to 35 °C		
	Humidity: 30%	6 to 60 %		
	LCM (E.U.T)	: Power up		
Testing equipment	Manufacture: N	Noise Ken, Model No. ESD-100L		
Testing condition	See drawing 1			
Direct discharge	0 to \pm 6 KV	Discharge point, see drawing 2		
Indirect discharge	0 to ± 12 KV	Discharge point, see drawing 1		
Pass condition	No malfunction	n of unit. Temporary malfunction of unit which		
	can be recovered by system reset			
Fail condition	Non. Recovera	ble malfunction of LCM or system		

ESD test method : IEC1000-4-2

FIG 1 ESD TESTING EQUIPMENT



DIRECT CONTACT DISCHARGE CONTACT POINT : A.B.C.D



Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply Voltage (Logic)	VDD-VSS		2.5	3.3	5.5	V
		-20°C	20.8	21.5	22.2	
Supply Voltage (LCD)	VEE-VSS	25°C	19.3	20	20.7	V
		70°C	18.3	19	19.7	
	Vін		0.8*VDD		Vdd	V
Input Voltage	VIL		VSS		0.2*VDD	v
	ldd	LCD-ON		10		
Logic Supply	סטו	LCD-OFF		0.1		mA
Current	IEL	IF EL B/L=ON		25		ШA
	ICL	IF EL B/L=OFF		0.05		

5. ELECTRICAL CHARACTERISTICS

6. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	Symbol	Condition	Min.	Тур.	Max.	Unit	Ref.
Rise Time	Tr	0°C				me	
Rise fille	11	25°C		260	520	ms	Note (1)
Fall Time	Tf	0°C				me	Note (1)
	11	25°C		300	600	ms	
Contrast	CR	25°C	2	6			Note (3)
View Angle	ngle $\begin{array}{c} \theta 1 \sim \theta 2\\ \varnothing 1, \ \varnothing 2\end{array}$	25°C &		60			Note (2)
View Angle		CR≥2		90			Note (2)
Frame Frequency	Ff	25°C		70		Hz	

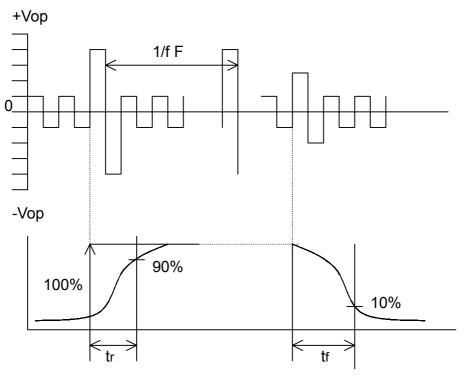
Note (1) & (2) : See next page

Note (3) : Contrast ratio is defined under the following condition:

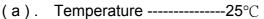
CR= Brightness of non-selected condition Brightness of selected condition

- (a). Temperature ----- 25°C
- (b). Frame frequency ---- 70Hz
- (c). Viewing angle ----- $\theta = 0^{\circ}$, $\emptyset = 0^{\circ}$
- (d). Operating voltage --- 20V

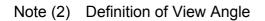
Note (1) Response time is measured as the shortest period of time possible between the change in state of an LCD segment as demonstrated below:

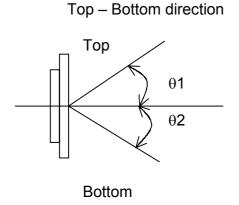


Condition:

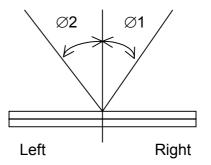


- (b). Frame frequency ----- 70Hz
- (c). View Angle ----- $\theta = 0^\circ, \emptyset = 0^\circ$
- (d). Operating voltage ------ 20V









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6.1 ELECTRIC - OPTICAL CHARACTERISTIC

(TEMP: 20°C)

ITEM	CONDITION	DIMENI			STANDARD			
	CONDITION	DIVIENI		MIN	TYP	MAX		
BRIGHTNESS	400H _z ,100Vrms SINE WAVE	cd/m ²	2	45	56			
UNIFORMITY	400H _z ,100Vrms SINE WAVE	%		85	90			
CURRENT CONSUMPTION	400H _z ,100Vrms SINE WAVE	m _A /cm ²			0.12	0.17		
		BLUE	х	0.15	0.17	0.19		
CHROMATICITY	400H _{z,} 100Vrms	GREEN	Y	0.33	0.35	0.37		
CHINOMATION	SINE WAVE	White	х	0.30	0.32	0.34		
		vville	Y	0.34	0.36	0.38		

.

Note: Measured at the EL panel unit

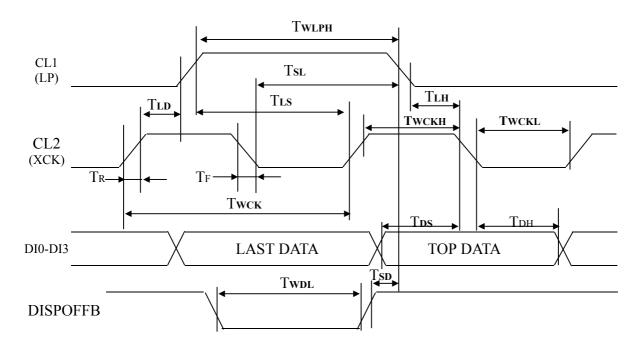
7. TIMING CHARACTERISTICS

Segment timing:

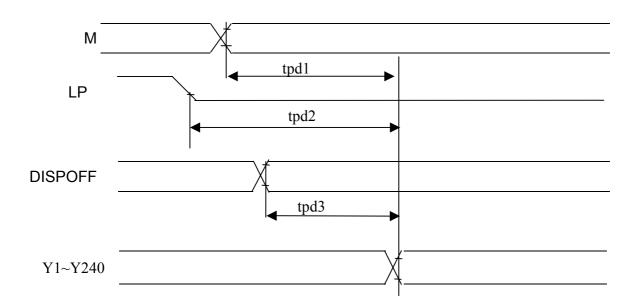
Item	Symbol	Condition	Min.	Тур.	Max.	Units
Shift Clock Period *1	Тwск	Tr,T F≤10ns	125			ns
Shift Clock Pulse Width	Twckh Twckl		51			ns
Data Set Up Time	TDS		30			ns
Data Hold Time	Трн		40			ns
Latch Pulse "H" Pulse Width	Twlph		51			ns
Shift Clock Rise to Latch Pulse Rise Time	Tld		0			ns
Shift Clock Fall to Latch Pulse Fall Time	Tsl		51			ns
Latch Pulse Rise to Shift Clock Rise Time	TLS		51			ns
Latch Pulse Fall to Shift Clock Fall Time	Тін		51			ns
Input Signal Rise/ Fall Time *2	Tr, Tf				50	ns
Enable Setup Time	Ts		36			ns
DISPOFFB Removal Time	Tsd		100			ns
DISPOFFB "L" Pulse Width	Twdl		1.2			u
Output Delay Time (1)	Тр	C _L =15pF			78	ns
Output Delay Time (2)	Tpd1, Tpd2	CL=15pF			1.2	us
Output Delay Time (3)	Трдз	CL=15pF			1.2	us

Note: *1. Take the cascade connection into consideration.

*2. (Twck-Twckh-Twckl)/2 is maximum in the case of high speed operation.

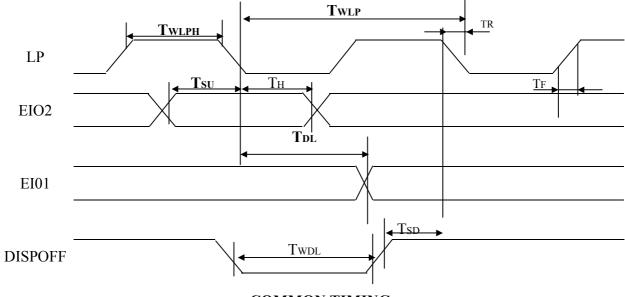


SEGMENT TIMING



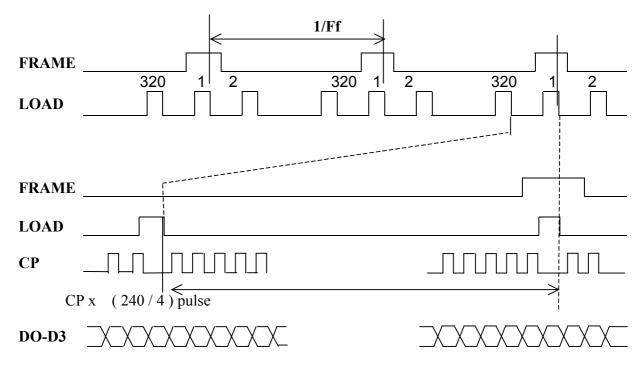
common timing:

Item	Symbol	Condition	Min.	Тур.	Max.	Units
Shift Clock Period	TWLP	Tr,T F≤20ns	250			ns
Pulse "H" Pulse Width	Тильч	VDD=+5.0V±10%	15			ns
ruise n ruise wiutii	I WLPH	Тwlph VDD=+2.5V~+4.5V 30				
Data Set Up Time	Tsu		30			ns
Data Hold Time	Тн		50			ns
Input Signal Rise/ Fall Time	Tr, Tf				50	ns
DISPOFFB Removal Time	Tsd		100			ns
DISPOFFB "L" Pulse Width	Twdl		1.2			u
Output Delay Time (1)	Tdl	CL=15pF			200	ns
Output Delay Time (2)	Тр д1, Трд 2	CL=15pF			1.2	us
Output Delay Time (3)	Tpd3	CL=15pF			1.2	us



COMMON TIMING

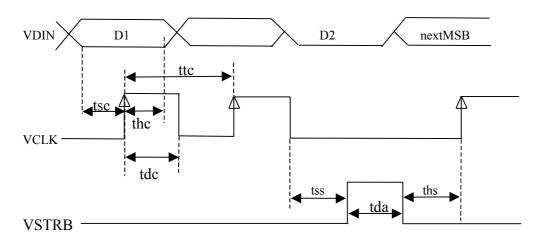
7.1 TIMING CHART



7.2 S-8330 Timing

common timing:

Item	Symbol	Condition	Min.	Тур.	Max.	Units
Output Voltage range	VOUT	S-8330A24	12	-	24	V
Potential division accuracy			-	-	±2.4	%
Electric volume resolution			-	1/256	-	
Error in linearity			-	-	±1/2	LSB
Data setup time	tsc		0.5	-	-	μs
Data hold time	thc		0.5	-	-	μs
VCLK falling edge to VSTRB rising edge	tss		0.5	-	-	μs
VSTRB pulse width	tda		0.5	-	-	μs
VSTRB falling edge to VCLK rising edge	ths		0.5	-	-	μs
VCLK pulse width	tdc		0.5	4.17	-	μs
VCLK period	ttc		5	8.33	-	μs
VDIN,VCLK,VSTRB input	VSHC1	$V_{IN} \ge 3.0V$	2.4	-	-	v
voltage "H" level	VSHC2	$V_{IN} \leq 3.0V$	1.6	-	-	v
VDIN,VCLK,VSTRB input voltage "L" level	VSLC		-	-	0.2	
VDIN,VCLK,VSTRB input leakage current	ISLC	V _{IN} =9V	-1	-	1	μΑ



Data input Timing

Note 1: An electric volume has 8-bit resolution. However, its error in linearity may exceed $\pm 1/2LSB$ only before and

after switching from 127 to 128(from 01111111 to 10000000) and vice versa. When the electric volume is used within the variable range of 12 V, its error in linearity corresponds to 6 bits ($\pm 2.4\%$ potential division accuracy is ensured).

Electric Volume

The S-8330/31 series incorporates an electric volume with an 8-bit shift register and an 8-bit latch. The output voltage is variable in the width of 12V. The output voltage can be held in the data transmission mode because the data read in the shift register is fetched into the latch by unshyncronized strobe input. Figure 1 shows a block diagram and timing charts

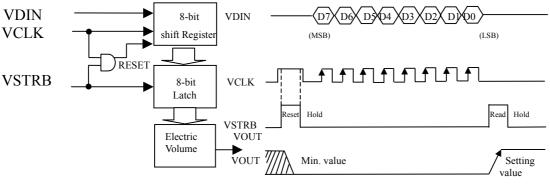


Figure 1 Block Diagram and Timing

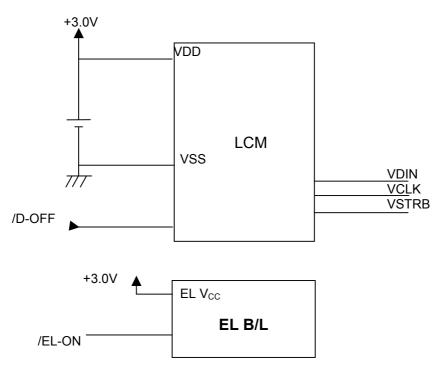
VDIN :Inputs data to the electric volume. "Data 1" is input at "H" level; "Data 0" is input at "LOW" level.

- VCLK :Inputs clock to the electric volume. Fetches data at VDIN pin into the shift register at the rising edge of clocks. When clocks of over 8 bits are input, the read data is shifted in succession for each clock, and data corresponding to 8 bits which was input lastly is valid.
- VSTRB :Inputs strobe signal . The contents of the shift register are latched by turning the strobe signal to "H". When the data fetched into the latch is directly transmitted to the electric volume, the output voltage changes. Data in the latch is held by turning the strobe signal to "L".

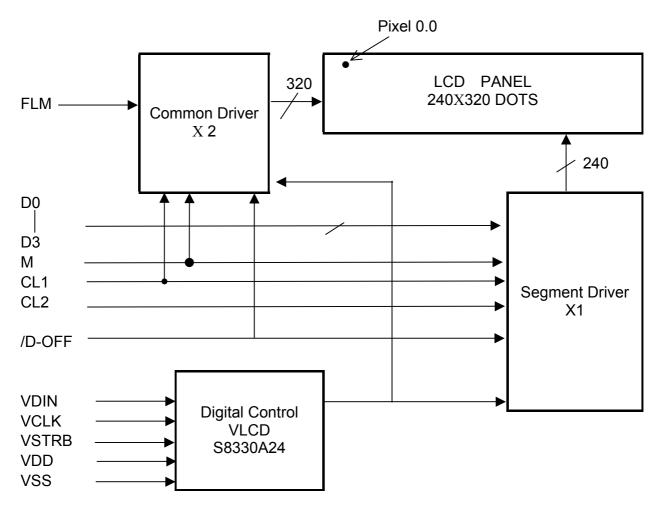
8. PIN CONNECTIONS

NO	Symbol	Function	
1	NC	No connection	
2	NC	No connection	
3	VEE	S8330A24 Voltage output for LCD (+)	
4	VDD	Power Supply for Logic(3.0V)	
5	FLM	Frame Start Signal	
6	VSS	Logic Ground	
7	CL1	Data Latch Pulse	
8	VSS	Logic Ground	
9	М	Switch Signal to convert LCD drive wave form into AC	
10	/D-OFF	Display ON/OFF control (H=ON,L=OFF)	
11	CL2	$H \rightarrow L$ Data Shift Pulse	
12	VDIN	S8330 serial data input.	
13	VCLK	S8330 serial clock input.	
14	D3		
15	D2	Display data bus	
16	D1	Display data bus	
17	D0		
18	VSTRB	S8330 strobe signal input.	
19	Y2		
20	X2	Signal for touch panel	
21	Y1		
22	X1		
23	EL-VCC	Back light Power Supply for EL (3.0V)	
24	/EL-on	L-on E/L ON/OFF control signal (H: off, L: on)	

9. POWER SUPPLY



10. BLOCK DIAGRAM



10.1 DISPLAY PATTERN

FIRST DATA								
COM001	D3	D2	D1	D0	 D3	D2	D1	D0
COM002	D3	D2	D1	D0	D3	D2	D1	D0
COM239	D3	D2	D1	D0	D3	D2	D1	D0
COM240	D3	D2	D1	D0	D3	D2	D1	D0
	SEG001	SEG002					SEG319	SEG320

320*240 Dots Matrix

11. TOUCH PANEL CHARACTERISTICS

1. Scope

This specification is applied to DATA IMAGE Display Devices Product/ TG24324

2. Features

Type : Analog Resistive Type Touch Panel Input Mode : Pen or Finger Structure: PET-----Non Glare ITO Film, 175µm Clear ITO Film, 175µm Glass------ITO Glass, 0.7mm/1.1mm/1.8mm Tape Gasket----60µm Dot pitch------1.0 mm(Optional)

Connector : FPC

3. General Specification

Item	Specification	Unit
ITO Glass	0.7±0.07(T)/1.1±0.1(T)/1.8±0.1(T)	mm
FPC (ITO Film)	175±15(ITO Film)	μm
Connector	FPC,4-pin connector (L23.3*W5.5),Pitch=1.0	mm
Total Thickness	87.3±0.3(W)*69.3±0.3(H)*0.95±0.15(T)	mm

4. RATING

- 4.1 The maximum voltage: DC 7Volts
- 4.2 Usable Temperature Range From: -20°C to 60 °C(20~90%)
- 4.3 Storage Temperature Range From: -40°C to 80 °C(20~90%)

5. Electrical Specification

- 5.1 Resistance between leads: FILM(X axis) GLASS (Y axis):200~900Ω
- 5.2 Linearity : X axis $\pm 1.5\%$, Y axis $\pm 1.5\%$
- 5.3 Insulation resistance: $20m\Omega^{\uparrow}$ @DC 25V
- 5.4 Chattering Time : 10msec @100K $\Omega \downarrow$ Pull -UP

6. Mechanical Specification

- 6.1 Input Method pen or Finger
- 6.2 Operating Force: $80g\downarrow$ With R8.0 HS40° Silicon Rubber $80g\downarrow$ With R0.8 Polyacetal pen
- 6.3 Surface Hardness: 2H (Pencil Test), Hard Coated PET
- 6.4 Light Transmission: 75%↑ (Non Glare Type),80%↑ (Clear Type)
 - (@550nm, Spectro Photometer Hitachi U3300)

7. Durability

7.1 Durability against Writing Characters

After writing 200,000 characters in the same area (20mm*20mm), Force : 250g, Speed: 1,000 characters/hour

- Resistance Between Leads : FILM (X axis) GLASS (Y axis)
- Linearity : X axis $\pm 1.0\%$, Y axis $\pm 1.0\%$
- Insulation Resistance: $20m\Omega^{\uparrow}$ @DC 25V
- 7.2 Punching Life

After Punching 1,200,000 Times with the R8.0 silicon rubber Force : 250g, Speed:2/sec

- Resistance Between Leads : FILM (X axis) GLASS (Y axis)
- Linearity : X axis $\pm 1.0\%$, Y axis $\pm 1.0\%$

- Insulation Resistance: $20m\Omega^{\uparrow}$ @DC 25V
- 7.3 Impact Resistance:

No damage when ϕ 9mm steel ball is dropped on the surface from 30cm height at 1 time.

- 7.4 Flexible Pattern Heat Seal Peeling Strength :1.5N/cm (peeling upward by 90deg. Speed:50mm/min)
- 7.5 Flexible Pattern Bending Resistance Bending: 3 times or more by bending radius R1.0mm.
- 7.6 Flexible Pattern insert/pull out Resistance 5 times at least. The requirements in 5.1 shall be satisfied.
- 7.7 Vibration Resistance (In operation) No faulty operation when sweep vibration of 0.2G 10~55Hz (1min.) is given for 30min. each in the directions of X,Y,Z. (Not in operation) The requirements in 6.2 and 5.1~5.4 shall be satisfied after sweep vibration of 2g 10~55Hz (1min.) is given for 30min. each in the directions of X,Y,Z.
- 7.8 Package Drop

No damage to the product.(1 corner edge, 2 ridges, 4 surfaces, drop from 50 cm height:65cm for the bottom)

- 8. Reliability
 - 8.1 High Temperature Test
 - After putting Panels at 80°C for 120 hours, then leaving for 24 hours at room temperature.
 - Resistance Between Leads : FILM (X axis) GLASS(Y axis)
 - Linearity : X axis $\pm 1.5\%$, Y axis $\pm 1.5\%$
 - Insulation Resistance: 20mΩ↑ @DC 25V
 - Operating Fore : $80g\downarrow$ With R8.0 HS40° Silicon Rubber

 $80g\downarrow$ With R0.8 Polyacetal pen

- 8.2 Low Temperature Test
 - After putting panels at-40°C for 120 hours, then leaving for 24 hours at room temperature.
 - Resistance Between Leads : FILM (X axis) GLASS(Y axis)
 - Linearity : X axis $\pm 1.5\%$, Y axis $\pm 1.5\%$
 - Insulation Resistance: $20m\Omega^{\uparrow}$ @DC 25V
 - Operating Fore : 80g↓ With R8.0 HS40° Silicon Rubber

 $80g\downarrow$ With R0.8 Polyacetal pen

- 8.3 Temperature and Humidity Test: After putting panels at 60°C, 90%RH for 120 hours,
 - then leaving for 24 hours at room temperature
 - Resistance Between Leads : FILM (X axis) GLASS(Y axis)
 - Linearity : X axis $\pm 1.5\%$, Y axis $\pm 1.5\%$
 - Insulation Resistance: $20m\Omega^{\uparrow}$ @DC 25V
 - Operating Fore: $80g\downarrow$ With R8.0 HS40° Silicon Rubber

80g↓ With R0.8 Polyacetal pen

8.4 Repetition of High and Low Temperature

After putting panels at the condition of -40°C for 30minutes, and then 80°C for 30 minutes and this process is repeated by 10 cycles, then leaving for 24 hours at room temperature.

- Resistance Between Leads : FILM (X axis) GLASS(Y axis)
- Linearity : X axis $\pm 1.5\%$, Y axis $\pm 1.5\%$
- Insulation Resistance: $20m\Omega^{\uparrow}$ @DC 25V
- Operating Fore : 80g↓ With R8.0 HS40° Silicon Rubber

 $80g\downarrow$ With R0.8 Polyacetal pen

12. QUALITY ASSURANCE

12.1 Test Condition

12.1.1 Temperature and Humidity(Ambient Temperature)

Temperature	:	$20 \pm 5^{\circ}C$
Humidity	:	$65 \pm \mathbf{5\%}$

12.1.2 Operation

Unless specified otherwise, test will be conducted with LCM in operation.

- 12.1.3 Container Unless specified otherwise, vibration test will be conducted on module only.
- 12.1.4Test Frequency Single cycle.

12.1.5 Test Method

No.	Parameter	Conditions	Regulations
1	High Temperature Operating	60 ± 2 °C	Note 3
2	Low Temperature Operating	-20 ± 2 °C	Note 3
3	High Temperature Storage	80 ± 2 °C	Note 3
4	Low Temperature Storage	-30 ± 2 °C	Note 3
5	Vibration Test (Non-operation state)	Total fixed amplitude : 1.5mm Vibration Frequency : 10 ~ 55Hz One cycle 60 seconds to 3 directions of X.Y.Z. for each 15 minutes	Note 3
6	Damp Proof Test (Non-operation state)	40°C ± 2°C, 90~95%RH, 96h	Note 1,2
7	Shock Test (Non-operation state)	To be measured after dropping from 60cm high once concrete surface in packing state	Note 3

Note 1: Returned under normal temperature and humidity for 4 hrs.

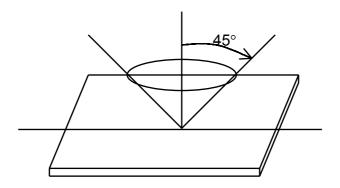
Note 2: No dew condensation to be observed.

Note 3: No change on display and in operation under the test condition

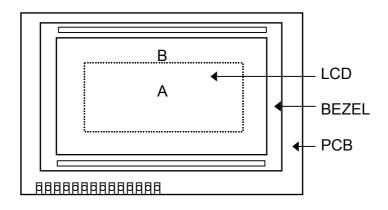
12.2 Inspection condition

12.2.1 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.



12.2.2 Definition of applicable Zones

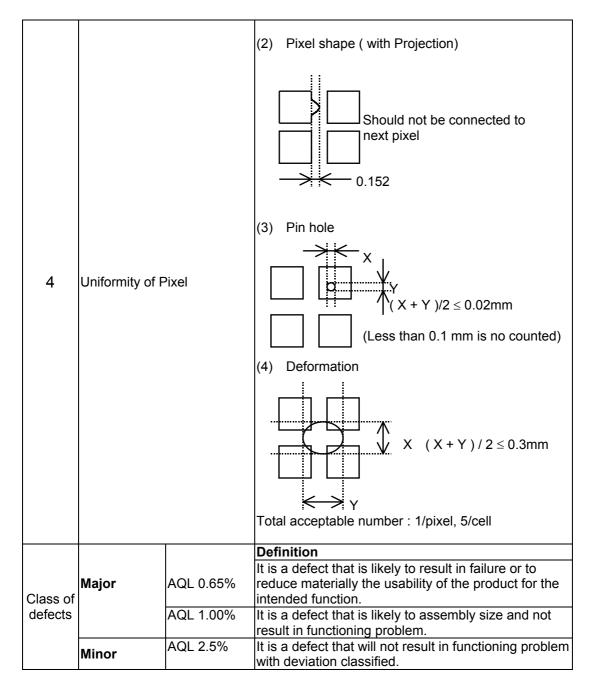


A : Display Area

B : Non-Display Area

12.2.3 Inspection Parameters

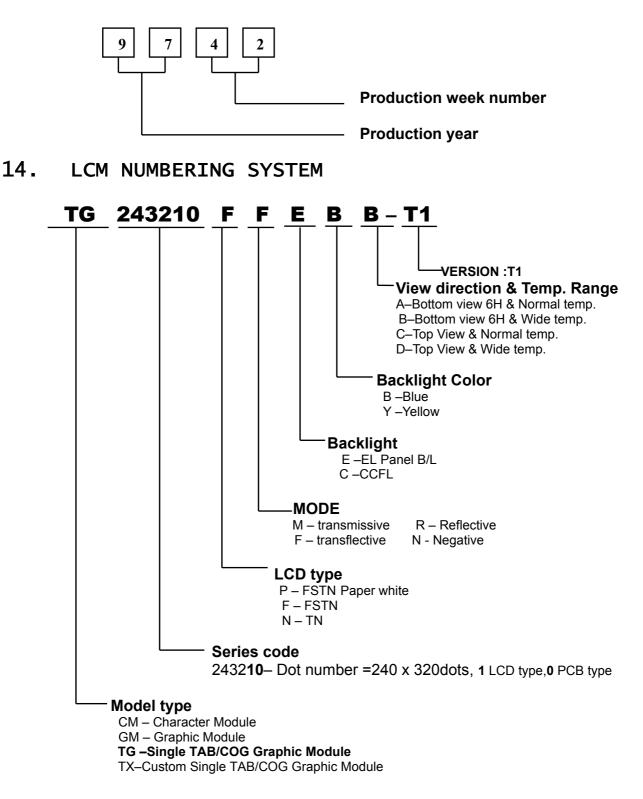
No.	Parameter	Criteria		
1	Black or White spots	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$		
2	Scratch, Substances	ZoneAcceptable numberClass Of DefectsAQL LevelX (mm)Y(mm)ABDefects* $0.04 \ge W$ ** $3.0 \ge L$ $0.06 \ge W$ 44 $2.0 \ge L$ $0.08 \ge W$ 23- $0.1 < W$ 01X : LengthY : Width* : DisregardTotal defectsshould not exceed 4/module		
3	Air Bubbles (between glass & polarizer)	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$		
4	Uniformity of Pixel	(1) Pixel shape (with Dent) 0.152		



12.3 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer. Lot size: Quantity of shipment lot per model. Sampling type: normal inspection, single sampling Inspection level: Level II Sampling table: MIL-STD-105E

13. LOT NUMBERING SYSTEM



15. PRECAUTION FOR USING LCM

1. LIQUID CRYSTAL DISPLAY (LCD)

LCD is made up of glass, organic sealant, organic fluid, and polymer based polarizers. The following precautions should be taken when handing,

(1). Keep the temperature within range of use and storage. Excessive temperature and humidity could cause

polarization degredation, polarizer peel off or bubble.

(2). Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin.

(3). Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.

(4). Glass can be easily chipped or cracked from rough handling, especially at corners and edges.

(5). Do not drive LCD with DC voltage.

2. Liquid Crystal Display Modules

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted. (1). Do not tamper in any way with the tabs on the metal frame.

(2). Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.

(3). Do not touch the elastomer connector, especially insert an backlight panel (for example, EL).

(4). When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting . Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

(5). Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

2.2. Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

(1). The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.

(2). The modules should be kept in antistatic bags or other containers resistant to static for storage.

(3). Only properly grounded soldering irons should be used.

(4). If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

(5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.(6). Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

2.3 Soldering

(1). Solder only to the I/O terminals.

(2). Use only soldering irons with proper grounding and no leakage.

(3). Soldering temperature : $280^{\circ}C \pm 10^{\circ}C$

(4). Soldering time: 3 to 4 sec.

(5). Use eutectic solder with resin flux fill.

(6). If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed after wards.

2.4 Operation

(1). The viewing angle can be adjusted by varying the LCD driving voltage V0.

(2). Driving voltage should be kept within specified range; excess voltage shortens display life.

(3). Response time increases with decrease in temperature.

(4). Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".

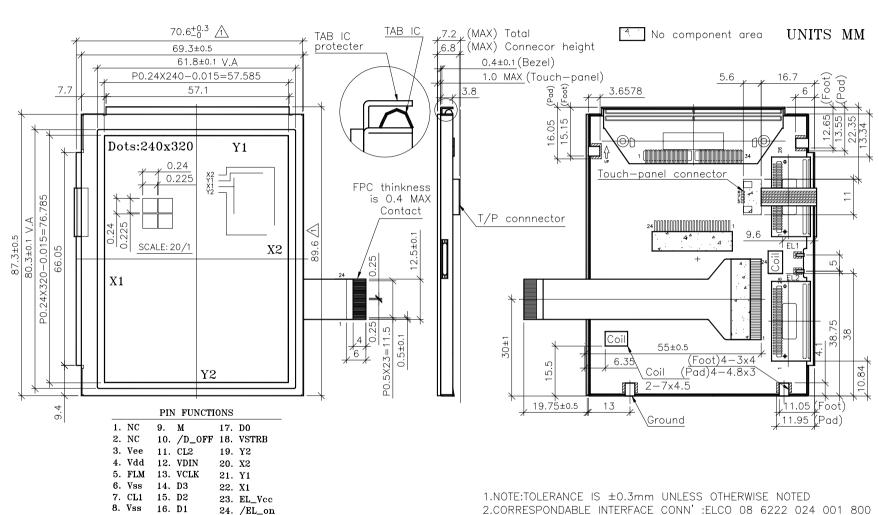
(5). Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

2.5 Storage

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

2.6 Limited Warranty

Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not responsible for any subsequent or consequential events.



16.OUTLINE DRAWING

