



TELEFUNKEN electronic
Creative Technologies

Preliminary specifications

NPN Silicon Darlington Power Transistor

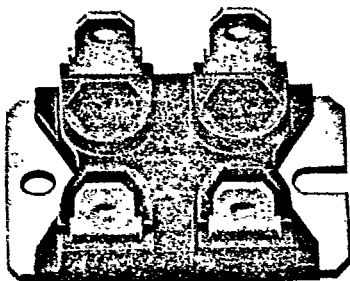
Applications: ● Motor-control (380 V-mains)

- UPS (Uninterruptible power supplies)
- High power SMPS (≥ 1000 W)
- Battery chargers
- Welding equipments
- Inductive heating equipment

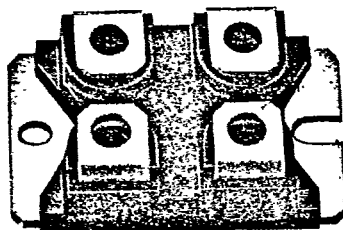
Features:

- High reverse voltage
- Short switching times
- Very fast C-E-free-wheel diode
- Base 1 and base 2 connectable
- Triple diffusion technique
- Glass passivation

Case variations

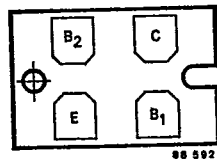
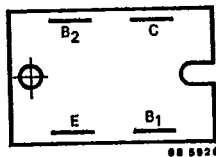
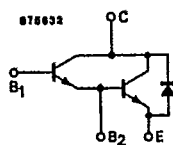


TFK 3070 D



TFK 3070 DA

Terminal connections

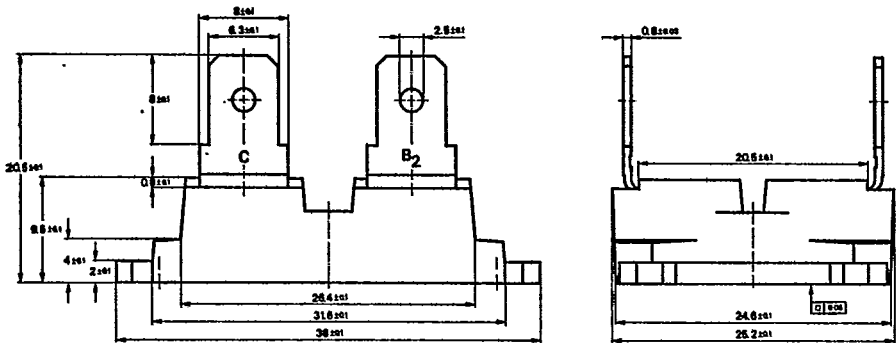


T1.2/1451.0888 E

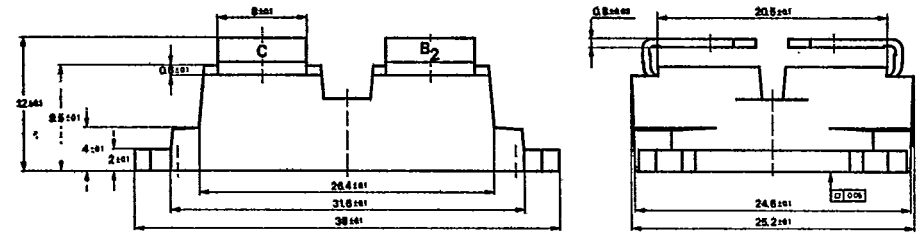
TFK 3070 D

T-33-35

Dimensions in mm



TFK 3070D



TFK 3070DA

Plastic case

Weight max. 30.0 g

TFK 3070D

T-33-35 -

Absolute maximum ratings

Collector-emitter voltage	V_{CEO}	700	V
	V_{CES}	1000	V
Emitter-base voltage	V_{EBO}	7	V
Collector current	I_C	20	A
Collector peak current	I_{CM}	30	A
Base current	I_B	4	A
Base peak current	I_{BM}	8	A
Total power dissipation	P_{tot}	125	W
$T_{case} = 25^\circ\text{C}$	T_J	150	$^\circ\text{C}$
Junction temperature	T_{stg}	-40...+150	$^\circ\text{C}$
Storage temperature range	V_{is}	2500	V
Insulation voltage			

Maximum thermal resistances

Junction case			
Power transistor	R_{thJC}	1.0	K/W
Free-wheel diode	R_{thJC}	2.0	K/W

Characteristics

 $T_{case} = 25^\circ\text{C}$, unless otherwise specified

Collector cut-off current

 $V_{CE} = 1000\text{ V}$, $R_1 = 270\ \Omega$, $R_2 = 100\ \Omega$ $V_{CE} = 1000\text{ V}$, $R_1 = 270\ \Omega$, $R_2 = 100\ \Omega$ $T_{case} = 100^\circ\text{C}$

	Min.	Typ.	Max.
I_{CER}			1.0 mA
I_{CER}			4.5 mA

Emitter cut-off current

 $V_{EB} = 5\text{ V}$

T1, T2

I_{EBO}			1.0 mA
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Collector-emitter breakdown voltage

 $I_C = 750\text{ mA}$, $L = 125\text{ mH}$ $I_C = 1\text{ mA}$, $R_1 = 270\ \Omega$, $R_2 = 100\ \Omega$

$V_{(BR)CEO}$	700		V
$V_{(BR)CER}$	1000		V

Emitter-base breakdown voltage

 $I_E = 5\text{ mA}$

T1, T2

$V_{(BR)EBO}$	7		V
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Collector saturation voltage

 $I_C = 17\text{ A}$, $I_B = 1.4\text{ A}$ $I_C = 17\text{ A}$, $I_B = 1.4\text{ A}$, $T_{case} = 125^\circ\text{C}$

V_{CEsat}		1.4	V
V_{CEsat}			2.4 V

Base saturation voltage

 $I_C = 12\text{ A}$, $I_B = 0.35\text{ A}$ $I_C = 12\text{ A}$, $I_B = 0.35\text{ A}$, $T_{case} = 125^\circ\text{C}$

V_{BEsat}		1.85	V
V_{BEsat}			3.0 V

DC forward current transfer ratio

 $V_{CE} = 2\text{ V}$, $I_C = 12\text{ A}$

h_{FE}	35		
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T-33-35

Dynamic saturation voltage

$V_S = 300 \text{ V}$, $R_C = 25 \Omega$, $I_{B1} = 0.55 \text{ A}$,
 $T_{\text{case}} = 100^\circ \text{C}$

	Min.	Typ.	Max.
$3 \mu\text{s}$ $V_{\text{CEsatdyn.}}$		16	30 V
$5 \mu\text{s}$ $V_{\text{CEsatdyn.}}$		12	20 V

Collector-emitter working voltage

$I_{\text{CWOFF}} = 15 \text{ A}$, $I_{B1} = 1.4 \text{ A}$, $L = 12 \mu\text{H}$,
 $-V_{\text{BB}} = 7 \text{ V}$, $R_{\text{BB}} = 0.6 \Omega$, $V_S = 50 \text{ V}$

V_{CEW}	700	V
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Forward voltage of the diode

$I_F = 17 \text{ A}$, $T_{\text{case}} = 100^\circ \text{C}$

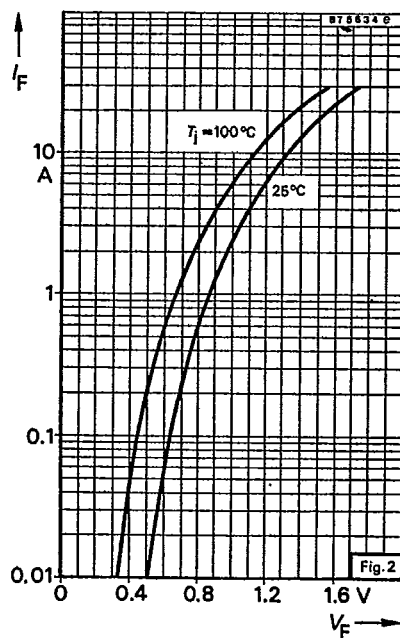
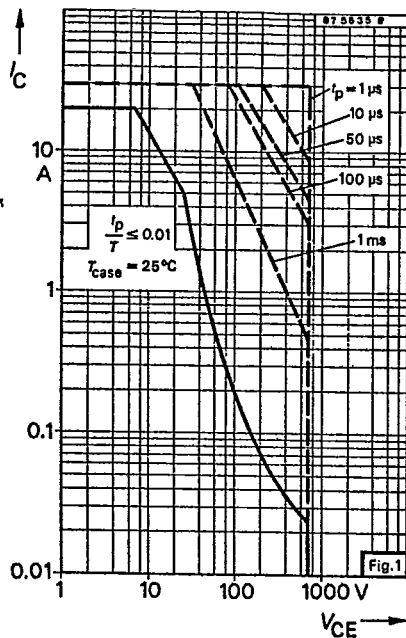
V_F	2.0	mA
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Switching characteristics

Inductive load, $T_{\text{case}} = 100^\circ \text{C}$

$I_C = 12 \text{ A}$, $I_{B1} = 0.35 \text{ A}$, $L = 0.2 \text{ mH}$, $V_{\text{clamp}} = 700 \text{ V}$,
 $-V_{\text{BB}} = 7 \text{ V}$, $R_{\text{BB}} = 0.6 \Omega$, $V_S = 50 \text{ V}$

Storage time	t_s	4.0	7.0 μs
Fall time	t_f	0.4	0.6 μs
Cross over time	t_c	1.0	1.4 μs



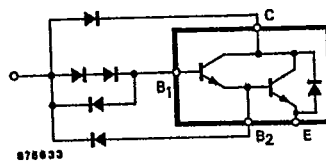
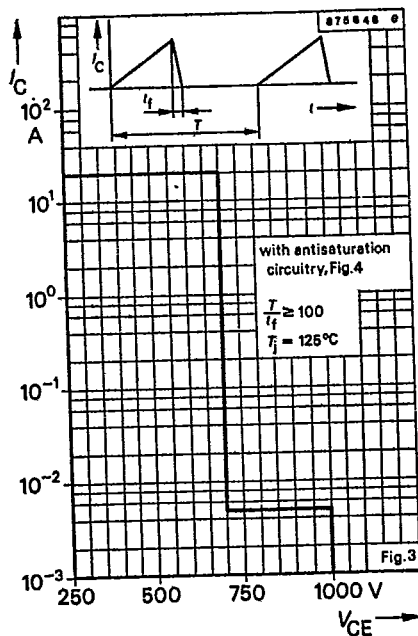
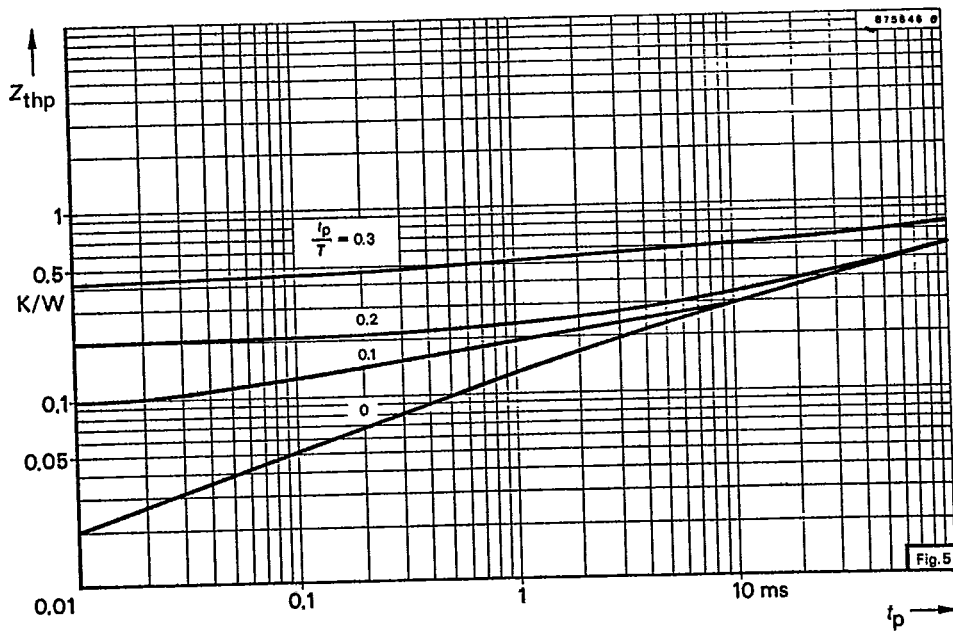


Fig. 4 Antisaturation circuitry



T-33.35

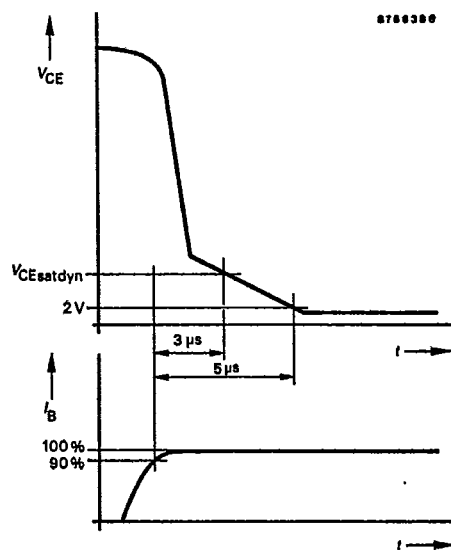
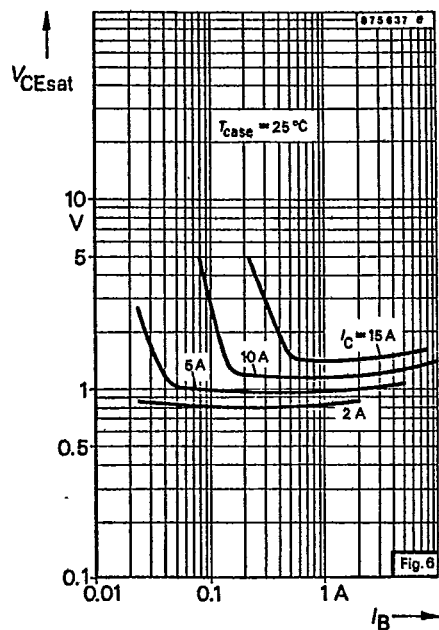
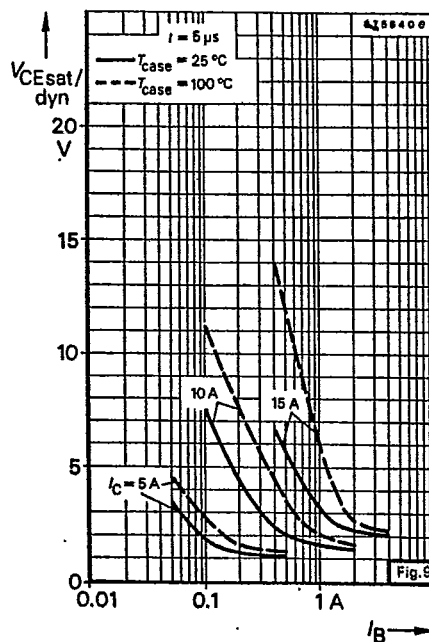
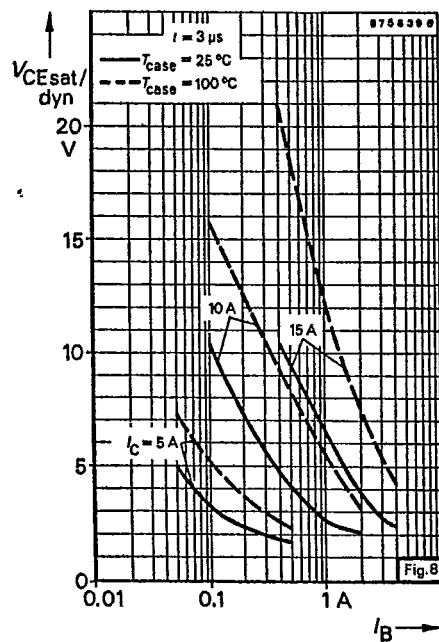
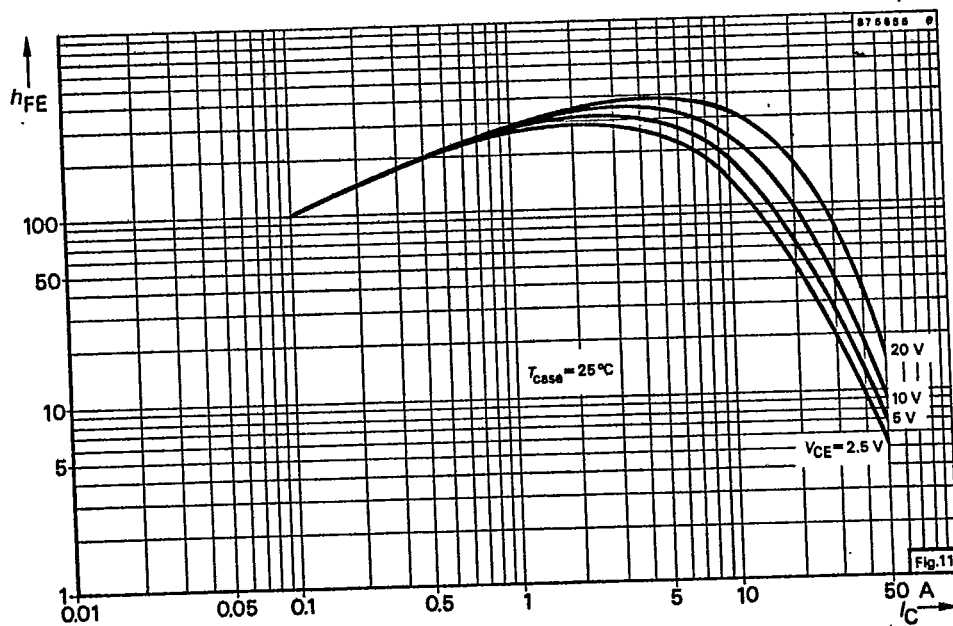
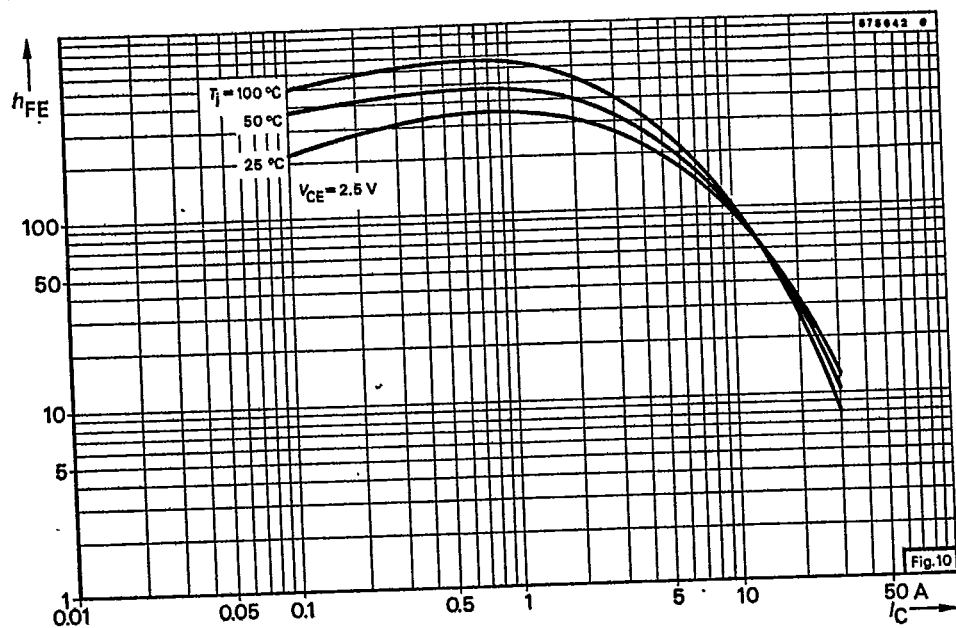
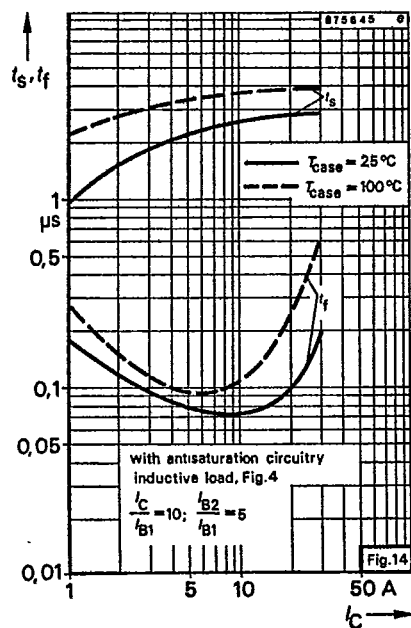
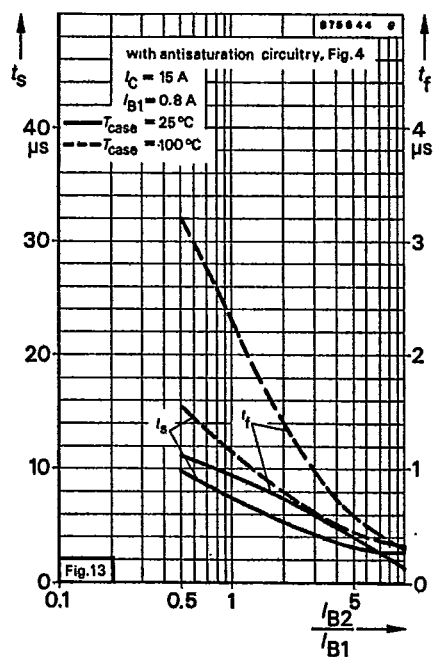
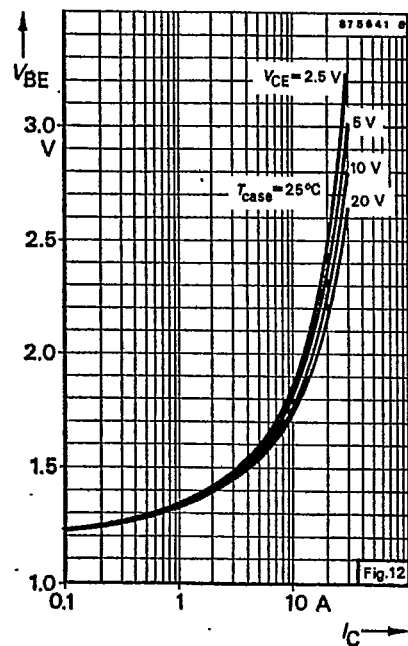


Fig. 7 $V_{CEsatdyn}$ -definition



T-33-35





T-91-20

● Family of curves

Besides the static (d. c.) and dynamic (a. c.) characteristics, family of curves are given for specified operating conditions. They show the typical interdependence of individual characteristics. Partly are given the scattering limits. They signify that at least 95% of the delivery lies inside these tolerances.

6.6. Additional informations

Preliminary specifications

This heading indicates that some information on the device concerned may be subject to slight changes.

Not for new developments

This heading indicates that the device concerned should not be used in equipment under development, it is, however, available for present production.

7. Taping and reeling

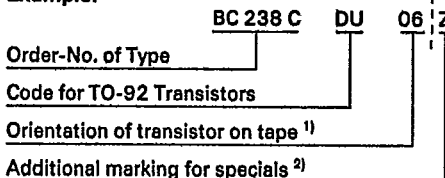
7.1. Taping of TO-92 transistors

Standard reeling: Taped on reel, reeled together with a paper film.

7.1.1. Order Numbers

Add the taping-code to the order number.

Example:



¹⁾ 06 = View on flat side of transistor, view on gummed tape

05 = View on round side of transistor, view on gummed tape

²⁾ Additional marking "O":

Taping without paper film

Additional marking "Z":

Zigzag folded tape in special box. Marking for orientation of transistor not necessary, because box can be opened on top or bottom.

Example for order No.: BC 237 C DU Z

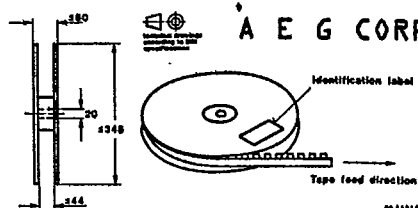


Fig. 7.1. Dimensions of reel in mm

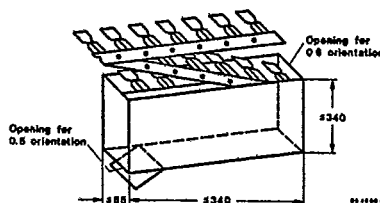


Fig. 7.2. Dimension of box for Zigzag folding in mm

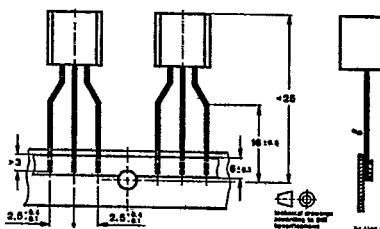


Fig. 7.3. Dimensions of tape in mm

7.1.2 Quantity of devices

1000 devices per reel

2000 devices per folded tape in special box.

7.2 Taped transistors in SOT 23 and SOT 143 case

a) Standard taping

Designation is attached with code GS 08 in case of standard taping. Example for normal version transistors as standard taped: BF 569-GS08.

Example for R-version transistors as standard taped: BF 569 R-GS 08.

In case of standard taping, the transistor orientation on the tape is shown in Fig. 7.4 and Fig. 7.5.

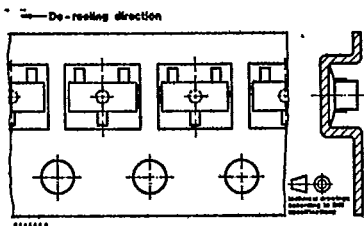


Fig. 7.4 Standard taped SOT 23

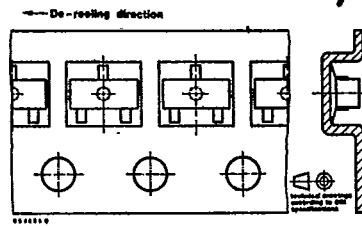


Fig. 7.6 Reverse taped SOT 23

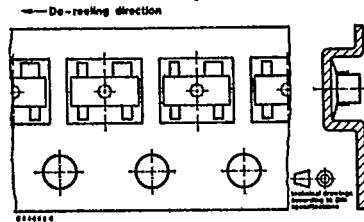


Fig. 7.5 Standard taped SOT 143

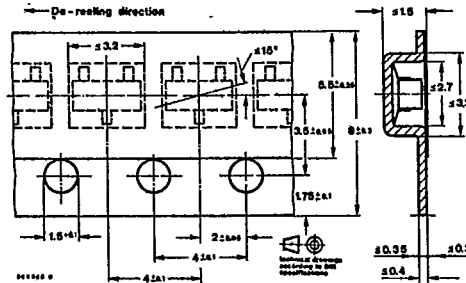


Fig. 7.7 Dimensions of tape in mm

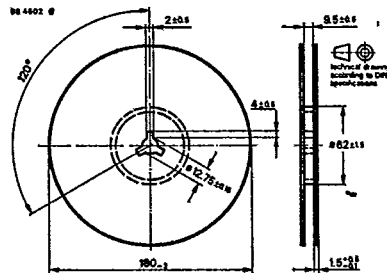


Fig. 7.8 Dimensions of reel in mm

b) Reverse taping

Designation is attached with code GS 07 in case of reverse taping. Example for normal version transistors as reverse taped: BF 569 R-GS 07.

Example for R-version transistors as reverse tapping: BF 569 R-GS 07.

In case of reverse taping, the transistor orientation on the tape is shown in Fig. 6. Regarding MOF-FET and MES-FET devices, reverse taping is at present not available.

8. Assessories

Number	Fig.	Designation	For case
119 880	8.1.	Isolating washer thickness 60 µm	12 A 3 DIN 41 869 JEDEC TO 126 (SOT 32)
564 542	8.2.	Isolating washer thickness 50 µm	14 A 3 DIN 41 869 JEDEC TO 220 (SOT 78)
912 884	8.3	Isolating washer thickness 50 µm	15 A 3 DIN 41 869 (TOP3) for clip mounting
191 131	8.4	Isolating washer thickness 50 µm	15 A 3 DIN 41 869 (TOP3) for screw mounting
191 140	8.5	Mounting clip	15 A 3 DIN 41 869 (TOP3)
569 524	8.6	Isolating washer thickness 100 µm + 50 µm	3B 2 DIN 41 872 JEDEC TO 3 Devices with high reverse voltage