

### **TDA7569LV**

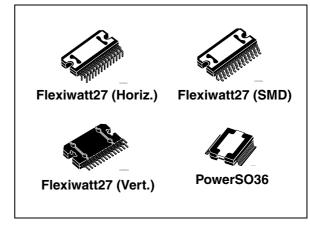
# 4 x 50 W power amplifier with full I<sup>2</sup>C diagnostics, high efficiency and low voltage operation

#### **Features**

- Multipower BCD technology
- MOSFET output power stage
- DMOS power output
- Class SB high efficiency
- High output power capability  $4x28 \text{ W}/4 \Omega$  @ 14.4 V, 1 kHz, 10% THD,  $4 \times 45 \text{ W}$  max power
- Max power 4 x 72 W / 2  $\Omega$
- Full I<sup>2</sup>C bus driving:
  - Standby
  - Independent front/rear soft play/mute
  - Selectable gain 26 dB /16 dB (for low noise line output function)
  - High efficiency enable/disable
  - I<sup>2</sup>C bus digital diagnostics (including DC bus AC load detection)
- Full fault protection
- DC offset detection
- Four independent short circuit protection
- Clipping detector pin with selectable threshold (2 %/10 %)
- Standby/mute pin
- Linear thermal shutdown with multiple thermal warning
- ESD protection
- Very robust against misconnections
- Improved SVR suppression during battery transients
- Capable to operate down to 6 V (e.g. "Startstop")

### **Description**

The TDA7569LV is the most advanced BCD technology quad bridge car radio amplifier of his family, including a wide range of innovative features.



The TDA7569LV is equipped with the most complete diagnostics array that communicates the status of each speaker through the I<sup>2</sup>C bus.

The dissipated output power under average listening condition is significantly reduced when compared to the conventional class AB solutions, thanks to the patented solution. Moreover it has been designed to be very robust against several kinds of misconnections.

It is moreover compliant to the most recent OEM specifications for low voltage operation (so called 'start-stop' battery profile during engine stop and re-start), helping car manufacturers to reduce the overall emissions and thus contributing to environment protection.

Table 1. Device summary

Order code	Package	Packing
TDA7569LVSM	Flexiwatt27	Tube
TDA7569LVSMTR	(SMD)	Tape and reel
TDA7569LV	Flexiwatt27 (vertical)	Tube
TDA7569LVH	Flexiwatt27 (horizontal)	Tube
TDA7569LVPD	PowerSO36	Tube
TDA7569LVPDTR	Fower3030	Tape and reel

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## 1 Block diagram and application circuits

Figure 1. Block diagram

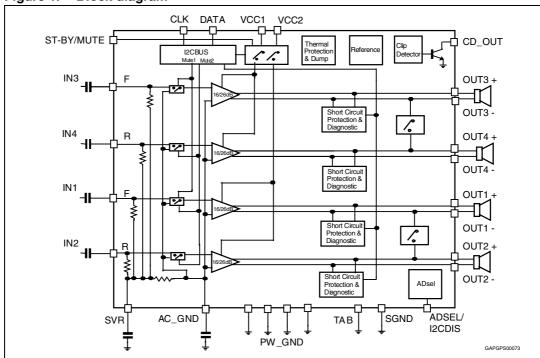
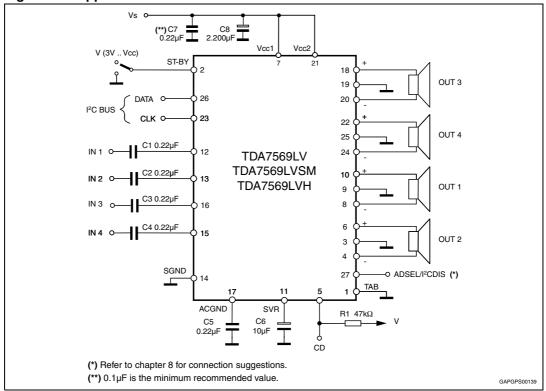


Figure 2. Application circuit



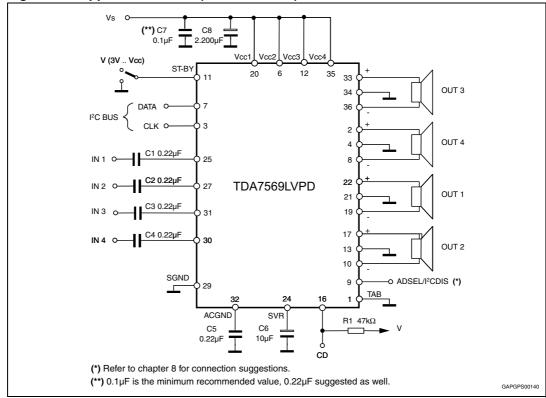


Figure 3. Application circuit (TDA7569LVPD)

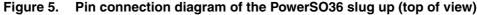
Pin description **TDA7569LV** 

#### Pin description 2

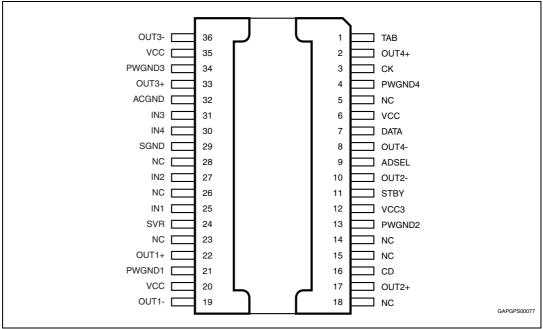
For channel name reference: CH1 = LF, CH2 = LR, CH3 = RF and CH4 = RR.

ADSEL/I2CDIS ADSEL/I2CDIS 26 DATA 26 DATA PWGND 4 25  $\blacksquare$ PWGND 4 25 24 OUT 4-OUT 4-24 СК П СК OUT 4+ OUT 4+ 21  $V_{\text{CC2}}$  $\mathbf{I}$ 21  $V_{CC2}$ 20 OUT 3-20 OUT 3-PWGND 3 19 19 PWGND 3 18 OUT 3+ 18 OUT 3+ 17 AC GND 17 AC GND 16 IN 3 16 IN 3 15 IN 4 15 14 SGND S\_GND 14 IN 2  $\blacksquare$ 13 13 IN 2 12 IN 1 12 IN 1 11 SVR 11 SVR OUT 1+ OUT 1+ 10 10 PWGND 1 PWGND 1 OUT 1-OUT 1-П V<sub>CC1</sub>  $V_{CC1}$ OUT 2+ OUT 2+ OUT 2-OUT 2- $\overline{\mathbb{I}}$ PWGND 2 3 PWGND 2 ■ STBY TAB TAB Flexiwatt27 (vertical) Flexiwatt27 (horizontal/SMD)

Pin connection diagram of the Flexiwatt27 (top of view)



GAPGPS00141



GAPGPS001

TDA7569LV Pin description

Table 2. Pin list description

Pin # (PowerSo36)	Pin # (Flexiwatt27)	Pin name	Function
1	1	TAB	-
2	22	OUT4+	Channel 4, + output
3	23	CK	I <sup>2</sup> C bus clock/HE selector
4	25	PWGND4	Channel 4 output power ground
5	-	NC	Not connected
6	21	VCC2	Supply voltage pin2
7	26	DATA	I <sup>2</sup> C bus data pin/gain selector
8	24	OUT4-	Channel 4, - output
9	27	ADSEL	Address selector pin/ I <sup>2</sup> C bus disable (legacy select)
10	4	OUT2-	Channel 2, - output
11	2	STBY	Standby pin
12	-	VCC3	Supply voltage pin3
13	3	PWGND2	Channel 2 output power ground
14	-	NC	Not connected
15	-	NC	Not connected
16	5	CD	Clip detector output pin
17	6	OUT2+	Channel 2, + output
18	-	NC	Not connected
19	8	OUT1-	Channel 1, - output
20	7	VCC1	Supply voltage pin1
21	9	PWGND1	Channel 1 output power ground
22	10	OUT1+	Channel 1, + output
23	•	NC	Not connected
24	11	SVR	SVR pin
25	12	IN1	Input pin, channel 1
26	•	NC	Not connected
27	13	IN2	Input pin, channel 2
28	-	NC	Not connected
29	14	SGND	Signal ground pin
30	15	IN4	Input pin, channel 4
31	16	IN3	Input pin, channel 3
32	17	AC GND	AC ground
33	18	OUT3+	Channel 3, + output
34	19	PWGND3	Channel 3 output power ground
35	-	VCC4	Supply voltage pin4
36	20	OUT3-	Channel 3, - output

## 3 Electrical specifications

## 3.1 Absolute maximum ratings

Table 3. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>op</sub>	Operating supply voltage	18	V
V <sub>S</sub>	DC supply voltage	28	V
V <sub>peak</sub>	Peak supply voltage (for t <sub>max</sub> = 50 ms)	50	V
GNDmax	Ground pins voltage	-0.3 to 0.3	V
V <sub>CK</sub> , V <sub>DATA</sub>	CK and DATA pin voltage	-0.3 to 6	V
V <sub>cd</sub>	Clip detector voltage	-0.3 to V <sub>op</sub>	٧
V <sub>stby</sub>	STBY pin voltage	-0.3 to V <sub>op</sub>	V
	Output peak current (not repetitive t <sub>max</sub> = 100ms)	8	Α
I <sub>O</sub>	Output peak current (repetitive f > 10 kHz)	6	A
P <sub>tot</sub>	Power dissipation T <sub>case</sub> = 70°C <sup>(1)</sup>	85	W
T <sub>stg</sub> , T <sub>j</sub>	Storage and junction temperature (2)	-55 to 150	°C
т	Operative temperature range (Flexiwatt package)	-40 to +105	°C
T <sub>amb</sub>	Operative temperature range (PowerSO package)	-40 to +85	

<sup>1.</sup> This is maximum theoretical value; for power dissipation in real application conditions, please refer to curves reported in *Section 3.4: Electrical characteristics curves*.

#### 3.2 Thermal data

Table 4. Thermal data

Symbol	Parameter	PowerSO	Flexiwatt	Unit
R <sub>th j-case</sub>	Thermal resistance junction-to-case Max.	1	1	°C/W

<sup>2.</sup> A suitable dissipation system should be used to keep Tj inside the specified limits.

### 3.3 Electrical characteristics

Refer to the test circuit, V  $_S$  = 14.4 V; R  $_L$  = 4  $\Omega;$  f = 1 kHz; G  $_V$  = 26 dB; T  $_{amb}$  = 25 °C; unless otherwise specified.

Table 5. Electrical characteristics

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
General cl	haracteristics					
Vs	Supply voltage range	-	6	-	18	V
I <sub>d</sub>	Total quiescent drain current	-	-	170	250	mA
R <sub>IN</sub>	Input impedance	-	45	60	70	kΩ
M	Min. accorded to the sea of a fell	IB1(D7) = 1	7	7.5	8	V
$V_{AM}$	Min. supply mute threshold	IB1(D7) = 0 (default) <sup>(1)</sup>	5	5.5	6	V
V <sub>OS</sub>	Offset voltage	Mute & play	-80	0	80	mV
V <sub>dth</sub>	Dump threshold	-	19	20	21	V
I <sub>SB</sub>	Standby current	V <sub>standby</sub> = 0	-	1	10	μΑ
SVR	Supply voltage rejection	$f = 100$ Hz to 10 kHz; $V_r = 1$ Vpk; $R_g = 600 \Omega$	50	55	-	dB
T <sub>ON</sub>	Turn on timing (Mute play transition)	D2/D1 (IB1) 0 to 1	-	25	40	ms
T <sub>OFF</sub>	Turn off timing (Play mute transition)	D2/D1 (IB1) 1 to 0	-	25	40	ms
TH <sub>WARN1</sub>	Average junction temperature for TH warning 1	DB1 (D7) = 1	-	155	-	
TH <sub>WARN2</sub>	Average junction temperature for TH warning 2	DB4 (D7) = 1	-	140	-	°C
TH <sub>WARN3</sub>	Average junction temperature for TH warning 3	DB4 (D6) = 1	-	125	-	
Audio per	formances					
		Max. power <sup>(2)</sup> $V_s = 14.4 \text{ V}, R_L = 4 \Omega$	-	45	-	W
ĺ		THD = 10 %, $R_L = 4 \Omega$	25	28		W
		THD = 1 %, $R_L = 4 \Omega$	20	22	-	W
$P_{O}$	Output power	$R_L = 2 \Omega$ ; THD 10 %	45	50		W
		$R_L = 2 \Omega$ ; THD 1 %	32	40	-	W
		$R_L = 2 \Omega$ ; Max. power <sup>(2)</sup> $V_s = 14.4 V$	60	75		W
		Max power@ $V_s = 6 \text{ V}$ , $R_L = 4 \Omega$	-	6	-	W
		P <sub>O</sub> = 1 W to 10 W; STD mode		0.035	0.1	%
		HE MODE; P <sub>O</sub> = 1.5 W	-	0.05	0.1	%
		HE MODE; P <sub>O</sub> = 8 W		0.1	0.5	%
THD	Total harmonic distortion	P <sub>O</sub> = 1-10 W, f = 10 kHz; STD mode	-	0.3	0.5	%
		$G_V = 16 \text{ dB}$ ; STD mode $V_O = 0.1 \text{ to 5 VRMS}$	-	0.02	0.05	%
C <sub>T</sub>	Cross talk	$f = 1 \text{ kHz to } 10 \text{ kHz}, R_g = 600 \Omega$	50	65	-	dB
		9		<u> </u>	1	L

Table 5. Electrical characteristics (continued)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
G <sub>V1</sub>	Voltage gain 1	-	25	26	27	dB
∆G <sub>V1</sub>	Voltage gain match 1	-	-1	•	1	dB
G <sub>V2</sub>	Voltage gain 2	-	15	16	17	dB
∆G <sub>V2</sub>	Voltage gain match 2	-	-1	-	1	dB
E <sub>IN1</sub>	Output noise voltage 1	$R_g$ = 600 $\Omega$ 20 Hz to 22 kHz	-	50	70	μV
E <sub>IN2</sub>	Output noise voltage 2	$R_g$ = 600 $\Omega$ ; GV = 16d B 20 Hz to 22 kHz	-	20	35	μV
BW	Power bandwidth	-	100	-	-	KHz
CMRR	Input CMRR	$V_{CM} = 1 \text{ Vpk-pk}; \text{ Rg} = 0 \Omega$	-	70	-	dB
AV/	During mute ON/OFF output offset voltage	ITU R-ARM weighted	-7.5	-	+7.5	mV
ΔV <sub>OS</sub>	During standby ON/OFF output offset voltage	(see Figure 21)	-7.5	-	+7.5	mV
Clip detec	etor					
CD <sub>LK</sub>	Clip det. high leakage current	CD off / V <sub>CD</sub> = 6 V	-	0	5	μΑ
CD <sub>SAT</sub>	Clip det sat. voltage	CD on; I <sub>CD</sub> = 1 mA	-	-	300	mV
CD	Clin det TUD level	D0 (IB1) = 1	5	10	15	%
CD <sub>THD</sub>	Clip det THD level	D0 (IB1) = 0	1	2	3	%
Control pi	n characteristics					
V <sub>SBY</sub>	Standby/mute pin for standby	-	0	-	1.2	V
V <sub>MU</sub>	Standby/mute pin for mute	-	2.9	-	3.5	V
V <sub>OP</sub>	Standby/mute pin for operating	-	4.5	-	18	V
	Ctondby/myto nin gurrant	V <sub>st-by/mute</sub> = 4.5 V	-	1	5	μΑ
I <sub>MU</sub>	Standby/mute pin current	V <sub>st-by/mute</sub> < 1.2 V	-	0	5	μΑ
A <sub>SB</sub>	Standby attenuation	-	90	110	-	dB
A <sub>M</sub>	Mute attenuation	-	80	100	-	dB
Turn on d	iagnostics 1 (Power amplifier mo	de)				
Pgnd	Short to GND det. (below this limit, the Output is considered in short circuit to GND)		-	-	1.2	V
Pvs	Short to Vs det. (above this limit, the output is considered in short circuit to Vs)		Vs -1.2	-	-	V
Pnop	Normal operation thresholds. (Within these limits, the output is considered without faults).	Power amplifier in standby	1.8	-	Vs -1.8	V
Lsc	Shorted load det.		-	-	0.5	Ω
Lop	Open load det.		85	-	-	Ω
Lnop	Normal load det.		1.5	-	45	Ω

Table 5. Electrical characteristics (continued)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit	
Turn on diagnostics 2 (Line driver mode)							
Pgnd	Short to GND det. (below this limit, the output is considered in short circuit to GND)	Power amplifier in standby	-	1	1.2	٧	
Pvs	Short to Vs det. (above this limit, the output is considered in short circuit to Vs)	-	Vs -1.2	1	-	>	
Pnop	Normal operation thresholds. (Within these limits, the output is considered without faults).	-	1.8	-	Vs -1.8	٧	
Lsc	Shorted load det.	-	-	-	1.5	Ω	
Lop	Open load det.	-	330	-	-	Ω	
Lnop	Normal load det.	-	7	-	180	Ω	
Permanen	t diagnostics 2 (Power amplifier	mode or line driver mode)					
Pgnd	Short to GND det. (below this limit, the Output is considered in short circuit to GND)		-	-	1.2	٧	
Pvs	Short to Vs det. (above this limit, the output is considered in short circuit to Vs)	Power amplifier in mute or play, one or more short circuits protection activated	Vs -1.2	-	-	٧	
Pnop	Normal operation thresholds. (Within these limits, the output is considered without faults).		1.8	-	Vs -1.8	٧	
-	Charted land dat	Power amplifier mode	-	-	0.5	Ω	
L <sub>SC</sub>	Shorted load det.	Line driver mode	-	-	1.5	Ω	
V <sub>O</sub>	Offset detection	Power amplifier in play, AC input signals = 0	±1.5	±2	±2.5	٧	
I <sub>NLH</sub>	Normal load current detection	\/ . (\/ E\p\ ID0 (D7) 0	500	-	-	mA	
I <sub>OLH</sub>	Open load current detection	$V_{O} < (V_{S}-5)pk, IB2 (D7) = 0$	-	-	250	mA	
I <sub>NLL</sub>	Normal load current detection	\/ . (\/ E\p\ ID0 (D7) 1	250	-	-	mA	
I <sub>OLL</sub>	Open load current detection	$V_{O} < (V_{S}-5)pk$ , IB2 (D7) = 1	-		125	mA	
I <sup>2</sup> C bus in	terface						
S <sub>CL</sub>	Clock frequency	-	-	-	400	kHz	
V <sub>IL</sub>	Input low voltage	-	-	-	1.5	V	
V <sub>IH</sub>	Input high voltage	-	2.3	-	-	V	

<sup>1.</sup> In legacy mode only low threshold option is available.

<sup>2.</sup> Saturated square wave output.

#### 3.4 Electrical characteristics curves

Figure 6. Quiescent current vs. supply voltage age Figure 7. Output power vs. supply voltage (4  $\Omega$ )

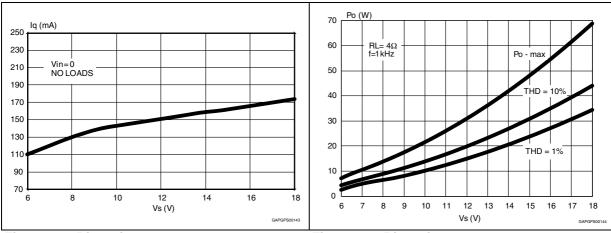


Figure 8. Distortion vs. output power (4  $\Omega$ , STD)

Figure 9. Distortion vs. output power  $(4 \Omega, HI-EFF)$ 

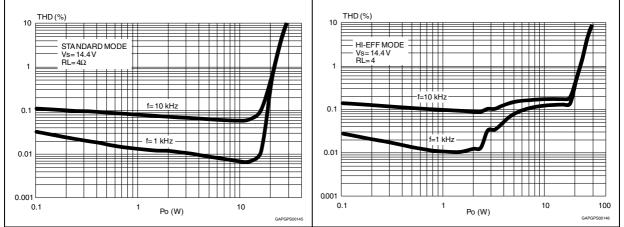
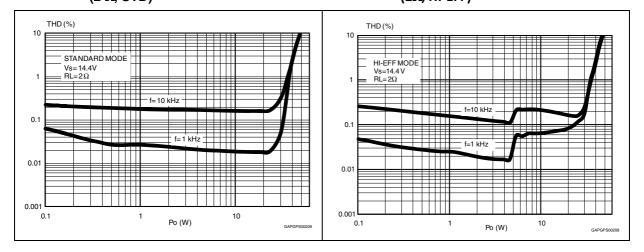


Figure 10. Distortion vs. output power (2  $\Omega$ , STD)

Figure 11. Distortion vs. output power  $(2\Omega, HI-EFF)$ 



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Figure 12. Distortion vs. frequency (2  $\Omega$ )

Figure 13. Distortion vs. output power  $V_s = 6 V$  (4  $\Omega$ , STD)

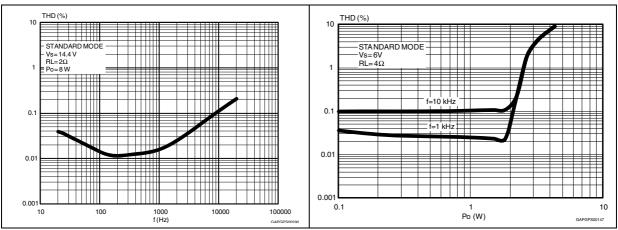


Figure 14. Distortion vs. frequency (4  $\Omega$ )

Figure 15. Crosstalk vs. frequency

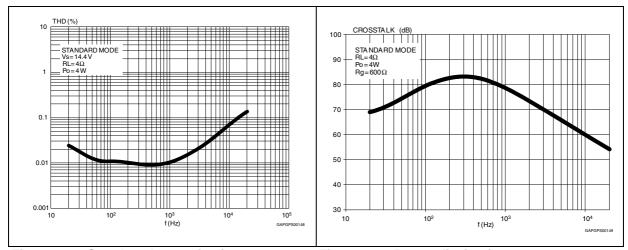


Figure 16. Supply voltage rejection vs. frequency

Figure 17. Power dissipation vs. average output power (audio program simulation, 2  $\Omega$ )

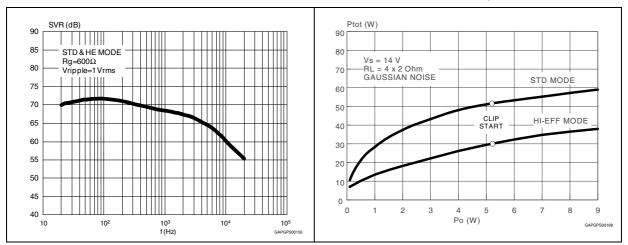
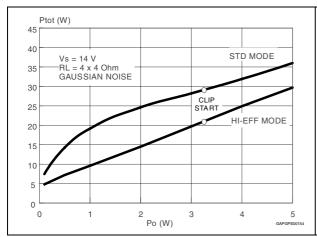


Figure 18. Power dissipation vs. average output power (audio program simulation, 4  $\Omega$ )

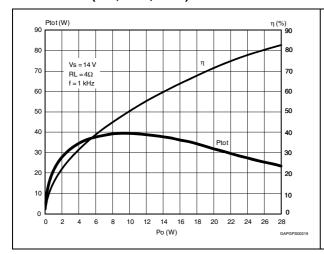
Figure 19. Total power dissipation and efficiency vs. output power (4  $\Omega$ , HI-EFF, Sine)

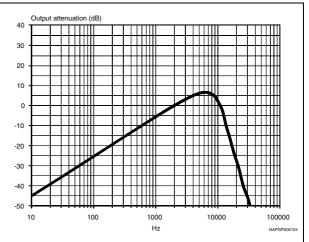


Ptot (W) 90 80 Vs=14 RL=4Ω f=1 KHz 70 70 HIEFF mod 50 50 40 40 30 30 20 20 10 100

Figure 20. Total power dissipation and efficiency vs. output power (4  $\Omega$ , STD, Sine)

Figure 21. ITU R-ARM frequency response, weighting filter for transient pop





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#### **Diagnostics functional description** 4

#### **Turn-on diagnostic** 4.1

It is strongly recommended to activate this feature at turn-on (standby out) with I<sup>2</sup>C bus request. Detectable output faults are:

- SHORT TO GND
- SHORT TO Vs
- SHORT ACROSS THE SPEAKER
- **OPEN SPEAKER**

To verify if any of the above misconnections are in place, a subsonic (inaudible) current pulse (Figure 22) is internally generated, sent through the speaker(s) and sunk back. The Turn On diagnostic status is internally stored until a successive diagnostic pulse is requested (after a I<sup>2</sup>C reading).

If the "standby out" and "diag. enable" commands are both given through a single programming step, the pulse takes place first (power stage still in stand-by mode, low, outputs= high impedance).

Afterwards, when the Amplifier is biased, the PERMANENT diagnostic takes place. The previous Turn On state is kept until a short appears at the outputs.

Figure 22. Turn-on diagnostic: working principle ~100ms t (ms)

Figure 23 and 24 show SVR and OUTPUT waveforms at the turn-on (stand-by out) with and without turn-on diagnostic.

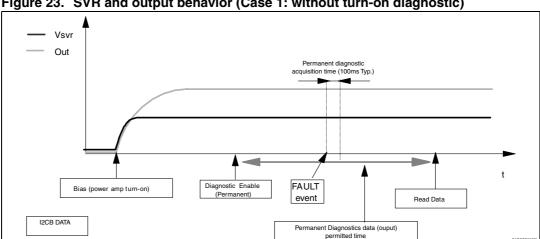


Figure 23. SVR and output behavior (Case 1: without turn-on diagnostic)

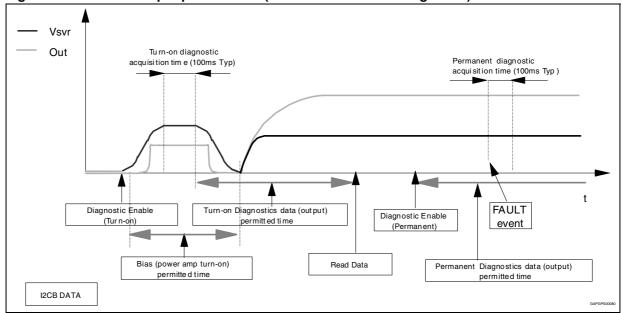
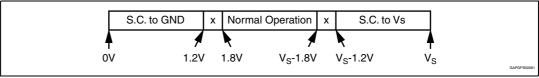


Figure 24. SVR and output pin behavior (Case 2: with turn-on diagnostic)

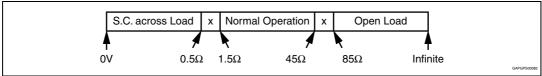
The information related to the outputs status is read and memorized at the end of the current pulse top. The acquisition time is 100 ms (typ.). No audible noise is generated in the process. As for SHORT TO GND / Vs the fault-detection thresholds remain unchanged from 26 dB to 16 dB gain setting. They are as follows:

Figure 25. Short circuit detection thresholds



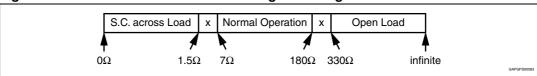
Concerning SHORT ACROSS THE SPEAKER / OPEN SPEAKER, the threshold varies from 26 dB to 16 dB gain setting, since different loads are expected (either normal speaker's impedance or high impedance). The values in case of 26 dB gain are as follows:

Figure 26. Load detection thresholds - high gain setting



If the Line-Driver mode (Gv= 16 dB and Line Driver Mode diagnostic = 1) is selected, the same thresholds will change as follows:

Figure 27. Load detection threshold - low gain setting



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#### 4.2 Permanent diagnostics

Detectable conventional faults are:

- Short to GND
- Short to Vs
- Short across the speaker

The following additional features are provided:

Output offset detection

The TDA7569LV has 2 operating statuses:

- RESTART mode. The diagnostic is not enabled. Each audio channel operates independently from each other. If any of the a.m. faults occurs, only the channel(s) interested is shut down. A check of the output status is made every 1 ms (*Figure 28*). Restart takes place when the overload is removed.
- 2. DIAGNOSTIC mode. It is enabled via I<sup>2</sup>C bus and self activates if an output overload (such to cause the intervention of the short-circuit protection) occurs to the speakers outputs. Once activated, the diagnostics procedure develops as follows (*Figure 29*):
  - To avoid momentary re-circulation spikes from giving erroneous diagnostics, a check of the output status is made after 1ms: if normal situation (no overloads) is detected, the diagnostic is not performed and the channel returns back active.
  - Instead, if an overload is detected during the check after 1 ms, then a diagnostic cycle having a duration of about 100 ms is started.
  - After a diagnostic cycle, the audio channel interested by the fault is switched to RESTART mode. The relevant data are stored inside the device and can be read by the microprocessor. When one cycle has terminated, the next one is activated by an I<sup>2</sup>C reading. This is to ensure continuous diagnostics throughout the carradio operating time.
  - To check the status of the device a sampling system is needed. The timing is chosen at microprocessor level (over half a second is recommended).

Figure 28. Restart timing without diagnostic enable (permanent) - Each 1 mS time, a sampling of the fault is done

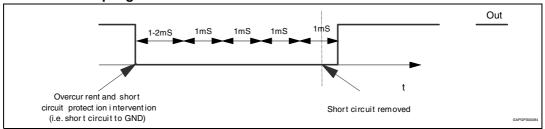
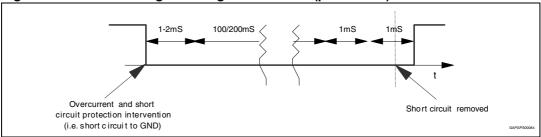


Figure 29. Restart timing with diagnostic enable (permanent)



#### 4.3 Output DC offset detection

Any DC output offset exceeding ±2 V is signalled out. This inconvenient might occur as a consequence of initially defective or aged and worn-out input capacitors feeding a DC component to the inputs, so putting the speakers at risk of overheating.

This diagnostic has to be performed with low-level output AC signal (or Vin = 0).

The test is run with selectable time duration by microprocessor (from a "start" to a "stop" command):

- START = Last reading operation or setting IB1 D5 (OFFSET enable) to 1
- STOP = Actual reading operation

Excess offset is signalled out if persistent throughout the assigned testing time. This feature is disabled if any overloads leading to activation of the short-circuit protection occurs in the process.

#### 4.4 AC diagnostic

It is targeted at detecting accidental disconnection of tweeters in 2-way speaker and, more in general, presence of capacitive (AC) coupled loads.

This diagnostic is based on the notion that the overall speaker's impedance (woofer + parallel tweeter) will tend to increase towards high frequencies if the tweeter gets disconnected, because the remaining speaker (woofer) would be out of its operating range (high impedance). The diagnostic decision is made according to peak output current thresholds, and it is enabled by setting (IB2-D2) = 1. Two different detection levels are available:

- High current threshold IB2 (D7) = 0
   lout > 500 mApk = normal status
   lout < 250 mApk = open tweeter</li>
- Low current threshold IB2 (D7) = 1
   lout > 250 mApk = normal status
   lout < 125 mApk = open tweeter</li>

To correctly implement this feature, it is necessary to briefly provide a signal tone (with the amplifier in "play") whose frequency and magnitude are such to determine an output current higher than 500 mApk with IB2(D7) = 0 (higher than 250 mApk with IB2(D7) = 1) in normal conditions and lower than 250 mApk with IB2(D7) = 0 (lower than 125 mApk with IB2(D7) = 1) should the parallel tweeter be missing.

The test has to last for a minimum number of 3 sine cycles starting from the activation of the AC diagnostic function IB2<D2>) up to the I<sup>2</sup>C reading of the results (measuring period). To confirm presence of tweeter, it is necessary to find at least 3 current pulses over the above threadless over all the measuring period, else an "open tweeter" message will be issued.

The frequency / magnitude setting of the test tone depends on the impedance characteristics of each specific speaker being used, with or without the tweeter connected (to be calculated case by case). High-frequency tones (> 10 kHz) or even ultrasonic signals are recommended for their negligible acoustic impact and also to maximize the impedance module's ratio between with tweeter-on and tweeter-off.

*Figure 30* shows the load impedance as a function of the peak output voltage and the relevant diagnostic fields.

This feature is disabled if any overloads leading to activation of the short-circuit protection occurs in the process.

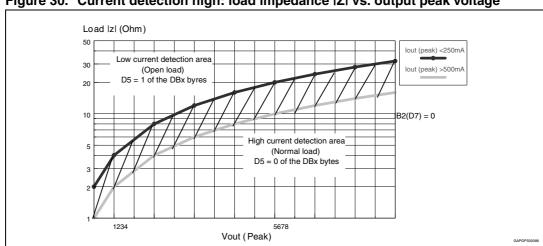
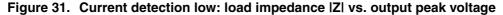
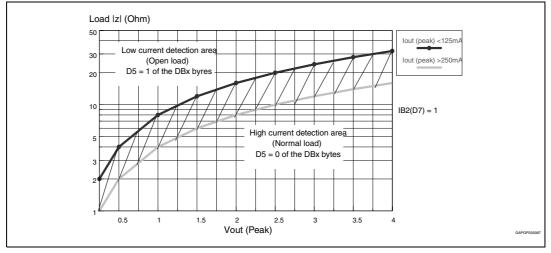


Figure 30. Current detection high: load impedance |Z| vs. output peak voltage





Multiple faults TDA7569LV

### 5 Multiple faults

When more misconnections are simultaneously in place at the audio outputs, it is guaranteed that at least one of them is initially read out. The others are notified after successive cycles of I<sup>2</sup>C reading and faults removal, provided that the diagnostic is enabled. This is true for both kinds of diagnostic (Turn on and Permanent).

The table below shows all the couples of double-fault possible. It should be taken into account that a short circuit with the 4 ohm speaker unconnected is considered as double fault.

	S. GND	S. Vs	S. Across L.	Open L.
S. GND	S. GND	S. Vs + S. GND S. GND S.		S. GND
S. Vs	/	S. Vs	S. Vs	S. Vs
S. Across L.	/	/	S. Across L.	N.A.
Open L.	/	/	/	Open L. (*)

Table 6. Double fault table for turn on diagnostic

In Permanent Diagnostic the table is the same, with only a difference concerning Open Load(\*), which is not among the recognizable faults. Should an Open Load be present during the device's normal working, it would be detected at a subsequent Turn on Diagnostic cycle (i.e. at the successive Car Radio Turn on).

#### 5.1 Faults availability

All the results coming from  $I^2C$  bus, by read operations, are the consequence of measurements inside a defined period of time. If the fault is stable throughout the whole period, it will be sent out.

To guarantee always resident functions, every kind of diagnostic cycles (Turn on, Permanent, Offset) will be reactivate after any  $I^2C$  reading operation. So, when the micro reads the  $I^2C$ , a new cycle will be able to start, but the read data will come from the previous diag. cycle (i.e. The device is in Turn On state, with a short to Gnd, then the short is removed and micro reads  $I^2C$ . The short to Gnd is still present in bytes, because it is the result of the previous cycle. If another  $I^2C$  reading operation occurs, the bytes do not show the short). In general to observe a change in Diagnostic bytes, two  $I^2C$  reading operations are necessary.

TDA7569LV Thermal protection

### 6 Thermal protection

Thermal protection is implemented through thermal foldback (Figure 32).

Thermal foldback begins limiting the audio input to the amplifier stage as the junction temperatures rise above the normal operating range. This effectively limits the output power capability of the device thus reducing the temperature to acceptable levels without totally interrupting the operation of the device.

The output power will decrease to the point at which thermal equilibrium is reached. Thermal equilibrium will be reached when the reduction in output power reduces the dissipated power such that the die temperature falls below the thermal foldback threshold. Should the device cool, the audio level will increase until a new thermal equilibrium is reached or the amplifier reaches full power. Thermal foldback will reduce the audio output level in a linear manner.

Three thermal warning are available through the I<sup>2</sup>C bus data. After thermal shut down threshold is reached, the CD could toggle (as shown in *Figure 32*) or stay low, depending on signal level.

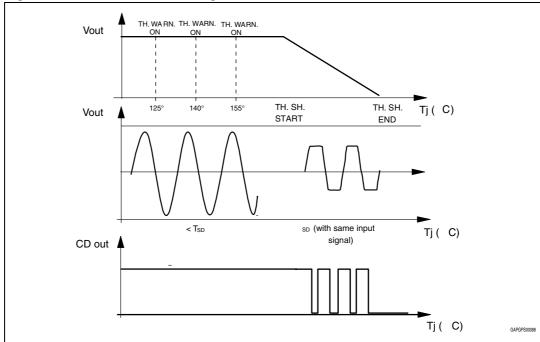


Figure 32. Thermal foldback diagram

### 6.1 Fast muting

The muting time can be shortened to less than 1.5ms by setting (IB2) D5 = 1. This option can be useful in transient battery situations (i.e. during car engine cranking) to quickly turnoff the amplifier for avoiding any audible effects caused by noise/transients being injected by preamp stages. The bit must be set back to "0" shortly after the mute transition.

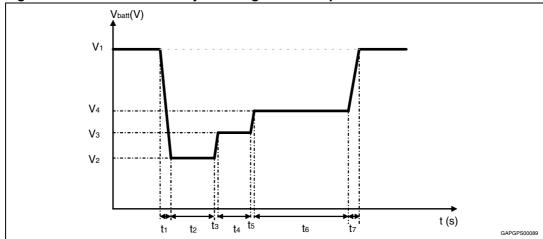
### 7 Battery transitions management

#### 7.1 Low voltage operation ("start stop")

The most recent OEM specification are requiring automatic stop of car engine at traffic light, in order to reduce emissions of polluting substances. The TDA7569LV, thanks to its innovating design, allows a continuous operation when battery falls down to 6/7V during such conditions, without producing pop noise. The maximum system power will be reduced accordingly.

Worst case battery cranking curves are shown below, indicating the shape and durations of allowed battery transitions.

Figure 33. Worts case battery cranking curve sample 1



V1 = 12 V; V2 = 6 V; V3 = 7 V; V4 = 8 V

t1 = 2 ms; t2 = 50 ms; t3 = 5 ms; t4 = 300 ms; t5 = 10 ms; t6 = 1 s; t7 = 2 ms

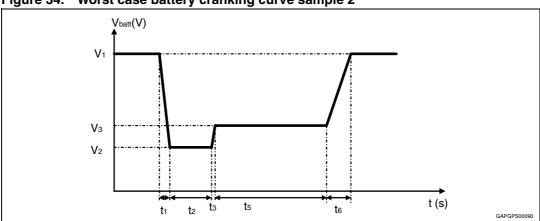


Figure 34. Worst case battery cranking curve sample 2

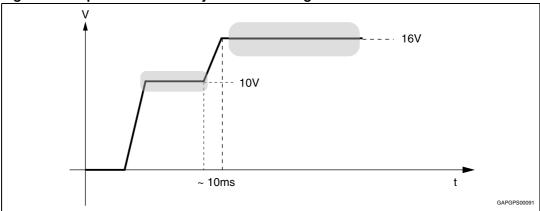
V1 = 12 V; V2 = 6 V; V3 = 7 V

t1 = 2 ms; t2 = 5 ms; t3 = 15 ms; t5 = 1 s; t6 = 50 ms

## 7.2 Advanced battery management

In addition to compatibility with low  $V_{batt}$ , the TDA7569LV is able to substain upwards fast battery transitions (like the one showed in Figure 19) without causing unwanted audible effect, thanks to the innovative circuit topology.





I<sup>2</sup>C bus TDA7569LV

### 8 I<sup>2</sup>C bus

## 8.1 I<sup>2</sup>C programming/reading sequences

A correct turn on/off sequence respectful of the diagnostic timings and producing no audible noises could be as follows (after battery connection):

- TURN-ON: PIN2 > 4.5V --- 10ms --- (STAND-BY OUT + DIAG ENABLE) --- 1 s (min) --- MUTING OUT
- TURN-OFF: MUTING IN --- 20 ms --- (DIAG DISABLE + STAND-BY IN) --- 10ms -- PIN2 = 0
- Car Radio Installation: PIN2 > 4.5V --- 10ms DIAG ENABLE (write) --- 200 ms --- I<sup>2</sup>C read (repeat until All faults disappear).
- OFFSET TEST: Device in Play (no signal) -- OFFSET ENABLE 30ms I<sup>2</sup>C reading (repeat I<sup>2</sup>C reading until high-offset message disappears).

#### 8.2 Address selection and I<sup>2</sup>C disable

When the ADSEL/I2CDIS pin is left open the I<sup>2</sup>C bus is disabled and the device can be controlled by the STBY/MUTE pin.

In this status (no -  $I^2$ C bus) the CK pin enables the HIGH-EFFICIENCY MODE (0 = STD MODE; 1 = HE MODE) and the DATA pin sets the gain (0 = 26 dB; 1 = 16 dB).

When the ADSEL/I2CDIS pin is connected to GND the I<sup>2</sup>C bus is active with address <1101100-x>.

To select the other I<sup>2</sup>C address a resistor must be connected to ADSEL/I2CDIS pin as following:

 $0 < R < 2 \text{ k}\Omega$ : I<sup>2</sup>C bus active with address <1101100x>

 $14 < R < 24 \text{ k}\Omega$ : I<sup>2</sup>C bus active with address <1101101x>

 $40 < R < 70 \text{ k}\Omega$ : I<sup>2</sup>C bus active with address <1101110x>

 $R > 120 \text{ k}\Omega$ : Legacy mode

(x: read/write bit sector)

### 8.3 I<sup>2</sup>C bus interface

Data transmission from microprocessor to the TDA7569LV and viceversa takes place through the 2 wires I<sup>2</sup>C bus interface, consisting of the two lines SDA and SCL (pull-up resistors to positive supply voltage must be connected).

#### 8.3.1 Data validity

As shown by *Figure 36*, the data on the SDA line must be stable during the high period of the clock. The HIGH and LOW state of the data line can only change when the clock signal on the SCL line is LOW.

TDA7569LV I<sup>2</sup>C bus

#### 8.3.2 Start and stop conditions

As shown by *Figure 37* a start condition is a HIGH to LOW transition of the SDA line while SCL is HIGH. The stop condition is a LOW to HIGH transition of the SDA line while SCL is HIGH.

#### 8.3.3 Byte format

Every byte transferred to the SDA line must contain 8 bits. Each byte must be followed by an acknowledge bit. The MSB is transferred first.

#### 8.3.4 Acknowledge

The transmitter\* puts a resistive HIGH level on the SDA line during the acknowledge clock pulse (see *Figure 38*). The receiver\*\* the acknowledges has to pull-down (LOW) the SDA line during the acknowledge clock pulse, so that the SDA line is stable LOW during this clock pulse.

#### \* Transmitter

- master (μP) when it writes an address to the TDA7569LV
- slave (TDA7569LV) when the  $\mu P$  reads a data byte from TDA7569LV

#### \*\* Receiver

- slave (TDA7569LV) when the μP writes an address to the TDA7569LV
- master (μP) when it reads a data byte from TDA7569LV

Figure 36. Data validity on the I<sup>2</sup>C bus

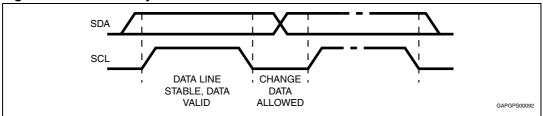


Figure 37. Timing diagram on the I<sup>2</sup>C bus

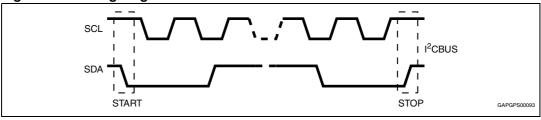
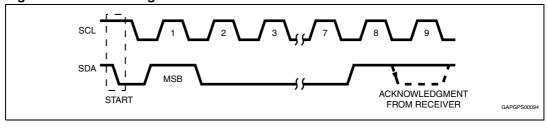


Figure 38. Acknowledge on the I<sup>2</sup>C bus

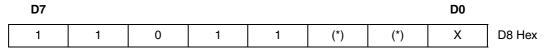


## 9 Software specifications

All the functions of the TDA7569LV are activated by  $I^2C$  interface.

The bit 0 of the "ADDRESS BYTE" defines if the next bytes are write instruction (from  $\mu P$  to TDA7569LV) or read instruction (from TDA7569LV to  $\mu P$ ).

#### Chip address



X = 0 Write to device

X = 1 Read from device

If R/W = 0, the  $\mu$ P sends 2 "Instruction Bytes": IB1 and IB2.

(\*) address selector bit, please refer to address selection description on Chapter 8.2.

Table 7. IB1

Bit	Instruction decoding bit
D7	Supply transition mute threshold high (D7 = 1) Supply transition mute threshold low (D7 = 0)
D6	Diagnostic enable (D6 = 1) Diagnostic defeat (D6 = 0)
D5	Offset Detection enable (D5 = 1) Offset Detection defeat (D5 = 0)
D4	Front Channel (CH1, CH3)  Gain = 26 dB (D4 = 0)  Gain = 16 dB (D4 = 1)
D3	Rear Channel (CH2, CH4)  Gain = 26 dB (D3 = 0)  Gain = 16 dB (D3 = 1)
D2	Mute front channels (D2 = 0) Unmute front channels (D2 = 1)
D1	Mute rear channels (D1 = 0) Unmute rear channels (D1 = 1)
D0	CD 2% (D0 = 0) CD 10% (D0 = 1)

Table 8. IB2

Bit	Instruction decoding bit
D7	Current detection threshold High th (D7 = 0) Low th (D7 =1)
D6	0
D5	Normal muting time (D5 = 0) Fast muting time (D5 = 1)
D4	Stand-by on - Amplifier not working - (D4 = 0) Stand-by off - Amplifier working - (D4 = 1)
D3	Power amplifier mode diagnostic (D3 = 0) Line driver mode diagnostic (D3 = 1)
D2	Current Detection Diagnostic Enabled (D2 =1) Current Detection Diagnostic Defeat (D2 =0)
D1	Right Channel Power amplifier working in standard mode (D1 = 0) Power amplifier working in high efficiency mode (D1 = 1)
D0	Left Channel Power amplifier working in standard mode (D0 = 0) Power amplifier working in high efficiency mode (D0 = 1)

If R/W = 1, the TDA7569LV sends 4 "Diagnostics Bytes" to  $\mu P$ : DB1, DB2, DB3 and DB4.

Table 9. DB1

Bit	Instruction d	ecoding bit
D7	Thermal warning 1 active (D7 = 1), T <sub>j</sub> =155°C	-
D6	Diag. cycle not activated or not terminated (D6 = 0) Diag. cycle terminated (D6 = 1)	-
D5	Channel LF (CH1) Current detection IB2 (D7) = 0 Output peak current < 250 mA - Open load (D5 = 1) Output peak current > 500 mA - Normal load (D5 = 0)	Channel LF (CH1) Current detection IB2 (D7) = 1 Output peak current < 125 mA - Open load (D5 = 1) Output peak current > 250 mA - Normal load (D5 = 0)
D4	Channel LF (CH1) Turn-on diagnostic (D4 = 0) Permanent diagnostic (D4 = 1)	-
D3	Channel LF (CH1) Normal load (D3 = 0) Short load (D3 = 1)	-
D2	Channel LF (CH1) Turn-on diag.: No open load (D2 = 0) Open load detection (D2 = 1) Offset diag.: No output offset (D2 = 0) Output offset detection (D2 = 1)	-

### Table 9. DB1 (continued)

Bit	Instruction de	ecoding bit
D1	Channel LF (CH1) No short to Vcc (D1 = 0) Short to Vcc (D1 = 1)	-
D0	Channel LF (CH1) No short to GND (D1 = 0) Short to GND (D1 = 1)	-

#### Table 10. DB2

Bit	Instruction	decoding bit
D7	Offset detection not activated (D7 = 0) Offset detection activated (D7 = 1)	-
D6	Х	-
D5	Channel LR (CH2) Current detection IB2 (D7) = 0 Output peak current < 250 mA - Open load (D5 = 1) Output peak current > 500 mA - Normal load (D5 = 0)	
D4	Channel LR (CH2) Turn-on diagnostic (D4 = 0) Permanent diagnostic (D4 = 1)	-
D3	Channel LR (CH2) Normal load (D3 = 0) Short load (D3 = 1)	-
D2	Channel LR (CH2) Turn-on diag.: No open load (D2 = 0) Open load detection (D2 = 1) Permanent diag.: No output offset (D2 = 0) Output offset detection (D2 = 1)	-
D1	Channel LR (CH2) No short to Vcc (D1 = 0) Short to Vcc (D1 = 1)	-
D0	Channel LR (CH2) No short to GND (D1 = 0) Short to GND (D1 = 1)	-

Table 11. DB3

Bit	Instruction	decoding bit
D7	Standby status (= IB2 - D4)	-
D6	Diagnostic status (= IB1 - D6)	-
D5	Channel RF (CH3) Current detection IB2 (D7) = 0 Output peak current < 250 mA - Open load (D5 = 1) Output peak current > 500 mA - Normal load (D5 = 0)	/
D4	Channel RF (CH3) Turn-on diagnostic (D4 = 0) Permanent diagnostic (D4 = 1)	-
D3	Channel RF (CH3) Normal load (D3 = 0) Short load (D3 = 1)	-
D2	Channel RF (CH3) Turn-on diag.: No open load (D2 = 0) Open load detection (D2 = 1) Permanent diag.: No output offset (D2 = 0) Output offset detection (D2 = 1)	-
D1	Channel RF (CH3) No short to Vcc (D1 = 0) Short to Vcc (D1 = 1)	-
D0	Channel RF (CH3) No short to GND (D1 = 0) Short to GND (D1 = 1)	-

Table 12. DB4

Bit	Instruction d	ecoding bit
D7	Thermal warning 2 active (D7 = 1), T <sub>j</sub> = 140°C	-
D6	Thermal warning 3 active (D6 = 1) T <sub>j</sub> = 125°C	-
D5	Channel RR (CH4) Current detection IB2 (D7) = 0 Output peak current < 250 mA - Open load (D5 = 1) Output peak current > 500 mA - Normal load (D5 = 0)	Channel RR (CH4) Current detection IB2 (D7) = 1 Output peak current < 125 mA - Open load (D5 = 1) Output peak current > 250 mA - Normal load (D5 = 0)
D4	Channel RR (CH4) Turn-on diagnostic (D4 = 0) Permanent diagnostic (D4 = 1)	-
D3	Channel R (CH4) R Normal load (D3 = 0) Short load (D3 = 1)	-
D2	Channel RR (CH4) Turn-on diag.: No open load (D2 = 0) Open load detection (D2 = 1) Permanent diag.: No output offset (D2 = 0) Output offset detection (D2 = 1)	-
D1	Channel RR (CH4) No short to Vcc (D1 = 0) Short to Vcc (D1 = 1)	-
D0	Channel RR (CH4) No short to GND (D1 = 0) Short to GND (D1 = 1)	-

## 10 Examples of bytes sequence

1 - Turn-On diagnostic - Write operation

Start Address byte with D0 = 0	ACK	IB1 with D6 = 1	ACK	IB2	ACK	STOP
--------------------------------	-----	-----------------	-----	-----	-----	------

2 - Turn-On diagnostic - Read operation

Start Ac	ddress byte with D0 = 1	ACK	DB1	ACK	DB2	ACK	DB3	ACK	DB4	ACK	STOP
----------	-------------------------	-----	-----	-----	-----	-----	-----	-----	-----	-----	------

The delay from 1 to 2 can be selected by software, starting from 1ms

3a - Turn-On of the power amplifier with 26dB gain, mute on, diagnostic defeat, CD = 2%

Start	Address byte with D0 = 0	ACK	IB1	ACK	IB2	ACK	STOP
			X0000000		XXX1XX11		

3b - Turn-Off of the power amplifier

Start	Address byte with D0 = 0	ACK	IB1	ACK	IB2	ACK	STOP
			X0XXXXXX		XXX0XXXX		

4 - Offset detection procedure enable

Start	Address byte with D0 = 0	ACK	IB1	ACK	IB2	ACK	STOP
			XX1XX11X		XXX1XXXX		

**5** - Offset detection procedure stop and reading operation (the results are valid only for the offset detection bits (D2 of the bytes DB1, DB2, DB3, DB4)

Start   Address byte with D0 = 1   ACK   DB1   ACK   DB2   ACK   DB3   ACK   DB4   ACK   STO
--

- The purpose of this test is to check if a D.C. offset (2V typ.) is present on the outputs, produced by input capacitor with anomalous leakage current or humidity between pins.
- The delay from 4 to 5 can be selected by software, starting from 1ms

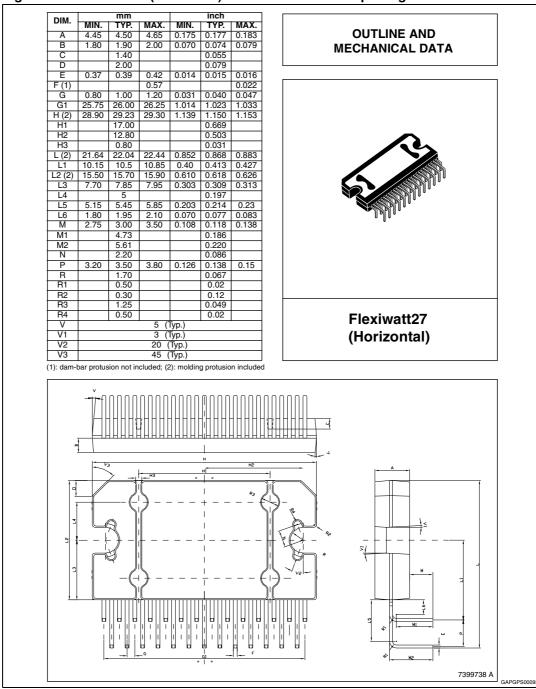
Package information TDA7569LV

### 11 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>.

ECOPACK® is an ST trademark.

Figure 39. Flexiwatt27 (horizontal) mechanical data and package dimensions



TDA7569LV Package information

Figure 40. Flexiwatt27 (vertical) mechanical data and package dimensions

MIN.   TYP.   MAX.   MIN.   TYP.   MAX.   A   4.5   4.50   4.56   0.175   0.177   0.183	DIM	1	mm			inch		
Name	DIM.		TYP.			TYP.		OUTLINE AND
C						0.177		
D 0.75 0.90 1.05 0.029 0.035 0.041 E 0.37 0.39 0.42 0.014 0.015 0.016 F(1) 0.57 0.00 1.20 0.031 0.040 0.047 G 0.80 1.00 1.20 0.031 0.040 0.047 G1 25.75 26.00 26.25 1.014 1.023 1.033 H(2) 28.90 29.23 29.30 1.139 1.150 1.153 H1 17.00 1 0.669 H2 12.80 0 0.031 H3 0.80 0 0.031 L(2) 22.07 22.47 22.87 0.869 0.884 0.904 L1 18.57 18.97 19.37 0.731 0.747 0.762 L2 (2) 15.50 15.70 15.90 0.610 0.618 0.626 13 7.70 7.85 7.95 0.303 0.309 0.313 L4 5 0.197 L5 3.5 0.197 L5 3.5 0.197 N 2.20 0.086 0.02 R1 1.70 0.067 R1 0.5 0.02 R2 0.3 0.12 R3 1.25 0.049 R4 0.50 0 0.099 R8 1.70 0.009 R9 1.70 0.009 R9 1.70 0.009 R1 0.50 0.009 R1 0.50 0.009 R1 0.50 0.009 R1 0.50 0.009 R2 20 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 3 (Typ.) V3 4 45 (Typ.) V1 3 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 3 (Typ.) V3 H1 H1 H2  C1 1.05 0.009 R2 20 (Typ.) V3 3 (Typ.) V4 3 (Typ.) V5 (Typ.) V6 (Typ.) V7 3 (Typ.) V8 1 3 (Typ.) V9 1 3 (Typ.) V9 1 3 (Typ.) V9 1 3 (Typ.) V1 3 (Typ.) V1 3 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 4 4 (Typ.)		1.80		2.00	0.070		0.079	MECHANICAL DATA
E 0.37 0.39 0.42 0.014 0.015 0.016 F(1) 0.80 1.00 1.20 0.031 0.040 0.047 G1 25.75 26.00 26.25 1.014 1.023 1.033 H(2) 28.90 29.23 29.30 1.139 1.150 1.153 H1 17.00 0.666 H2 12.80 0.5.03 H3 0.80 0.0.031 L(2) 22.07 22.47 22.87 0.869 0.884 0.904 L1 18.57 18.97 19.37 0.731 0.747 0.762 L2 (2) 15.50 15.70 15.90 0.610 0.618 0.626 L3 7.70 7.85 7.95 0.303 0.309 0.313 L4 5 5 0.0197 L5 3.5 0.138 M 3.70 4.00 4.30 0.145 0.157 0.169 M1 3.60 4.00 4.40 0.142 0.157 0.169 M1 3.60 4.00 4.40 0.145 0.157 0.169 M1 3.60 4.00 0.40 0.086 O 2 0 0.096 R 1.70 0.0067 R1 0.55 0.02 R2 0.3 0.012 R3 1.25 0.0049 R4 0.50 0.019 V 5 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) V4		0.75		1.05	0.020		0.041	
F(1)								
G1 25.75 26.00 26.25 1.014 1.023 1.033 1.130 1.150 1.150 1.153 1.150 1.150 1.150 1.150 1.150 1.150 1.150 1.150 1.150 1.150 1.153 1.150 1.1		0.07	0.00		0.011	0.0.0	0.022	
H (2) 28.90 29.23 29.30 1.139 1.150 1.153   H1	Ğ						0.047	
H1								
H2		28.90		29.30	1.139		1.153	
H3								
L(2) 22.07 22.47 22.87 0.869 0.884 0.904 L1 18.57 18.97 19.97 19.37 0.731 0.747 0.762 L2 (2) 15.50 15.70 15.90 0.610 0.618 0.626 L3 7.70 7.85 7.95 0.303 0.309 0.313 L4 5 0.35 0.303 0.309 0.313 M 3.70 4.00 4.30 0.145 0.157 0.169 M1 3.60 4.00 4.40 0.142 0.157 0.173 N 2.20 0.099 R 1.70 0.067 R1 0.5 0.02 R2 0.3 0.12 R3 1.25 0.049 R4 0.50 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) V3 45 (Typ.) V3 H3 H3 H2  Flexiwatt27 (vertical)								
1.50			22.47			0.884		
1.70								
L4								
Section   Sect		7.70		7.95	0.303		0.313	
M1 3.70 4.00 4.30 0.145 0.157 0.169 M1 3.60 4.00 4.40 0.142 0.157 0.173 N 2.20 0.086 0 0 2 0.079 R 1.70 0.067 R1 0.5 0.02 R2 0.3 0.12 R3 1.25 0.049 V 5 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) (1): dam-bar protusion not included (2): molding protusion included (2): molding protusion included								
M1 3.60 4.00 4.40 0.142 0.157 0.173 N 2.20 0.086 O 2 0.0079 R 1.70 0.067 R1 0.5 0.02 R2 0.3 0.12 R3 1.25 0.049 R4 0.50 5 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) (1): dam-bar protusion not included (2): molding protusion included		3.70		4.30	0.145		0.169	
O		3.60		4.40	0.142			
R								
R1								
R2								
R3								
V 5 (Typ.) V1 3 (Typ.) V2 20 (Typ.) V3 45 (Typ.) (1): dam-bar protusion not included (2): molding protusion included	R3		1.25			0.049		
V1 3 (Typ.) V2 20 (Typ.) (1): dam-bar protusion not included (2): molding protusion included  V3 H1			0.50			0.019		
V2 20 (Typ.) V3 45 (Typ.) (1): dam-bar protusion not included (2): molding protusion included								Flexiwatt27 (vertical)
(1): dam-bar protusion not included (2): molding protusion included  V  H  H  H  R  A  V1  R  R  R  R  R  R  R  R  R  R  R  R  R								i iominatizi (i oi iiodi)
(2): molding protusion included  B  H  H  H  R  A  A  V  R  L  L  L  L  L  L  L  L  L  L  L  L								
B H H H H H H H H H H H H H H H H H H H								
H H1 H2 A A A A A A A A A A A A A A A A A A	(2): mol	ding protu	sion inclu	ded				
Pin 1 F	<u>°</u>	F3 F4 O			нз	H1		R L L1 V1 R1 R1 R1
7139011			-			<u> </u>		FLEX27ME M M1

Package information TDA7569LV

Figure 41. Flexiwatt27 (SMD) mechanical data and package dimensions

DNA	UIL MINK 1979: MAX. MRN, 1979: MAX. MRN, 1979: MAX. MRN, 1979: MRX. MRX. MRN, 1979: MRX. MRX. MRN, 1979: MRX. MRX. MRN, 1979: MRX. MRX. MRX. MRX. MRX. MRX. MRX. MRX.								
Milk	USB. Milks 1979 MAX. MRN. 1979 MAX. MRN. 1979 MAX. A 4-65 4-50 4-50 -1779 C-1779 C-1831 MR. 1979 MR. 1			mm			inch		
B	8 2.12 2.22 2.32 0.0055 0.0074 0.0737 0.0737 0.0737 0.0737 0.0738 0.035 0.040 0.044 0.0142 0.0157 0.0737 0.0737 0.0737 0.0737 0.073 0.0737 0.0737 0.073 0.0737 0.0737 0.073 0.0737 0.0337 0.0337 0.0339 0.033	DIM.	MIN.		MAX.	MIN.		MAX.	
B	8 2.12 2.22 2.32 0.0055 0.0074 0.0737 0.0737 0.0737 0.0737 0.0738 0.035 0.040 0.044 0.0142 0.0157 0.0737 0.0737 0.0737 0.0737 0.073 0.0737 0.0737 0.073 0.0737 0.0737 0.073 0.0737 0.0337 0.0337 0.0339 0.033	Α	4.45	4.50	4.65	0.1752	0.1772	0.1831	OUTLINE AND
C	C   1.4.0   0.0572   0.0572   0.0773								
D   0.00   0.04   0.0142   0.0157   0.0173   0.0173   0.0173   0.0173   0.0173   0.0173   0.0173   0.0173   0.0173   0.0173   0.0013   0.0014   0.0142   0.0014   0	© 1.200   1.4		2.12		2.02	0.0000		0.0310	MECHANICAL DAT A
E 0.36 0.40 0.44 0.0142 0.0157 0.0186 0.0021 0.0224 0.0717	E 0.36 0.40 0.44 0.01 0.052 0.0555 0.0554 0.0575 0.0775 0.0775 0.0775 0.0775 0.0775 0.0775 0.0775 0.0075 0.0055 0.0594 0.0492 0.0775 0.0055 0.0594 0.0492 0.0555 0.0594 0.0797 0.0585 0.0594 0.0797 0.0594 0.0797 0.0585 0.0594 0.0797 0.0797 0.0585 0.0594 0.0797 0.0797 0.0585 0.0594 0.0797 0.0797 0.0585 0.0594 0.0797 0.0797 0.								
Fr	F** 0.47 0.51 0.27 1.25 0.0299 1.0264 0.0264 0.075 1.000 1.25 0.0299 1.0264 0.0								
G(1) 5.70 5.00 0.00 1.25 0.00589 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.0787 0.0886 0.08	G(Y) 0.75 1.00 1.25 0.0295 0.0594 0.0492 (1.020	E	0.36	0.40	0.44	0.0142	0.0157	0.0173	
GCT 15 70 0.00 0.9 3.0 1.0118 1.0239 1.0354   GCT) 1.75 2.00 2.22 0.0689 0.0737 0.0889   HY1 9.8 85 29.23 29.40 1.138 0.0503   HY1 1.75 2.00   1.15 2.	G1 25 70 26 00 26 30 10 10 18 10 20 30 10 34 1 10 35 1	F**	0.47	0.51	0.57	0.0185	0.0201	0.0224	
GCT 15 70 0.00 0.9 3.0 1.0118 1.0239 1.0354   GCT) 1.75 2.00 2.22 0.0689 0.0737 0.0889   HY1 9.8 85 29.23 29.40 1.138 0.0503   HY1 1.75 2.00   1.15 2.	G1 25 70 26 00 26 30 10 10 18 10 20 30 10 34 1 10 35 1	G(*)	0.75	1.00	1.25	0.0295	0.0394	0.0492	
CR(Y)   1.75   2.00   2.25   0.0689   0.0787   0.0880     HY	CRY  1.79   2.00   2.29   0.0899   0.0797   0.0999     H1								
NT   28.85   28.22   28.40   11.556   11.557   11.557   11.558   11.559   11.577   15.50   15.70   15.50   0.6093   11.570   15.50   0.6102   0.6191   0.6280   11.177   17.55   15.70   15.50   0.6102   0.6191   0.6280   11.177   17.50   15.70   15.50   0.6102   0.6191   0.6280   11.177   17.50   15.70   15.50   0.6102   0.6191   0.6280   11.177   17.50   17.50   17.50   0.6291	H1 17 28.80 28.20 28.40 1 13088 11508 11507 11308 11507 11308 11507 11308 11507 11308 11507 11308 11507 11308 11507 11308 11307 11308 11307 11308 11309 11308 11309 1130								
H1	H1 10.00 0.05039								
H2 12.50	H2		28.85		29.40	1.1358		1.1575	
H3 0.90 0.90 0.051 0.051 0.051 0.0520 0.6181 0.6580 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181	H3	H1		17.00			0.6693		
H3 0.90 0.90 0.051 0.051 0.051 0.0520 0.6181 0.6580 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181 0.6181 0.6880 0.6181	H3	H2		12.80			0.5039		
L(*) 15.50 15.70 15.90 6.102 0.6181 0.6260 1.111 0.0261 1.111 0.1111 0.1200 1.111 0.0261 1.111 0	L(**) 15.50 15.70 15.90 0.0102 0.0181 0.0000 L1 7.70 7.85 7.95 0.0303 0.03091 0.3100 L2 14.00 14.20 14.40 0.5512 0.5501 0.5000 L3 11.80 12.00 12.20 0.464 0.0724 0.4003 L3 11.80 12.00 12.20 0.464 0.0724 0.4003 L4 1.50 1.48 1.66 0.0512 0.0563 0.0004 L5 2.42 2.50 2.50 0.050 0.0000 0.0017 0.0226 M** 1.50 2.42 2.50 2.50 0.050 0.0000 0.0017 0.0226 M** 1.50 2.42 2.50 2.50 0.0500 0.0000 0.0017 0.0226 M** 1.50 2.20 2.00 2.00 0.0000 0.0000 0.0000 M** 1.50 0.0000 0.0000 0.0000 0.0000 M** 1.50 0.0000 0.0000 0.0000 0.0000 0.0000 M** 1.50 0.00000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.00000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.00000 0.00000 0.00000 0.00000 0.0000 0.0000 0.0000 0.0000 0.0000 0.0000 0.00								
L1 7,70 7,85 7,95 0.3031 0.3931 0.3130  L2 14.00 114.00 114.00 1512 0.5591 0.5690  L3 11.90 112.00 12.20 0.4646 0.4724 0.4603  L4 1.30 1.48 1.66 0.6512 0.0583 0.0654  L5 2.42 2.50 2.58 0.0653 0.0984 0.1016  L6 0.42 0.50 0.58 0.0165 0.0591 0.0281  M 0.1.50 0.88 0.0165 0.0591 0.0281  N1 1.50 1.48 1.68 0.0512 0.0885 0.0654  N11 1.30 1.48 1.68 0.0512 0.0885 0.0654  N11 1.30 1.48 0.050 0.051 0.0197 0.0177  R1 0.50 0.00 0.00 0.00 0.00 0.00 0.00 0.0	1.1   7.70   7.85   7.95   0.3031   0.3901   0.3190   0		15.50		15.00	0.6100		0.6360	
L2 14.00 14.20 14.00 5512 0.5581 0.5591 0.5580 1.55	12								
13   11.80   12.00   12.20   0.4646   0.4724   0.4803   0.0554	13								
13   11.80   12.00   12.20   0.4646   0.4724   0.4803   0.0554	13	L2	14.00	14.20	14.40	0.5512	0.5591	0.5669	
L4 1.30 1.48 1.69 0.0512 0.0583 0.0656 1.0516 1.0512 0.0583 0.0984 0.10516 1.0512 0.0583 0.0984 0.10516 0.1997 0.0228 M 1.50 1.48 1.69 0.0512 0.0583 0.0984 0.1051 0.0512 0.0583 0.0551 0.1151 0.1154 1.05 0.0512 0.0583 0.0551 0.0514 0.1554 1.05 0.0512 0.0583 0.0551 0.0514 0.1554 1.05 0.0512 0.0583 0.0551 0.0514 0.1554 1.05 0.0512 0.0583 0.0551 0.0517 0.0177 0.0512 0	L4 1.30 1.48 1.66 0.0512 0.0583 0.0564   L5 2.42 2.20 2.58 0.0595 0.0594 1.0161   L6 0.42 0.50 0.58 0.0565 0.0594 1.0161   NN 1.50 0.000 0.050 0.0501 0.0591   NN 1.130 1.48 1.66 0.0512 0.0583 0.0564   NB2(7) 2.73 2.28 2.23 0.1075 0.1114 0.1154   R1 1.70 0.050 0.050 0.050 0.0501   R1 1.70 0.050 0.050 0.050 0.0501   R1 1.70 0.050 0.050 0.0501 0.0501   R1 1.00 0.050 0.050 0.050 0.0501   R2 0.050 0.050 0.050 0.050 0.0501   R3 0.050 0.050 0.050 0.050 0.0501   R4 0.050 0.050 0.050 0.050 0.0501   R5 0.050 0.050 0.050 0.050   R5 0.050 0.050 0.050 0.0500 0.0500   R5 0.050 0.050 0.050 0.0500 0.0500   R5 0.050 0.050 0.050 0.0500 0.0500 0.0500 0.0500   R5 0.050 0.050 0.050 0.0500 0.0500 0.0500 0.0500   R5 0.050 0.050 0.050 0.0500 0.0500 0.0500 0.0500 0   R5 0.050 0.050 0.050 0.0500 0.0500 0.0500 0   R5 0.050 0.050 0.050 0.0500 0.0500 0.0500 0   R5 0.050 0.050 0.050 0.0500 0.0500 0   R5 0.050 0.050 0.050 0.0500 0   R5 0.050 0.050 0.0500 0.0500 0   R5 0.050 0.050 0.050 0.0500 0   R5 0.050 0.050 0.0500 0   R5 0.050 0.050 0.0500 0   R5 0.050 0.050 0.0500 0.0500 0   R5 0.050 0.050 0   R5 0.050 0								
L5 2.42 2.50 2.58 0.0963 0.0964 0.1016 L6 0.42 0.50 0.58 0.0165 0.0197 0.0228 M 1.50 0.50 0.58 0.0165 0.0197 0.0228 NN 1 1.50 0.0561 0.0586 0.0666 NN 1 1.30 1.48 1.66 0.0512 0.0588 0.0664 NR(?) 2.73 2.83 2.93 0.1975 0.1114 0.1154 P(?) 4.73 4.83 4.93 0.1802 0.1902 0.1941 R 1 1.70 0.00 0.0569 R1 0.35 0.40 0.45 0.0138 0.0157 0.0177 R1 0.35 0.40 0.45 0.0138 0.0157 0.0177 R1 0.50 0.50 0.50 0.50 0.0157 0.0177 R1 0.50 0.50 0.50 0.0177 R1 0.50 0.50 0.0157 0.0177 R1 0.50 0.50 0.0177 R1 0.50 0.017	LS 2-42 2.50 2.58 0.0653 0.0984 0.1016  LG 0-42 0.50 0.58 0.0165 0.0197 0.0268  M 1.50 0.0591 0.00591  N 220 1.66 0.0591 0.0598 0.0654  N1 1.1.30 1.48 1.66 0.0512 0.0588 0.0654  N2(1) 2.73 2.83 2.93 0.1075 0.1114 0.1154  P(1) 4.73 4.83 4.93 0.1982 0.1992 0.1941  R 1.70 0.068 0.0159 0.0159 0.0159 0.0177  R3 0.35 0.40 0.45 0.0138 0.0157 0.0177  R3 0.35 0.40 0.45 0.0138 0.0157 0.0177  R4 0.050 0.0000 0.019 0.00000  PR2 0.05 0.40 0.45 0.0138 0.0157 0.0177  R4 0.050 0.00000 0.019 0.00000  PR2 0.05 0.40 0.45 0.00000 0.00000  PR2 0.05 0.40 0.45 0.00000 0.00000  PR2 0.05 0.40 0.45 0.0138 0.0157 0.0177  R4 0.050 0.00000 0.00000 0.000000  PR2 0.050 0.40 0.45 0.00000 0.000000  PR2 0.050 0.40 0.45 0.0000000 0.00000000000								
1.0	LS								
N	N								
N 1 2.20	N   2.20   1.48   1.66   0.0512   0.0583   0.0654   N2(*)   2.73   2.83   2.93   0.1075   0.1114   0.1154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.1914   0.154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.1914   0.154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.1914   0.154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.177   Pt   0.177   Pt   0.06   0.0518   0.0176   0.0177   Pt   0.06   0.0518   0.0157   0.0177   Pt   0.06   0.0518   0.0157   0.0177   Pt   0.06   0.0518   0.0157   0.0039   0.0518	L6	0.42	0.50	0.58	0.0165	0.0197	0.0228	
N 1 2.20	N   2.20   1.48   1.66   0.0512   0.0583   0.0654   N2(*)   2.73   2.83   2.93   0.1075   0.1114   0.1154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.1914   0.154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.1914   0.154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.1914   0.154   Pt)   4.73   4.83   4.93   0.1862   0.1592   0.177   Pt   0.177   Pt   0.06   0.0518   0.0176   0.0177   Pt   0.06   0.0518   0.0176   0.0177   Pt   0.06   0.0518   0.0157   0.0177   Pt   0.06   0.0518   0.0157   0.0039   0.0518	M		1.50			0.0591		
N21 2.73 2.83 1.56 0.0512 0.0583 0.0654  N21 2.73 2.83 2.93 0.1075 0.1114  R1 0.30 0.86 0.0138 0.0186  R1 0.30 0.04 0.45 0.0138 0.0157 0.0177  R3 0.35 0.40 0.45 0.0138 0.0157 0.0177  R4 3 0.55 0.40 0.45 0.0138 0.0157 0.0177  R4 3 0.55 0.40 0.45 0.038 0.0157 0.0177  R5 1 0.00 0.05699  V 45 45 0.00 0.019 0.0039  V 45 5 0.00 0.0197 0.0177  V 1 3 7 3 5 7 3 5 7 7 3 5 7 7 7 3 5 7 7 7 7	N1 1 30 1.48 1.68 0.0512 0.0583 0.0554  N2(7) 2.73 2.83 2.93 0.1075 0.1114  N2(7) 4.73 4.83 4.93 0.1862 0.1902 0.1904  R1 0.39 0.00 0.45 0.0198 0.0157 0.0177  R3 0.35 0.40 0.45 0.0198 0.0157 0.0177  R4 0.050 0.00 0.00 0.00 0.00039  N2 0.00 0.45 0.0198 0.0157 0.0177  R4 0.050 0.00 0.00 0.00039  N2 0.00 0.00 0.00 0.00039  N3 0.00 0.00 0.00 0.00039  N3 0.00 0.00 0.00 0.00039  N4 0.00 0.00 0.00039  N5 0.00 0.00 0.00039  N5 0.00 0.00 0.00039  N5 0.00039								Sall Ross
Na(1)   2.73   2.83   2.93   0.1075   0.1114   0.1154     P(1)   4.73   4.83   4.53   0.1862   0.1902   0.1941     R	N2(1)   2.73   2.83   2.39   0.1075   0.1114   0.1154     P(1)   4.73   4.83   4.39   0.1862   0.1902   0.1941     R		1 20		1 66	0.0512		0.0654	
P(') 4.73 4.83 4.93 0.1982 0.1902 0.1941 R1 0.30 0.0669 0.0669 R1 0.30 0.018 0.018 0.008	P(') 4.73 4.83 4.93 0.1862 0.1902 0.1941 R1 1.77 0.00669 R2 0.35 0.40 0.45 0.0138 0.0157 0.0177 R3 0.35 0.40 0.45 0.0138 0.0157 0.0177 R4 4.5 0.50 0.0063 0.0157 0.0177 R4 1.5 0.006 0.0063 0.0057 0.0177 R4 1.5 0.006 0.0063 0.0057 0.0177 R5 0.00660 0.0063								* **
R 1 1.70	R								
R1	R1		4.73		4.93	0.1862		0.1941	
R1	R1	R		1.70			0.0669		
R2 0.35 0.40 0.45 0.0138 0.0157 0.0177 R3 0.35 0.40 0.45 0.0138 0.0157 0.0177 R4 0.50 0.0138 0.0157 0.0177 R7 0.008 0.01 0.0031 0.0039 V 45 45 45 45 V2 3 5 7 3 3 5 7 V3 5 7 V3 12 15 18 18 12 15 18 18 V4 5 V5 0.0039 V4 5 0.008 0.008 0.009 0.	R2 0.35 0.40 0.45 0.0138 0.0157 0.0177 R3 0.35 0.40 0.45 0.0138 0.0157 0.0177 R4 0.50 0.10 0.0039 0.0197 T(1) 0.008 0.10 1.00039 0.0039 V 45 45 45 45 V1 3 5 7 3 5 7 7 3 5 5 7 V3 12 15 18 12 15 18 12 15 18 V4 5 0.0138 12 0.0157 18 V5 0.008 0.009 0.0039 C(1) Golden parameters (1) Golden parameters (2) Golden parameters (1) Golden parameters (2) Golden parameters (2) Golden parameters (3) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (4) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (3) Golden parameters (3) Golden parameters (4) Golden parameters (5) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (4) Golden parameters (5) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (4) Golden parameters (5) Golden parameters (6) Golden parameters (7) Golden parameters (8) Golden parameters (9) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (4) Golden parameters (2) Golden parameters (3) Golden parameters (4) Golden parameters (3) Golden parameters (4) Golden parameters (5) Golden parameters (6) Golden parameters (7) Golden parameters (7) Golden parameters (8) Golden parameters (9) Golden parameters (1) Golden parameters (1) Golden parameters (1) Golden parameters (1) Golden parameters (2) Golden parameters (3) Golden parameters (4) Golden parameters (4) Golden parameters (5) Golden parameters (6) Golden parameters (7) Golden parameters (7) Golden								
R3 0.35 0.40 0.45 0.0138 0.0157 0.177 T(1) 0.08 0.50 0.10 0.10 0.0039 V	R3		0.35		0.45	0.0138		0.0177	
Read   0.50	R4								
T(7)   0.08	T()		0.35		0.45	0.0138		0.01//	
## And To The Professions of The Company of The Com	Record   Park   P			0.50			0.0197		
Seed Set   Column	Name	T(*)	-0.08		0.10	-0.0031		0.0039	
V	V			0.1			0.0039		
V1	V1   3   5   7   3   5   7   7   8   12   15   18   18   12   15   18   18   12   15   18   18   18   18   18   18   18	v							
V2   3   5   7   3   5   7   7   8   12   15   18	V2   3   5   7   3   5   7   8   12   15   18   1	1 .	+						
V3   12   15   18   12   15   18   12   15   18   V4   5   20   20   20   V5   parameters   V5   potentian 1° doesn't include dam-bar profrusion.	V3   12   15   18   12   15   18   12   15   18   V3   V3   V5   V5   V5   V5   V5   V5				l			1	Florging
(SIVID)  (Solder parameters (*) – Dimensions **H* and **L* include mold flash or protrusions.  (*) – Dimensions **H* and **L* include mold flash or protrusions.    V4	V4 5 20 20 20 20 20 20 20 20 20 20 20 20 20		1 3						FIDVIMATT//
(*) Obmension *F* doesn't include dam-bar protrusion.  - Dimension *F* doesn't include dam-bar protrusions.  Detail *A* Rotated 90° CCW  H3  H1  H2  See detail *A*  T993733 B	To Golden parameters  (*) - Dimensions *F* doesn't include dam-bar protrusions.  - Dimensions *H* and *L* include mold flash or protrusions.  Petoli *A* Roloted 90* CCW    13							-	Flexiwatt2/
(*) Oimension *F* doesn't include dam-bar protrusion.  - Dimension *F* doesn't include dam-bar protrusions.  Detail *A* Rototed 90° CCW  H3  H1  H2  See defall *A*  T993733 B	To Golden parameters  (*) - Dimensions *F* doesn't include dam-bar protrusions.  - Dimensions *H* and *L* include mold flash or protrusions.  Petoli *A* Roloted 90* CCW    13	V3						-	
(*) Golden parameters (*) — Dimensions "F" and "L" include dam-bar protrusion.  — Dimensions "H" and "L" include mold flash or protrusions.  Defoil "A" Roleted 90" CCW  AUGU PLANE SEATING PLANE  SEATING PLANE  See defail "A"  7993733 B	(*) Golden parameters (*) — Dimensions "P" doesn't include dam-bar protrusion.  — Dimensions "H" and "L" include mold flash or protrusions.   To include mold flash or protrusions.   To include mold flash or protrusions.  To include mold flash or protru			15			15	-	
(") – Dimensions "F" and "L" include mold flash or protrusions.  Defoil "A"  Roferded 90° CCW  SEATING PLANE  3  Leader?  Leader?  See defoil "A"  7993733 B	(*) - Dimensions "F" dead". I include mold flash or protrusions.  - Dimensions "F" and "L" include mold flash or protrusions.  Detail "A" Rototed 90" CCW  INS. HI  I	V4		15 5			15 5	-	
Detail "A" Rototed 90° CCW  ADDED  AD	Defoil "A" Rolated 90° CCW  SEATING PLANE  Sea defail "A"  Typogy33 B	V4 V5	12	15 5			15 5	-	
Detail "A" Rotated 90' CCW  H3  H1  H2  See detail "A"  7993733 B	Defail "A" Rolated 90° CCW  H3— H1  H2  Related 90° CCW  Seating Plane  Seating P	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	
Rolcted 90° CCW  15 V3  10 ANGE PLANE  10 SEATING PLANE  10 See detail "A"	Rolated 90° CCW  AND PLANE SEATING PLANE  Seating P	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	
AND PLANE SEATING PLANE  See defall "A"  7993733 B	H3 — H2 — See defall "A"  See defall "A"  Typogram B	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)
H3— H1  H2  H2  H2  H2  H2  H2  H2  H2  H2	GAUGE PLANE SEATING PLANE  SEATING P	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)
H3— H1  H2  H2  H2  H2  H2  H2  H2  H2  H2	CAUGE PLANE SEATING PLANE  AND PLANE  SEATING PLANE	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)
H3 H1 SEATING PLANE	GAUGE PLANE  SCATING PLANE  STATING	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)
H3 H1 H2 AME PLANE SEATING PLANE  SE	GAUGE PLANE  SCATING PLANE  STATING	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)  Detail "A" Roterted 90° CCW
H3— H1  H2  H2  H2  H2  H2  H3  H3  H3  H4  H2  H2  H3  H4  H3  H4  H4  H4  H5  H4  H5  H6  H7  H7  H7  H7  H7  H7  H7  H7  H7	H3— H2  H2  H2  See defall "A"  7993733 B	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)  Detail "A" Roterted 90' CCW  Le  V3
H2  H2  N2  See defall "A"  7993733 B	H2 H2 See defall "A"  Types/333 B	V4 V5 (*) Golden (**) – Dime	12 parameters nsion "F" doe	15 5 20 esn't include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)  Detail "A" Rotered 90" CCW  Lg V5
H2  N2  N2  N2  See defall "A"  7993733 B	N2	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)  Detail "A" Rotated 90° CCW  Seating Plane Seating Plane
H2  N2  N2  N2  See detall "A"  7993733 B	N2   See defall "A"   Typ93733 B	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	(SMD)  Detail "A" Rotated 90° CCW  Seating Plane Seating Plane
Leader Leader Typ93733 B	Leader Leader See defall "A"  7993733 B	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	OBMD)  Deball "A" Rotated 90° CCW  AND Deball "A" Rotated 90° CCW  SEATING PLANE  SEATING PLANE  SEATING PLANE
Leader 1 Cader 1 Cader 27 Cade	12   12   12   13   14   14   14   14   14   14   14	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rototed 50" CCW  LE  V4  OANGE PLANE SEATING PLANE
Leader 1  Leader 1  Leader 1  See detail "A"  7993733 B	Leader A. See defall "A."	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rototed 50" CCW  LE  V4  OANGE PLANE SEATING PLANE
Leader 1  Leader 1  Leader 1  See detail "A"  7993733 B	Lead#27   Lead#7   Lead#1 'A'  G1   Typ93733 B	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rototed 50" CCW  LE  V4  OANGE PLANE SEATING PLANE
(ead#27   Lead#1	Lead#27   Lead#7   Lead#7   Lead#7   Lead#7   Lead#7   Typ93733 B	V4 V5 (*) Golden (**) – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rotated 90" CCW  LG  V5  OANGE PLANE SEATING PLANE  N1  N2  P  N2  P  OANGE PLANE SEATING PLANE
(ead#27   Lead#1	Lead#27   Lead#7   Lead#7   Lead#7   Lead#7   Lead#7   Typ93733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rotated 90" CCW  LG  V5  OANGE PLANE SEATING PLANE  N1  N2  P  N2  P  OANGE PLANE SEATING PLANE
Lead#27   Lead#1	Lead#27  Lead#27  F  See defall "A"  7993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rotated 90" CCW  LG  V5  OANGE PLANE SEATING PLANE  N1  N2  P  N2  P  OANGE PLANE SEATING PLANE
Lead#27   Lead#1	Lead#77   Lead#1 "A"   See defall "A"   7993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	Obtoll "A" Rototled 90" CCW  La  V5  AND  SEATING PLANE  SEATING PLANE  TO THE THE PLANE  TO THE
Lead#27   Lead#1	Lead#77   Lead#1 "A"   See defall "A"   7993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	Obtoll "A" Rototled 90" CCW  La  V5  AND  SEATING PLANE  SEATING PLANE  TO THE THE PLANE  TO THE
Lead#27   Lead#1	Lead#27  Lead#77  Typg3733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detail "A" Rotated 90" CCW  LG  V5  OANGE PLANE SEATING PLANE  N1  N2  P  N2  P  OANGE PLANE SEATING PLANE
Lead#27   Lead#1	Lead#27  Lead#27  Lead#27  Typ93733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	CSMD)  Detcil "A" Rototed 90" CCW  LG  VA  N2  P  CAMAGE PLANE SEATING PLANE  SEA
(ead#27   G1   F   See defall "A"	Lead#27   Lead#7   Lead#1   Lead#1   Lead#1   Lead#1   Ty993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	Detail "A" Rotated 90° CCW  LG  N2  P  A  N2  P  Column S  Column
Lead#27   Lead#1	Lead#27   Lead#7   Le	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	Detcil "A" Rotored 90" CCW  LG  VS  SATING PLANE  SEATING PLANE  S
Lead#27   Lead#1	Lead#27   Lead#7   Le	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	Detcil "A" Rotored 90" CCW  LG  VS  SATING PLANE  SEATING PLANE  S
Lead#27   Lead#1	Lead#27   Lead#7   Le	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	-	Detcil "A" Rotored 90" CCW  LG  VS  SATING PLANE  SEATING PLANE  S
(ced#27	Lead#77   Lead#7   Lead#7   Lead#7   Lead#7   Lead#7   Lead#7   Lead#7   Lead#7   Ty93733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	18	Detcil "A" Rotored 90" CCW  LG  VS  SATING PLANE  SEATING PLANE  S
(ced#27	Lead#27   Lead#7   Lead#1 "A"   See detail "A"   T993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	18	Detcil "A" Rotored 90" CCW  LG  VS  SATING PLANE  SEATING PLANE  S
(ced#27	Lead#27   Lead#7   Lead#1 "A"   See detail "A"   T993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters nsion "F" doe nsions "H" a	15 5 20 esn't include nd "L" include	18 dam-bar pro	12 otrusion.	15 5 20	18	Detcil "A" Rotored 90" CCW  LG  VS  SATING PLANE  SEATING PLANE  S
i- g    i- g    See detail "A"	i- g    i- g    See detail "A"   See detail "A"   7993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters rision "F" document of the parameters rision "F" document of the parameters rision "H" a	15 5 20 ssn't include nd "L" include	dam-bar prote mold flash	or protrusion.	15 5 20 ons.	18	CSMD)  Detail "A" Rotated 90" CCW  LG  VS  OAUGE PLANE SEATING PLANE  SEATING PLA
	- c -   c	V4 V5 (*) Golden (**) – Dime – Dime	parameters rision "F" document of the parameters rision "F" document of the parameters rision "H" a	15 5 20 ssn't include nd "L" include	dam-bar prote mold flash	or protrusion.	15 5 20 ons.	18	CSMD)  Detail "A" Rotated 90° CCW  AND  Detail "A" Rotated 90° CCW  SEATING PLANE  SATING PLANE  SAT
	G1 See defall "A"  1993733 B	V4 V5 (*) Golden (**) – Dime – Dime	parameters rision Fri doc ensions Hri a	15 5 20 ssn't include nd "L" include	dam-bar protein mold flash	or protrusion.	15 5 20 ons.	18	CSMD)  Detail "A" Rotated 90° CCW  AND  AND  AND  AND  AND  AND  AND  AN
	G1 See defall "A"  1993733 B	V4 V5 (*) Golden (*) – Dime	parameters prison "F" documents of the state	15 5 20 ssn't include nd "L" include	dam-bar protein mold flash	or protrusion.	15 5 20 ons.	18	CSMD)  Detail "A" Rotated 90° CCW  AND  Detail "A" Rotated 90° CCW  SEATING PLANE  SATING PLANE  SAT
	- c -   c	V4 V5 (*) Golden (*) – Dime	parameters prison "F" documents of the state	15 5 20 ssn't include nd "L" include	dam-bar prr le mold flast	obtrusion.  n or protrusion.	15 5 20 ons.	18	CSMD)  Detail "A" Rotated 90° CCW  AND  Detail "A" Rotated 90° CCW  SEATING PLANE  SATING PLANE  SAT
G1 7993733 B	G1 7993733 B	V4 V5 (*) Golden (*) – Dime	parameters prison "F" documents of the state	15 5 20 ssn't include nd "L" include	dam-bar prr le mold flast	obtrusion.  n or protrusion.	15 5 20 ons.	18	CSMD)  Detail "A" Rotated 90° CCW  AND  Detail "A" Rotated 90° CCW  SEATING PLANE  SATING PLANE  SAT
G1 7993733 B	G1 7993733 B	V4 V5 (*) Golden (*) – Dime	parameters prison "F" documents of the state	15 5 20 sen't include nd "L" include	dam-bar prr le mold flast	obtrusion.  n or protrusion.	15 5 20 ons.	18	CSMD)  Detcil "A" Rototed 90" CCW  LG  N2  P  CAUGE PLANE SEATING PLANE  SEATING
		V4 V5 (*) Golden (*) – Dime	parameters prison "F" documents of the state	15 5 20 sen't include nd "L" include	dam-bar profee mold flast	obtrusion.  n or protrusion.	15 5 20 ons.	18	Detail "A" Rotated 90" CCW  LG  N2  P  LESSENTING PLANE  SEATING P
	GAPGPS	V4 V5 (*) Golden (*) – Dime	parameters prison "F" documents of the state	15 5 20 sen't include nd "L" include	dam-bar profee mold flast	or protrusion.	15 5 20 ons.	18	Detail "A"  Rotated 90° CCW  AND  AND  AND  AND  AND  AND  AND  AN

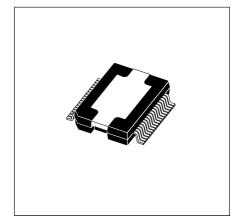
36/39 Doc ID 018712 Rev 3

TDA7569LV Package information

Figure 42. PowerSO36 (slug up) mechanical data and package dimensions

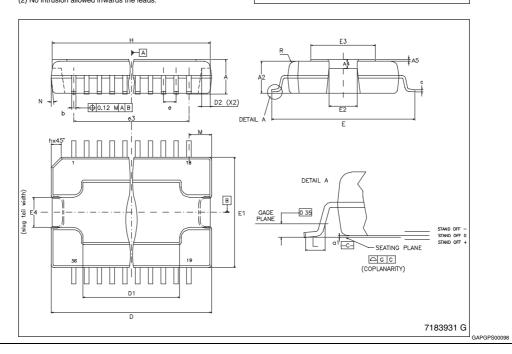
DIM.		mm		inch			
DIM.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Α	3.270	-	3.410	0.1287	-	0.1343	
A2	3.100	-	3.180	0.1220	-	0.1252	
A4	0.800	-	1.000	0.0315	-	0.0394	
A5	-	0.200	-	-	0.0079	-	
a1	0.030	-	-0.040	0.0012	-	-0.0016	
b	0.220		0.380	0.0087		0.0150	
С	0.230	-	0.320	0.0091	-	0.0126	
D	15.800	-	16.000	0.6220	-	0.6299	
D1	9.400	-	9.800	0.3701	-	0.3858	
D2	-	1.000	-	-	0.0394	-	
Е	13.900	-	14.500	0.5472	-	0.5709	
E1	10.900	-	11.100	0.4291	-	0.4370	
E2	-	-	2.900	-	-	0.1142	
E3	5.800	-	6.200	0.2283	-	0.2441	
E4	2.900	-	3.200	0.1142	-	0.1260	
е	-	0.650	-	-	0.0256	-	
e3	-	11.050	-	-	0.4350	-	
G	0	-	0.075	0	-	0.0031	
Н	15.500	-	15.900	0.6102	-	0.6260	
h	-	-	1.100	-	-	0.0433	
L	0.800	-	1.100	0.0315	-	0.0433	
N	-	-	10	-	-	10	
s	-	-8		-	-8		

OUTLINE AND MECHANICAL DATA



PowerSO36 (SLUG UP)

<sup>(1) &</sup>quot;D and E1" do not include mold flash or protusions.
Mold flash or protusions shall not exceed 0.15mm (0.006").
(2) No intrusion allowed inwards the leads.



Revision history TDA7569LV

## 12 Revision history

Table 13. Document revision history

Date	Revision	Changes
08-Apr-2011	1	Initial release.
24-May-2011	2	Changed from 40 to 45 W the minimum value of the output power at $R_L = 2 \Omega$ and THD 10 %; see <i>Table 5: Electrical characteristics on page 11</i> .
25-May-2011	3	Updated Section 4.1: Turn-on diagnostic on page 17.

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