TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

TD62M3700F

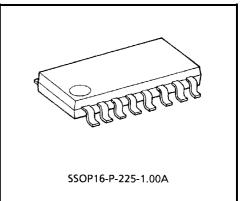
LOW SATURATION VOLTAGE DRIVER FOR MOTOR

TD62M3700F is low saturation, high current 3 phase full-wave type inverter IC designed especially for battery use motor drive applications.

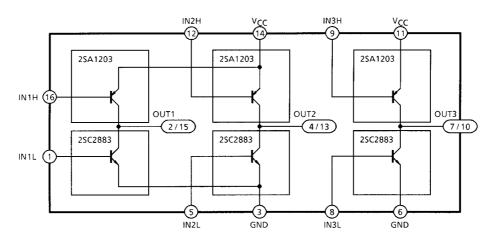
FEATURES

BLOCK DIAGRAM

- High current : IO (AVE) = 1.5 A IO (PEAK) = 3.0 A
- Sealed in 1 mm pitch 16pin surface mount package (SSOP16)



Weight: 0.14 g (Typ.)



PIN CONNECTION (TOP VIEW)

IN1L [1	16] ім1н
Ουτι [2	15] Ουτ1
GND [3	14] v _{cc}
Ουτ2 [4	13] OUT2
IN2L	5	12	IN2H
GND [6	11] v _{cc}
outs [7	10] ουτз
IN3L [8	9] імзн

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Power Supply Voltage	V _{CC}	30	V	
	V _{CBO}	30		
Breakdown Voltage	V _{CEO}	30	V	
	V _{EBO}	5		
Output Current (Average)	I _{O (AVE)}	1.5	А	
Output Current (Peak)	I _{O (PEAK)}	3.0 (Note 1)	А	
Base Current	Ι _Β	±0.3	А	
Power Dissipation	PD	490 (Note 2)	mW	
Junction Temperature	Тј	150	°C	
Operating Temperature	T _{opr}	-40~85	°C	
Storage Temperature	T _{stg}	-55~150	°C	

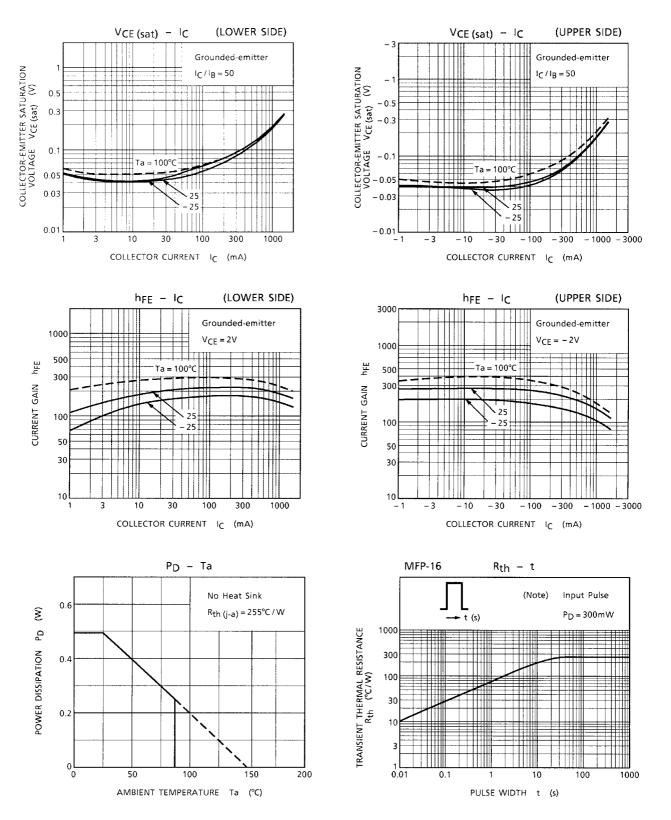
Note 1: T = 10 ms single pulse

Note 2: Free Air

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Gain		h _{FE (1)}		$V_{CE} = -2 V, I_C = -0.5 A$	160		320	
		h _{FE (2)}	_	V_{CE} = 0.4 V, I _C = 0.2 A	160	_	600	
h _{FE} Ratio		hFE (1) / hFE (2)	_	V _{CE} = 0.4 V, I _C = 30 mA / V _{CE} = 0.4 V, I _C = 0.2 A	0.75	_	1.25	_
Saturation Voltage	Upper Side	V _{CE (sat)}	_	I _C = −0.5 A, I _B = −5.0 mA	_	-0.35	-0.50	- V
				I _C = −1.5 A, I _B = −30 mA	—	_	-2.0	
	Lower Side			I _C = 0.5 A, I _B = 5.0 mA	—	0.2	0.35	
				I _C = 1.5 A, I _B = 30 mA	—	_	2.0	
	Summing Total			I _C = 0.5 A, I _B = 5.0 mA	_	0.55	0.85	
				I _C = 1.5 A, I _B = 30 mA	_	_	4.0	
Transition Frequency		f _T	_	V _{CE} = 2 V, I _C = 0.5 A	_	120	_	MHz
Leakage Current	Upper Side	I _{OL}	_	V _{CC} = -30 V	_	0	-5	μA
	Lower Side			V _{CC} = 30 V	_	0	5	
Base-Emitter Forward Voltage	Upper Side	V _{BE (PNP)}	_	V _{CE} = -1 V, I _C = -2 A	_	-0.84	-1.5	V
	Lower Side	V _{BE (NPN)}	—	V _{CE} = 1 V, I _C = 2 A	—	0.84	1.5	

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PRECAUTIONS for USING

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

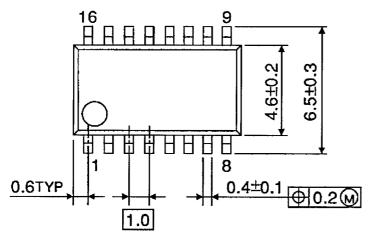
Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

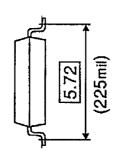
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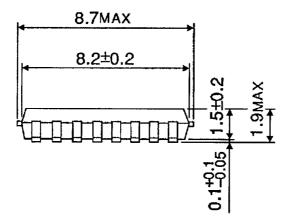
Unit: mm

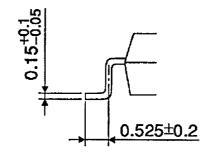
PACKAGE DIMENSIONS

SSOP16-P-225-1.00A









Weight: 0.14 g (Typ.)

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