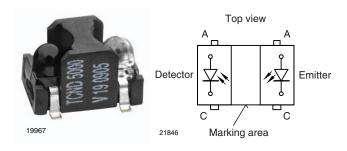


## Vishay Semiconductors

# **Reflective Optical Sensor with PIN Photodiode Output**



### **DESCRIPTION**

The TCND5000 is a reflective sensor that includes an infrared emitter and pin photodiode in a surface mount package which blocks visible light.

#### **FEATURES**

Package type: surface mount





• Peak operating distance: 6 mm

 Operating range within > 20 % relative collector current: 2 mm to 25 mm

Typical output current under test: I<sub>ra</sub> > 0.11 μA

· Daylight blocking filter

· High linearity

• Emitter wavelength: 940 nm

• Lead (Pb)-free soldering released

• Moisture sensitivity level (MSL): 4

 Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **APPLICATIONS**

- · Proximity sensor
- · Object sensor
- Motion sensor
- Touch key

PRODUCT SUMMARY				
PART NUMBER	DISTANCE FOR MAXIMUM CTR <sub>rel</sub> (1) (mm)	DISTANCE RANGE FOR RELATIVE I <sub>out</sub> > 20 % (mm)	TYPICAL OUTPUT CURRENT UNDER TEST (2) (mA)	DAYLIGHT BLOCKING FILTER INTEGRATED
TCND5000	6	2 to 25	0.0015	Yes

### Notes

 $^{(1)}$  CTR: current transfer ratio,  $I_{out}/I_{in}$ 

(2) Conditions like in table basic characteristics/sensors

ORDERING INFORMATION						
ORDERING CODE	PACKAGING	AGING VOLUME REMARKS				
TCND5000	Tape and reel	MOQ: 2000 pcs, 2000 pcs/reel	Drypack			

#### Note

· MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT (EMITTER)						
Reverse voltage		$V_{R}$	5	V		
Forward current		I <sub>F</sub>	100	mA		
Peak forward current	$t_p = 50 \ \mu s, \ t = 2 \ ms, \ T_{amb} \le 25 \ ^{\circ} C$	I <sub>FM</sub>	500	mA		
Power dissipation		P <sub>V</sub>	190	mW		
Junction temperature		T <sub>j</sub>	100	°C		



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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
OUTPUT (DETECTOR)							
Reverse voltage		$V_{R}$	60	V			
Power dissipation		P <sub>V</sub>	75	mW			
Junction temperature		Tj	100	°C			
SENSOR							
Ambient temperature range		T <sub>amb</sub>	-40 to +85	°C			
Storage temperature range		T <sub>stg</sub>	-40 to +100	°C			
Soldering temperature	acc. fig. 14	T <sub>sd</sub>	260	°C			

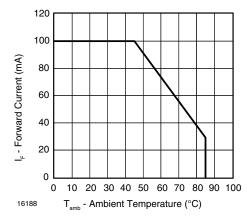


Fig. 1 - Forward Current Limit vs. Ambient Temperature

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT (EMITTER) (1)				•		
Forward voltage	$I_F = 50 \text{ mA}, t_p = 20 \text{ ms}$	V <sub>F</sub>		1.2	1.5	V
Temperature coefficient of V <sub>F</sub>	I <sub>F</sub> = 1 mA	TK <sub>VF</sub>		-1.3		mV/K
Reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>			10	μΑ
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0 lx	Cj		40		pF
Radiant intensity	$I_F = 20 \text{ mA}, t_p = 20 \text{ ms}$	I <sub>e</sub>		11	15	mW/sr
Angle of half intensity		φ		± 12		deg
Peak wavelength	I <sub>F</sub> = 100 mA	λ <sub>P</sub>	930	940		nm
Spectral bandwidth	I <sub>F</sub> = 100 mA	Δλ		30		nm
Temperature coefficient of $\lambda_p$	I <sub>F</sub> = 100 mA	TKλ <sub>P</sub>		0.2		nm/K
Rise time	I <sub>F</sub> = 100 mA	t <sub>r</sub>		15		ns
Fall time	I <sub>F</sub> = 100 mA	t <sub>f</sub>		15		ns

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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
OUTPUT (DETECTOR) (2)						
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1	1.3	V
Breakdown voltage	I <sub>R</sub> = 100 μA	$V_{BR}$	60			V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0 lx	I <sub>ro</sub>		1	10	nA
Diode capacitance	$V_R = 5 \text{ V}, f = 1 \text{ MHz}, E = 0 \text{ Ix}$	C <sub>D</sub>		1.8		pF
Reverse light current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>		12		μΑ
Temperature coefficient of Ira	$\lambda = 870 \text{ nm}, V_R = 5 \text{ V}$	TK <sub>ira</sub>		0.2		%/K
Angle of half intensity		φ		± 15		deg
Wavelength of peak sensitivity		λ <sub>P</sub>		930		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		840 to 1050		nm
SENSOR						
Reverse Light Current	$V_R = 2.5 \text{ V}, I_F = 20 \text{ mA}, D = 30 \text{ mm},$ reflective mode: see figure 2	I <sub>ra</sub>	110	260		nA

### Notes

<sup>(2)</sup> See figures 9 to 12 accordingly

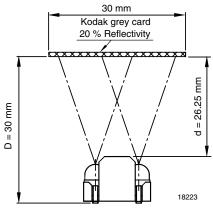


Fig. 2 - Test Circuit

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

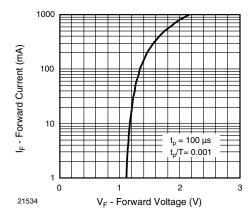


Fig. 3 - Forward Current vs. Forward Voltage

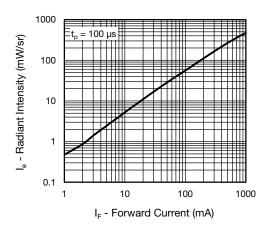


Fig. 4 - Radiant Intensity vs. Forward Current

<sup>(1)</sup> See figures 2 to 8 accordingly

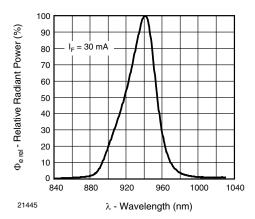


Fig. 5 - Relative Radiant Power vs. Wavelength

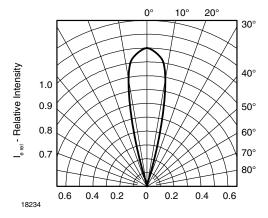


Fig. 6 - Relative Radiant Intensity vs. Angular Displacement

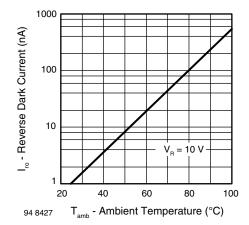


Fig. 7 - Reverse Dark Current vs. Ambient Temperature

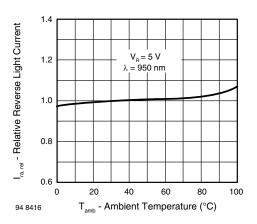


Fig. 8 - Relative Reverse Light Current vs. Ambient Temperature

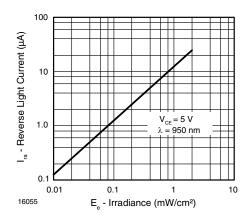


Fig. 9 - Reverse Light Current vs. Irradiance

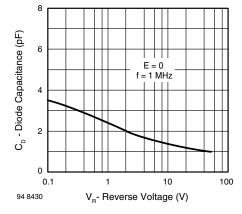


Fig. 10 - Diode Capacitance vs. Reverse Voltage

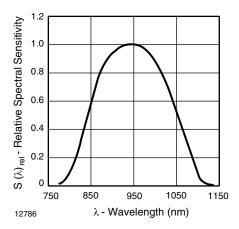


Fig. 11 - Relative Spectral Sensitivity vs. Wavelength

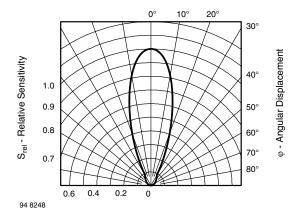


Fig. 12 - Relative Radiant Sensitivity vs. Angular Displacement

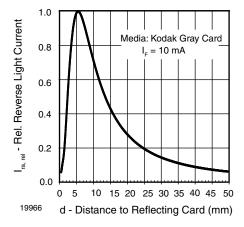
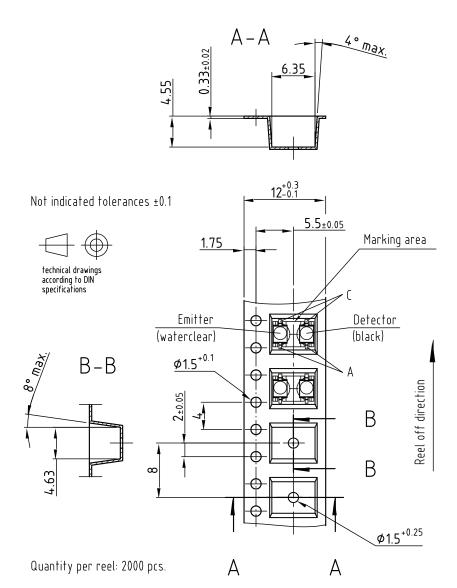


Fig. 13 - Relative Reverse Light Current vs. Distance



### **TAPING DIMENSIONS** in millimeters



Material of Blistertape: PC black Sealing of cavities with hot sealing cover tape, C-Pak Type CP - 2010 AS (Thickness: 0.055 - 0.075mm; Base Material: Polyester)

Drawing-No.: 9.700-5281.01-4

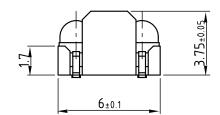
Issue: 4; 10.02.05

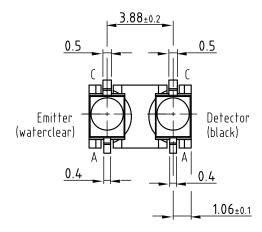
18222

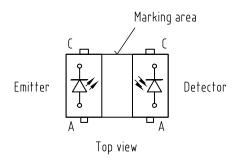


### **PACKAGE DIMENSIONS** in millimeters



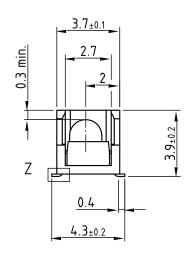


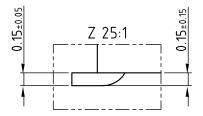


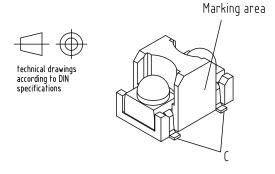


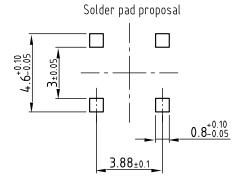
Drawing-No.: 6.544-5357.01-4 Issue: 2; 09.02.05

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# Vishay Semiconductors

### **PRECAUTIONS FOR USE**

### 1. Over-current-proof

Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).

### 2. Storage

- 2.1 Storage temperature and rel. humidity conditions are: 5  $^{\circ}\text{C}$  to 30  $^{\circ}\text{C}$  , RH 60 %
- 2.2 Floor life must not exceed 72 h, acc. to JEDEC® level 4, J-STD-020.
  - Once the package is opened, the products should be used within 72 h. Otherwise, they should be kept in a damp proof box with desiccant.
  - Considering tape life, we suggest to use products within one year from production date.
- 2.3 If opened more than 72 h in an atmosphere 5 °C to 30 °C, RH 60 %, devices should be treated at 60 °C  $\pm$  5 °C for 15 h.
- 2.4 If humidity indicator in the package shows pink color (normal blue), then devices should be treated with the same conditions as 2.3

### **REFLOW SOLDER PROFILES**

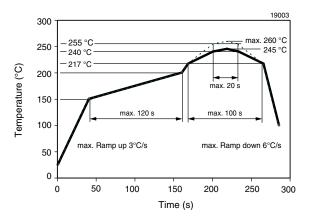


Fig. 14 - Lead (Pb)-Free Reflow Solder Profile

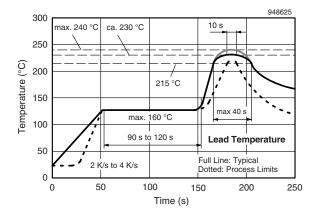


Fig. 15 - Lead Tin (SnPb) Reflow Solder Profile



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Vishay

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