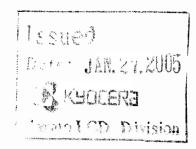
| SPEC. NO. | TQ3C-8EAFO-E1DDH12-00 | | | | | |
|-----------|-----------------------|--|--|--|--|--|
| DATE | January 26, 2005 | | | | | |

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TYPE: TCG075VG2AD-G00

CONTENTS

- 1. Application
- 2. Construction and Outline
- 3. Mechanical Specifications
- 4. Absolute Maximum Ratings
- 5. Electrical Characteristics
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- 7. Interface Signals
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- 13. Precautions for Use
- 14. Reliability Data/Environmental Test
- 15. Outline Drawing



KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

This specification is subject to change. Consult Kyocera before ordering.

| Original | Designed by | :Engineering | Dept. | Confirmed by :QA Dept. | | |
|------------------|-------------|--------------|------------|------------------------|------------|--|
| Issue Data | Prepared | Checked | Approved | Checked | Approved | |
| January 26, 2005 | 2. nistrino | J. Yamazaki | M.Fujitani | yryoshita. | S. Haysshi | |

Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

| Date | | Design | ed by: | Engineering D | ept. | Confirmed by: QA Dept. | | |
|---------|----|--------|----------|---------------|---------|------------------------|---------|----------|
| Date | | | Prepared | | Checked | Approved | Checked | Approved |
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1. Application

This data sheet defines the specification for a $(640 \times R.G.B) \times 480$ dot, amorphous silicon TFT transmissive color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline

(640 x R.G.B) x 480 dots, COG type LCD with CFL backlight.

Backlight system : Side-edge type CFL (2 tubes).

Inverter : Option.

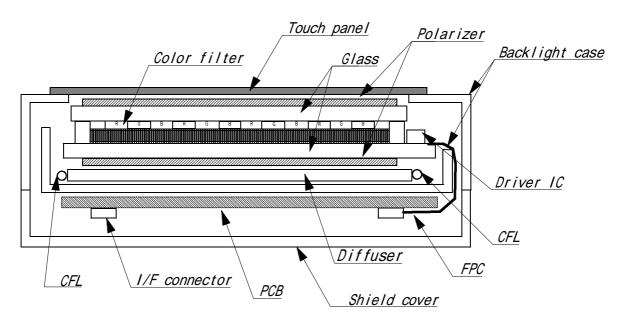
Recommended Inverter : CXA-0217 (TDK)

(Old Part Number : CXA-P1212-VJL)

Polarizer : Glare treatment.

Additional circuits: Timing controller, Power supply (3.3V input)

Touch Panel : Analog type. Non-Grare treatment.



This drawing is showing conception only.

3. Mechanical Specifications

3-1. LCD

| ITEM | SPECIFICATION | UNIT |
|------------------------|--------------------------------|------|
| Outline dimensions | 184 (W) × 139.8 (H) × 14.5 (D) | mm |
| Effective viewing area | 153.7 (W) × 115.8 (H) | mm |
| Dot number | (640×R.G.B) (W) × 480 (H) | Dots |
| Dot pitch | 0.079 (W) × 0.237 (H) | mm |
| Display mode *1 | Normally white | - |
| Mass | (440) | g |

^{*1} Due to the characteristics of the LCD material, the color vary with environmental temperature.

3-2. Mechanical Specifications of touch panel

| ITEM | SPECIFICATION | UNIT |
|------------------|--------------------------------------|------|
| Input | Radius-0.8 stylus or Finger | - |
| Actuation Force | 0.05~0.8 | N |
| Transmittance | Тур.83 | % |
| Surface hardness | pencil hardness 2H or more according | - |

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

| ITEM | SYMBOL | Min. | Max. | UNIT |
|----------------------------|--------|------|------|------|
| Power input voltage | VDD | 0 | 4.0 | V |
| Input signal voltage *1 | Vin | -0.3 | 6.0 | V |
| Touch panel supply voltage | Vtp | 0 | 6.0 | V |
| Touch panel Input current | l tp | 0 | 0.5 | mA |

^{*1} Input signals : CK, $RO \sim R5$, $GO \sim G5$, $BO \sim B5$, Hsync, Vsync, ENAB, R/L, U/D

4-2. Environmental absolute maximum ratings

| ITEM | | SYMBOL | Min. | Max. | UNIT |
|-----------------------|----|--------|------|------|-------|
| Operating temperature | *1 | Тор | -10 | 70 | deg.C |
| Storage temperature | *2 | Tsto | -30 | 80 | deg.C |
| Operating humidity | *3 | Нор | 10 | *4 | %RH |
| Storage humidity | *3 | Hsto | 10 | *4 | %RH |
| Vibration | | - | *5 | *5 | - |
| Shock | | - | *6 | *6 | - |

- *1 Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25 deg.C, another temperature range should be confirmed.
- *2 Temp. = -30 < 48 h , Temp = 80 < 168 h
 Store LCD panel at normal temperature/humidity.
 Keep it free from vibration and shock.
 LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.
 (Please refers to 12. Precautions for use as detail).
- *3 Non-condensation.
- *4 Temp. 40 deg.C, 85%RH Max.
 Temp. > 40 deg.C, Absolute Humidity shall be less than 85% RH at 40 deg.C.

*5

| Frequency | 10 ~ 55 Hz | Converted to acceleration value : |
|-----------------|-------------|-----------------------------------|
| Vibration width | 0.15 mm | $(0.3 \sim 9 \text{ m/s}^2)$ |
| Interval | 10-55-10 Hz | 1 minute |

2 hours in each direction $\,$ X/Y/Z (6 hours as total) EIAJ ED-2531

*6 Acceleration: 490m/s^2 Pulse width : 11 ms

3 times in each direction : $\pm X/\pm Y/\pm Z$.

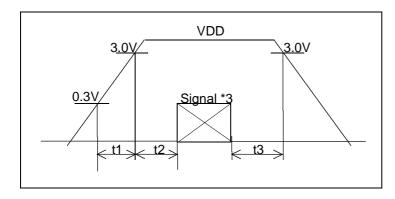
EIAJ ED-2531

5. Electrical Characteristics 5-1. LCD

| VDD = | +3.3V | + | 0.3 | | Temp. | = | -10 ~ 70 |
|-------|-------|----------|------|---|-----------|---|----------|
| VDD — | 10.00 | <u> </u> | 0.00 | • | i Cilip . | _ | 10 70 |

| ITEM | | SYMBOL | MIN | TYP | MAX | UNIT |
|---------------------------|----------------------|--------|--------|-------|--------|-------|
| Power input voltage *1 | | VDD | 3.0 | 3.3 | 3.6 | V |
| Current consumption *2 | VDD=3.3V Temp.=25 | IDD | - | (290) | (435) | mA |
| Permissive input ripple v | oltage(VDD=3.3V) | VRP | - | - | 100 | mVp-p |
| Input signal voltage (L | ow) *3 | VIL | 0 | - | 0.3VDD | V |
| Input signal voltage (H | igh) *3 | VIH | 0.7VDD | - | VDD | V |

*1 VDD-turn-on conditions



0 < t 1 2 0 ms

0 < t 2 5 0 ms

0 < t 3 1s

*2 Power consumption

Black & White pattern :

*3 Input signals : CK, RO~R5, GO~G5, BO~B5, Hsync, Vsync, ENAB, R/L, U/D

5-2. Touch Panel

5-2-1. Terminal resistance

Between xL and xR : 200 \sim 1000 Between yU and yL : 200 \sim 1000

5-2-2. Linearity

±1.5% x : 1.5% or less y : 1.5% or less

5-2-3. Insulation resistance

100M or more at DC25V

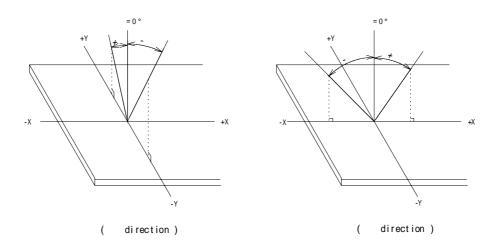
6 . Optical Characteristics

Measuring points = 6.0mm , Temp. = 25

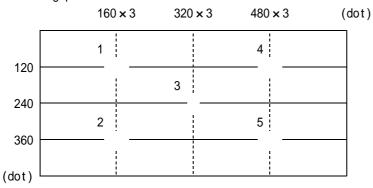
| ITEM | | SYMBOL | CONDI | TION | MIN | TYP | MAX | UNIT |
|--------------------------|--------|---------|--------------|--------|--------|--------|--------|-------|
| Response | Rise | r | = | =0° | - | (5) | - | ms |
| time | Down | d | = | =0° | - | (15) | - | ms |
| | | | +Y | - | (50) | - | | |
| | | | 00 5 | -Y | - | (70) | - | deg. |
| Viewing angle | range | | CR 5 | -X | - | (70) | - | |
| | | | +X | - | (70) | - | deg. | |
| Contrast rati | 0 | CR | = =0° | | (300) | (450) | - | - |
| Duightuss | | | IL=4.0mArms. | | (160) | (240) | - | ad/m² |
| Brightness | | L IL=5. | | mArms. | (210) | (300) | - | cd/m² |
| | Red | х | = =0° | | (0.56) | (0.61) | (0.66) | |
| | | у | | | (0.29) | (0.34) | (0.39) | |
| | 0 | х | | o°. | (0.25) | (0.30) | (0.35) | |
| Oh mamat i a i tu | Green | у | = | =0 | (0.50) | (0.55) | (0.60) | - |
| Chromaticity coordinates | Divis | х | = | 0° | (0.10) | (0.15) | (0.20) | |
| | Blue | у | = | =U | (0.07) | (0.12) | (0.17) | |
| | Wb: to | х | | 0° | (0.26) | (0.31) | (0.36) | |
| | White | У | = | =U | (0.27) | (0.32) | (0.37) | |

6-1. Contrast ratio is defined as follows:

6-2. Definition of viewing angle



6-3. Measuring points

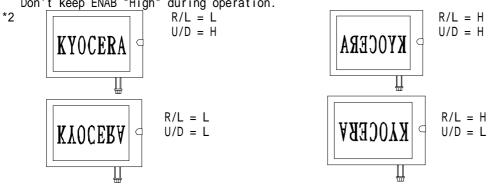


- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp.=25)
- 3) The inverter should meet the eccentric conditions;
 -Sine, symmetric waveform without spike in positive and negative.

7 . Interface signals 7-1. LCD

| PIN NO. | SYMBOL | DESCRIPTION | 1/0 | Note |
|---------|--------|---|-----|------|
| 1 | GND | GND | - | |
| 2 | CK | Clock signal for sampling each data signal | ı | |
| 3 | Hsync | Horizontal synchronous signal (negative) | ı | |
| 4 | Vsync | Vertical synchronous signal (negative) | ı | |
| 5 | GND | GND | - | |
| 6 | R0 | RED data signal (LSB) | ı | |
| 7 | R1 | RED data signal | I | |
| 8 | R2 | RED data signal | I | |
| 9 | R3 | RED data signal | ı | |
| 10 | R4 | RED data signal | I | |
| 11 | R5 | RED data signal (MSB) | ı | |
| 12 | GND | GND | - | |
| 13 | G0 | GREEN data signal (LSB) | I | |
| 14 | G1 | GREEN data signal | I | |
| 15 | G2 | GREEN data signal | I | |
| 16 | G3 | GREEN data signal | ı | |
| 17 | G4 | GREEN data signal | I | |
| 18 | G5 | GREEN data signal (MSB) | ı | |
| 19 | GND | GND | - | |
| 20 | В0 | BLUE data signal (LSB) | I | |
| 21 | B1 | BLUE data signal | I | |
| 22 | B2 | BLUE data signal | I | |
| 23 | B3 | BLUE data signal | - 1 | |
| 24 | B4 | BLUE data signal | I | |
| 25 | B5 | BLUE data signal (MSB) | 1 | |
| 26 | GND | GND | - | |
| 27 | ENAB | Signal to settle the horizontal display position (positive) | I | *1 |
| 28 | VDD | 3.3V power supply | - | |
| 29 | VDD | 3.3V power supply | - | |
| 30 | R/L | Horizontal display mode select signal | I | *2 |
| | | L : Normal , H : Left / Right reverse mode | | |
| 31 | U/D | Vertical display mode select signal | I | *2 |
| | | H : Normal , L : Up / Down reverse mode | | |
| 32 | NC | No connect | - | |
| 33 | GND | GND | - | |

*1 The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 8-2. Don't keep ENAB "High" during operation.



7-2. CFL

| <u> </u> | _ | |
|----------|--------|-----------------------------------|
| PIN NO. | SYMBOL | DESCRIPTION |
| 1 | Hot | Inverter output high voltage side |
| 2 | NC | |
| 3 | Cold | Inverter output low voltage side |

: BHR-03VS-1 LCD side connector (JST) Recommended matching connector : SM02-(8.0)B-BHS-1 (JST)

^{*} Please be careful NOT to connect inversely an inverter-output high voltage side to the CFL low voltage side. It may result in damage or electric shock.

7-3. Touch panel

| PIN No. | SYMBOL | DESCRIPTION |
|---------|--------|------------------|
| 1 | уU | y-Upper terminal |
| 2 | xL | x-Left terminal |
| 3 | yL | y-Lower terminal |
| 4 | xR | x-Right terminal |

8 . Timing Characteristics of input signals

8-1. Timing characteristics

| ITE | SYMBOL | MIN | TYP | MAX | UNIT | NOTE | |
|---------------------|---------------------------|------------|--------|---------|---------|-------|--|
| Clask | Frequency | 1/Tc | - | (25.18) | (28.33) | MHz | |
| Clock | Duty ratio | Tch/Tc | (40) | (50) | (60) | % | |
| Data | Set up time | Tds | (5) | - | - | ns | |
| Data | Hold time | Tdh | (10) | - | - | ns | |
| Horizontal sync. | Cyclo | TH | (30.0) | (31.8) | - | μs | |
| signal | Cycle | ΙП | (770) | (800) | (900) | clock | |
| | Pulse width | ТНр | (2) | (96) | (200) | clock | |
| Vertical sync. | Cycle | TV | (515) | (525) | (560) | line | |
| signal | Pulse width | TVp | (2) | - | (34) | line | |
| Horizontal displa | Horizontal display period | | | 640 | | | |
| HsyncClock phas | THc | 10 | - | Tc-10 | ns | | |
| HsyncVsync. pha | TVh | 0 - TH-THp | | TH-THp | ns | | |
| Vertical sync.signa | TVs | (34) | | | line | | |
| Vertical display | TVd | 480 | | | line | | |

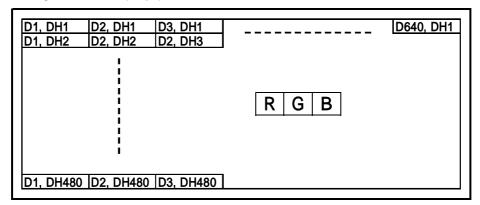
^{*}In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position The horizontal display position is determined by ENAB signal.

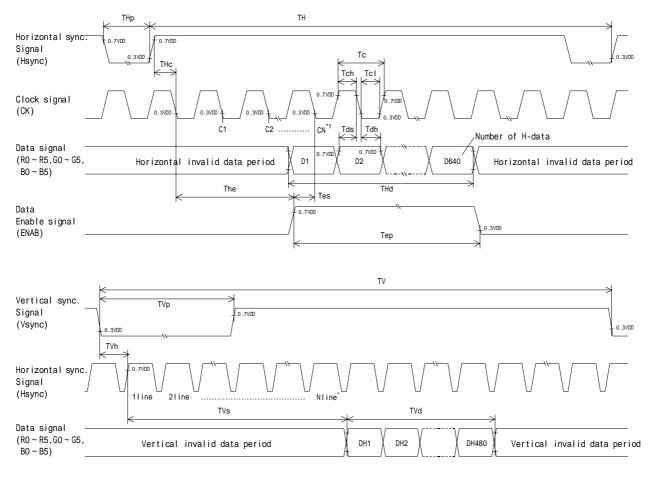
| ITI | SYMBOL | MIN | TYP | MAX | UNIT | NOTE | |
|-------------------|-------------|------|-----|----------|---------|-------|--|
| Frankla simual | Set up time | Tes | (5) | - | (Tc-10) | ns | |
| Enable signal | Pulse width | Тер | (2) | (320) | (TH-10) | clock | |
| HsyncEnable signa | The | (44) | - | (TH-664) | clock | | |

^{*} When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.

- 8-3. Vertical display position
 The vertical display position (TVs) is fixed at 34th line.
 Note) ENAB signal is independent of vertical display position.
- 8-4. Input Data Signals and Display position on the screen



8-5. Input Timing Characteristics



- *1 When ENAB is fixed "Low" and "V/Q=H", the display starts from the data of C104(Clock).
- *2 The vertical display position (TVs) is fixed at 34^{th} line (V/Q=H).

9. Backlight Characteristics

Temp. = 25

| ITEM | SYMBOL | MIN. | TYP. | MAX. | NOTE | |
|--------------------------------|--------|------|--------------|-------------|---------------|-------------|
| Starting discharge Voltage | | VC | - | - | (1,235) Vrms. | -10 |
| discharge vortage | *1 | VS | - | - | (725) Vrms. | 25 |
| Discharging tube current | *2 | IL | (2.0) mArms. | - | (6.0) mArms. | - |
| Discharging tube voltage | | VL | - | (440) Vrms. | - | IL=4.0mArms |
| | | | ı | (410) Vrms. | - | IL=5.0mArms |
| Operating life (IL=4.0 mArms.) | *3 T | Т | (45,000) h | (58,000) h | - | IL=4.0mArms |
| (TL=4.0 IIIATIII5.) | | | (36,000) h | (54,000) h | - | IL=5.0mArms |
| Operating frequency | *4 | F | (40) kHz | - | (100) kHz | - |

- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value: (1,600)Vrms Min.)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 End of life is defined as when the illuminance or quantity of light has decreased to 50% of the initial value. Illuminance of light will drastically decrease when LCD is operated at lower temperature for long hours.
- *4 The driving frequency of the CFL may interfere with the horizontal synch signal ,leaving interference stripes on the display. So please evaluate LCD panels beforehand.

 To avoid interference stripes, we recommend to separate as far as possible the CFL frequency from the horizontal synchronous signal and its high harmonic frequency.
- * There may be cases where interface noise on LCD PCB, generated by high-voltage products such as inverters, may leave stripes on the display. Please be careful when designing a mold to take into consideration that the inverter shall be located as far as possible from PCB. Shield protection may be effective.

1 0 . Design Guidance for Analog Touch-Panel(T/P)

10-1. Electrical

In customer's design, please remember the following considerations.

- 1. Do not use the current regulated circuit.
- 2. Keep the current limit with top and bottom layer. (See Sec, 4-1)
- 3. Analog T/P can not sense two point touching separately.
- 4. A contact resistance is appeared at the touch point between top and bottom layer. After this resistance has stable read the T/P position data.
- 5. Analog T/P is also a "Capacitor" in an equivalent circuit.

 Design your sensing circuit and low-pass filter with considering this "Capacitor" value.
- 6. Because noise of inverter or peripheral circuits may interfere signal of touch panel itself it is necessary to design carefully in advance to avoid these noise problem.

10-2. Software

- 1. Do the "User Calibration".
- 2. "User Calibration" may be needed with long term using. Include "User Calibration" menu in your software.
- 3. When drawing a line with a stylus, there may be a slight discontinuity when the stylus passes over a spacer-dot. If necessary, please provide a compensation feature within your software.

10-3. Mounting on display and housing bezel

- 1. Do not use an adhesive tape to bond it on the front of T/P and hang it to the housing bezel.
- 2. Never expand the T/P top layer (PET-film) like a balloon by internal air pressure. The life of the T/P will be extremely short.
- If a dew will be on the heat-sealed area or exposed traces at the end of a flexible tail, the migration of silver can occur.
 This will cause sometimes a short circuit.

11. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



| YEAR | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 |
|-------|------|------|------|------|------|------|
| TEAR | 2005 | 2006 | 2007 | 2006 | 2009 | 2010 |
| CODE | 5 | 6 | 7 | 8 | 9 | 0 |
| | | | | | | |
| MONTH | JAN. | FEB. | MAR. | APR. | MAY. | JUN. |
| CODE | 1 | 2 | 3 | 4 | 5 | 6 |
| | | | | | | |
| MONTH | JUL. | AUG. | SEP. | OCT. | NOV. | DEC. |
| CODE | 7 | 8 | 9 | Х | Υ | Z |

12. Warranty

12-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

12-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

13. Precautions for use

13-1. Installation of the LCD

- 1. LCD hole(right side) are not connected with GND, but the LCD is structured to have GND connection available to protect against noise. We recommend to connect customer's frame GND to LCD frame in order to stabilize the display performance.
- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. Please refer to the following our recommendable value of Clamp-down torque when installing. Clamp-down torque: 3.3+/-0.5kgf.cm

Please set up'SPEED-LOW', 'SOFT START-SLOW' when using electric driver .

Recommendable screw P-TITE screw two types nominal dia.3.0mm installing boss hole depth 5.5mm Max

Please be careful not to use high torque which may damage LCD module in installation.

- 7. Do not pull the CFL lead wires and do not bend the root of the wires.
 Housing should be designed to protect CFL lead wires from external stress.
- 8. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas.

Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

13-2. Static Electricity

1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operator should wear ground straps.

13-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

<u>This phenomenon may not recover.</u> The LCD shall be operated within the temperature limits specified.

13-4. Storage

- 1. The LCD shall be stored within normal temperature and humidity. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. The LCD should be packaged to prevent damage.

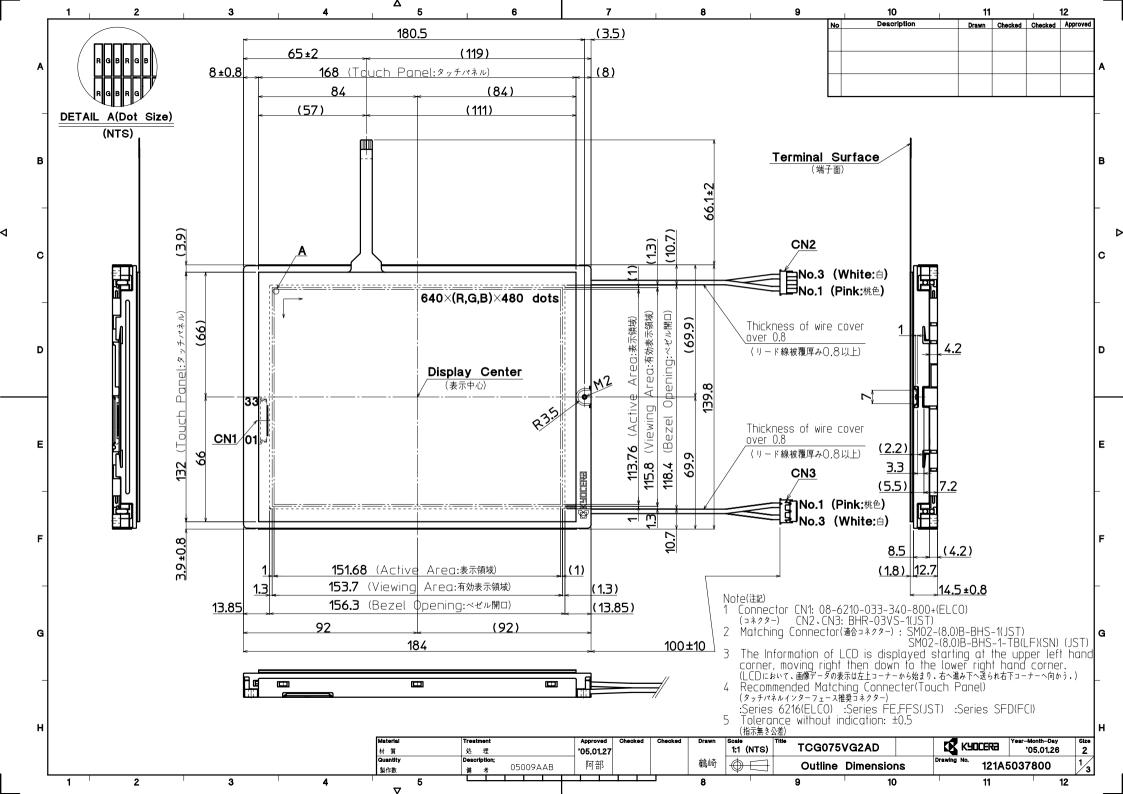
13-5. Screen Surface

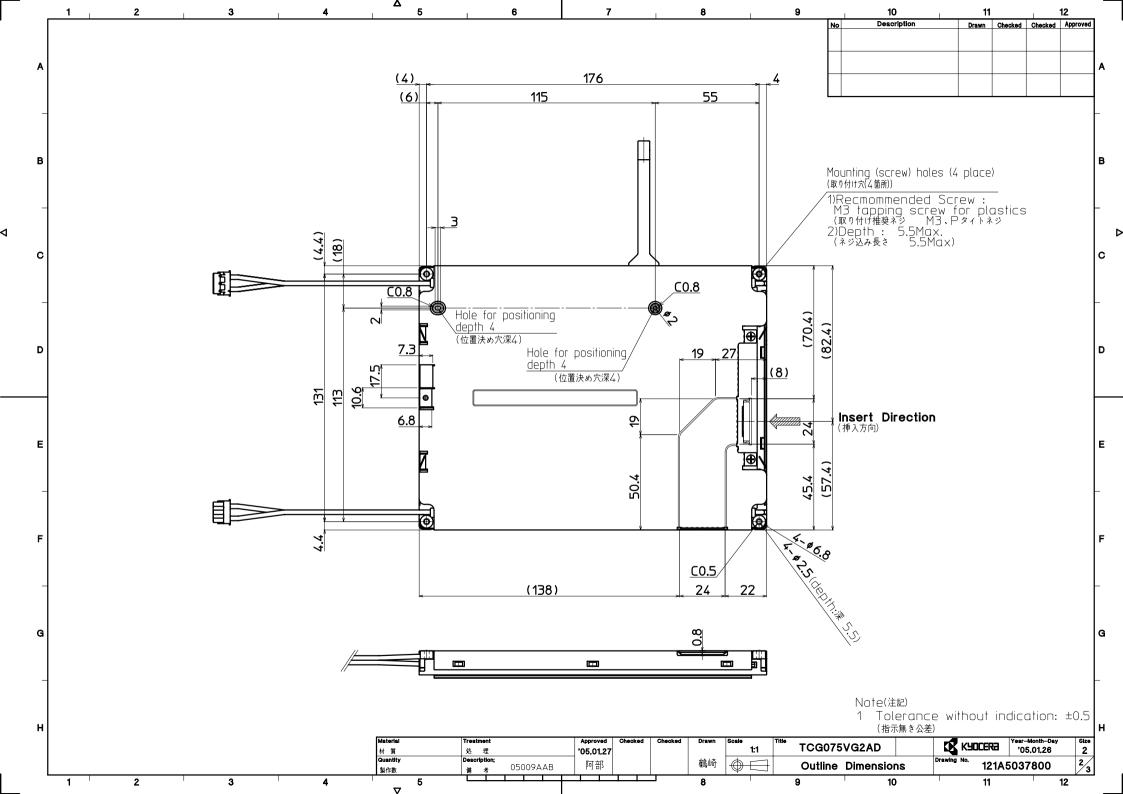
- 2. Do not push or rub the touch panel's surface with hard to sharp objects such as knives, or the touch panel may be scratched.
- 3. When the touch panel is dirty, gently wipe the surface with a soft cloth, sometimes moistened by mild detergent or alcohol. If a hazardous chemical is dropped on the touch panel by mistake, wipe it off right away to prevent human contact.
- 4. Touch panel edges are sharp. Handle the touch panel with enough care to prevent cuts.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.

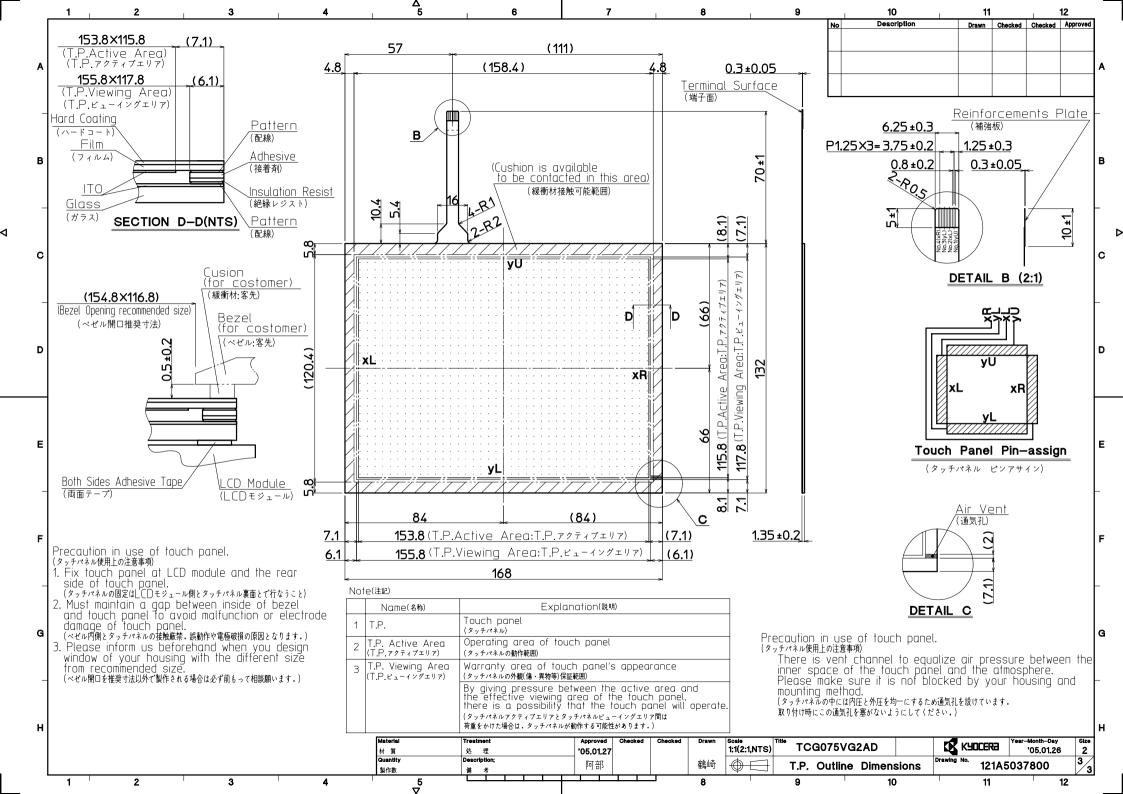
14. Reliability Data / Environmental Test

| TEST ITEM | TEST CONDITION | TEST TIME | RESULT |
|--------------------------------------|---|-------------------------|---|
| High Temp. Atmosphere | 80°C | 240 h | Display Quality : No defect Display Function : No defect Current Consumption : No defect |
| Low Temp. Atmosphere | -30℃ | 240 h | Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect |
| High Temp. Humidity Atmosphere | 40℃ 90 %RH | 240 h | Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect |
| Temp. Cycle | -30°C 0.5 h R. T. 0.5 h 80°C 0.5 h | 10 cycles | Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None |
| High Temp. Operation | 70°C | 500 h | Display Quality : No defect Current Consumption : No defect |
| Point Activation life | Polyacetal stylus(R0.8) Hitting force 3N Hitting speed 2 time/s | one million times | Satisfy spec below item Terminal resistance Insulation resistance Linearity Actuation Force |

- * Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- * The LCD is tested in circumstances in which there is no condensation.
- * The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.







| SPEC. NO. | TQ3C-8EAF0-E2DDH12-00 | | | | | |
|-----------|-----------------------|--|--|--|--|--|
| DATE | January 26, 2005 | | | | | |

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KYOCERA INSPECTION STANDARD

KYOCERA CORPORATION KAGOSHIMA HAYATO PLANT LCD DIVISION

| Original | Designed | by :Engineeri | ng Dept. | Confirmed by :QA Dept. | | |
|------------------|------------|---------------|------------|------------------------|----------|--|
| Issue Data | Prepared | Checked | Approved | Checked | Approved | |
| January 26, 2005 | A. Mishino | Y. Yamazaki | M.Fujitani | y yoshita. | S Hayash | |

Revision Record

| Date | | Designed by: En | | Engineering D | ept. | Confirmed by: QA Dept. | | |
|---------|-----|-----------------|----------|---------------|---------|------------------------|---------|----------|
| Da | аге | | Prepared | | Checked | Approved | Checked | Approved |
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1) Note

| | Note | | | | | | |
|-------------------------------|--|---|---|--|--|--|--|
| General | 1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. | | | | | | |
| | 2. Inspection Conditions Luminance : 500 Lux minimum Inspection distance : 300 mm (from the sample) : $25 \pm 5 \degree C$ Direction : directly above | | | | | | |
| Definition of Inspection item | Dot defect | Bright dot defect | The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool:5% Transparency neutral density filter. Count dot:If the dot is visible through the filter Don't count dot:If the dot is not visible through the filter. RGBRGBRGB RGBRGBRGB RGBRGBRGB | | | | |
| | | Black dot defect | The dot is constantly "off" when power applied to the LCD, even when all "white" data sent to the screen. | | | | |
| | | Adjacent dot | Adjacent dot defect is defined as two or more bright dot defects or black dot defects. | | | | |
| | | | RGBRGBRGB RGBRGBRGB RGBRGBRGB | | | | |
| | External inspection | Bubble, Scratches, Foreign particle (Polarizer, Cell, Backlight) | Visible operating (all pixcels "Black" or "White") and non operating. | | | | |
| | | Appearance inspection | Does not satisfy the value at the spec. | | | | |
| | Others | CFL wires | Damaged to the CFL wires, connector, pin, functional failure or appearance failure. | | | | |
| | Definition of size | Definition of ci | Definition of linear size | | | | |

2) Standard

| Classification Inspection item | | on item | Judgement standard | | | | | |
|--------------------------------|------------------|--|----------------------|--|--|-------------------------------|----------------------------------|--|
| defect Dot defect | | Bright dot defect | | Acceptable number : 4 bright dots defects Bright dot spacing : 5 mm or more | | | | |
| glass) | Black dot defect | | Acceptable number | | | | | |
| | | 2 dots join | Bright dot defect | Acceptable number : 2 | | | | |
| | | | Black dot defect | Acceptable number : 3 | | | | |
| Others | | 3 or more dots join | | Acceptable number : 0 | | | | |
| | | Total dot defects | | Acceptable number : 5 Max | | | | |
| | | White dot, Dark dot (Circle) | | Size (mm) | | | Acceptable Number | |
| | | | | d<0.2 | | (neglected) | | |
| | | | | 0.2 <d≤0.4< td=""><td colspan="2">5</td></d≤0.4<> | | 5 | | |
| | | | | 0. 4 < d ≤ 0. 5 | | 3 | | |
| | | | | 0.5 <d< td=""><td></td><td colspan="2">0</td></d<> | | 0 | | |
| | | | | | | | | |
| | pection | Polarizer (Scratches) | | Width (mm) | Length(n | nm) | Acceptable Number | |
| (Defect o Polarize | er or | | | W≦0.1 | - | | (neglected) | |
| between -er and | Polariz LCD | | | 0.1/W<0.2 | L≦ | 5. 0 | (neglected) | |
| glass) | | | | $0.1 < W \le 0.3$ | 5.0 <l< td=""><td></td><td>0</td></l<> | | 0 | |
| | | | | 0.3 <w< td=""><td>-</td><td></td><td>0</td></w<> | - | | 0 | |
| | | Polarizer Touch panel | | | | | | |
| | | (Bubble, Dent) | | Size(mm) | | Acceptable Number | | |
| | | | | d<0.2 | | (neglected) | | |
| | | | | 0. 2 < d ≤ 0. 3 | | 5 | | |
| | | | | $0.3 < d \le 0.5$ | | 3 | | |
| | | | | 0.5 <d< td=""><td colspan="2">0</td></d<> | | 0 | | |
| | | Foreign Particle(Circular | | Size(mm) | | A | | |
| | | shape) | | d<0.2 | | Acceptable Number (neglected) | | |
| | | | | $0.2 < d \le 0.4$ | | (neglected) 5 | | |
| | | | | $0.2 < d \le 0.4$ $0.4 < d \le 0.5$ | | 3 | | |
| | | | | 0. 5 < d | | 0 | | |
| | | | | | | | | |
| | | Foreign Particle (Linear shape), Scratches | | Width (mm) | Length(n | nm) | Acceptable Number | |
| | | | | W≦0.03 | _ | | (neglected) | |
| | | | | 0.03<₩≦0.1 | L≦ | 2.0 | (neglected) | |
| | | | | | 2.0 <l≦< td=""><td>4. 0</td><td>3</td></l≦<> | 4. 0 | 3 | |
| | | | | | 4.0 <l< td=""><td></td><td>0</td></l<> | | 0 | |
| | | | | 0.1 <w< td=""><td>-</td><td></td><td>(According to Circular shape)</td></w<> | - | | (According to Circular shape) | |
| | | | | | | | | |

| Classification | Inspection item | Judgement standard | | | | |
|----------------------|--|---|------------|-------------------|--|--|
| Touch Screen portion | Scratch | Width(mm) | Length(mm) | Acceptable number | | |
| | | W < 0.05 | | neglected | | |
| | | $0.05 \le W < 0.10$ | 10 < L | 3 | | |
| | | 0.10 ≤ W | | 0 | | |
| | Glass crack (Corner crack) | | | | | |
| | | X | Y | Z | | |
| | | OK ≦ 3 | 0K ≦ 3 | 0K ≦ t | | |
| | | ·If one of X,Y,Z is not satisfied, it is regarded as NG. ·Regarding the corner crack, within 0.5 mm depth is regarded as OK. (t=thickness of Touch panel) | | | | |
| | Glass crack (Cracks in other area than in corner) | Z. | | | | |
| | | X | Y | Z | | |
| | | OK ≦ 3 | 0K ≦ 3 | 0K ≦ t | | |
| | | ·If one of X, Y, Z is not satisfied, it is regarded as NG. ·Regarding the corner crack, within 0.5 mm depth is regarded as OK. (t=thickness of Touch panel) | | | | |
| | | | | | | |