TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC9235P,TC9235F

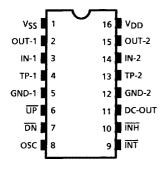
#### Electronic Volume Control IC

The TC9235P/F is an electronic volume control IC developed for use in audio equipment such as car stereo and portable stereo system.

#### **Features**

- Attenuation can be controlled from 0dB to -78dB by up, down input.
- This IC have 20dB tap for loudness circuit.
- This IC features a built-in DC output circuit (7 level) for volume level meter.
- Polysilicon resistors enables low-distortion, high-performance volume systems.
- Volume level remains in backup mode with low current consumption.
- Package is DIP16 and SOP16.

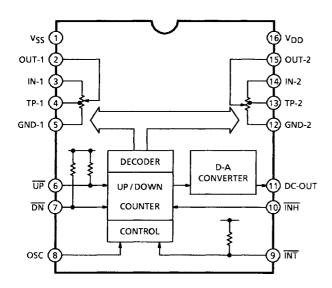
#### **Pin Assignment**



# TC9235P DIP16-P-300-2.54A TC9235F SOP16-P-300-1.27

Weight DIP16-P-300-2.54A: 1.0 g (typ.) SOP16-P-300-1.27: 0.16 g (typ.)

#### **Block Diagram**



# **Pin Function**

Pin No.	Symbol	Pin Name	Function and Operation	Note		
1	V <sub>SS</sub>	Negative power supply pin	Power cumply terminal	_		
16	V <sub>DD</sub>	Positive power supply pin	- Power supply terminal			
2	OUT-1	Valuma autnut nina		_		
15	OUT-2	Volume output pins	Volume circuit			
3	IN-1	Malaura a la mada mina	_			
14	IN-2	Volume input pins	OUT O			
4	TP-1	Tap output pins for	IN O			
13	TP-2	loudness	GND			
5	GND-1	A				
12	GND-2	Analog ground pins				
6	<del>UP</del>	Volume up input pin	Volume up, down control input pin.			
7	DN	Volume down input pin	The 1 step/1 push volume is controlled by pushing the Up or Down key. If the key has been pushed continuously, the continuous volume control.	Built-in pull-up resistor		
			Oscillation pin.			
8	osc	Oscillation pin	Oscillator circuit consist of C·R connection.	_		
			Oscillation is executed while key is pushed.			
9	ĪNT	Initializing pin	Input pin for setting initial volume level volume level set to 46dB by "L" input.	Built-in pull-up resistor		
	ĪNH	Inhibit terminal	Back up mode input pin.			
10			Internal all operation is stopped by "L" input, and volume level remains with low current consumption.	_		
	DC-OUT	DC output pin for level meter	DC output pin for volume level meter.			
11			DC voltage which is corresponded to volume step is generated.	_		

#### Operation

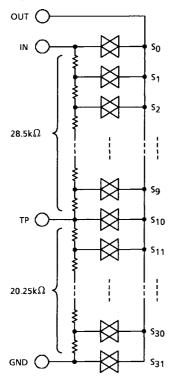
#### 1. Volume Circuit

Volume circuit consist of ladder resistor and analog switch.

Tap for loudness is connected to step 10 (20dB).

Attenuation is as follows when resistor (3.9 k $\Omega$ ) is connected between TP pin and GND pin.

#### **Equivalence Circuit**



#### **Volume Step and Attenuation**

(attenuation is as follows when resistor (3.9  $k\Omega)$  is connected between TP pin and GND pin.)

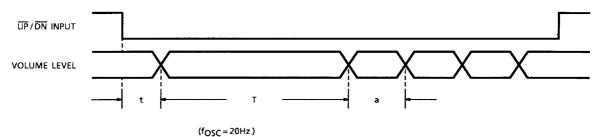
Step	Step Attenuation		Attenuation	
0	0 (dB)	16	32 (dB)	
1	2	17	34	
2	4	18	36	
3	6	19	38	
4	8	20	40	
5	10	21	42	
6	12	22 (Note 1)	46	
7	14	23	50	
8	16	24	54	
9	18	25	58	
10	20	26	62	
11	22	27	66	
12	24	28	70	
13	26	29	74	
14	28	30	78	
15	30	31	∞	

Note 1: Step 22 (46dB) initial value.

#### 2. Volume Up, Down Control Circuit

Volume up, down control is executed by  $\overline{UP}$ ,  $\overline{DN}$  key input.

- The 1 step/1 push volume is controlled by "L" level of  $\overline{UP}$ ,  $\overline{DN}$  key.
- If  $\overline{UP}$ ,  $\overline{DN}$  key is input "L" continuously, volume level is changed continuously.
- · Timing of key input



t: Prevent time for chattering

 $\approx 2.2 \times 1/f_{OSC} (\approx 110 \text{ ms})$ 

3

T: Switching time to automatic mode  $\approx 10 \times 1/f_{OSC}$  ( $\approx 500$  ms)

a: Up, down speed  $\approx 2 \times 1/f_{OSC} (\approx 100 \text{ ms})$ 

 $f_{OSC} \approx C_X \cdot R_X \text{ (Hz): } R_X = 12 \sim 220 \text{ k}\Omega$ 

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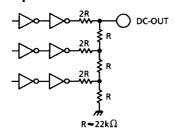
#### 3. DC Output Circuit for Volume Level

DC output for volume level meter is internally connected to D-A converter (R/2 R type).

8 stage output voltage which is corresponded to volume level is generated.

Because output impedance  $\approx 22~k\Omega$  (typ.) is high, if input impedance of next setting level meter IC is low, set to Buffer.

#### **Equivalence circuit**



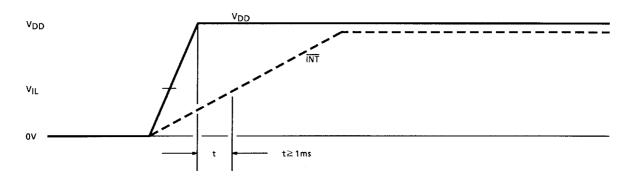
#### **Volume Step and Output Voltage**

Step	Attenuation (dB)	Output Voltage (V)			
0~3	0~6	7/8 V <sub>DD</sub>			
4~7	8~14	6/8 V <sub>DD</sub>			
8~11	16~22	5/8 V <sub>DD</sub>			
12~15	24~30	4/8 V <sub>DD</sub>			
16~19	32~38	3/8 V <sub>DD</sub>			
20~23	40~50	2/8 V <sub>DD</sub>			
24~27	54~66	1/8 V <sub>DD</sub>			
28~31	70~∞	0			

#### 4. Initialization and Backup Operation

#### (1) Initialization operation

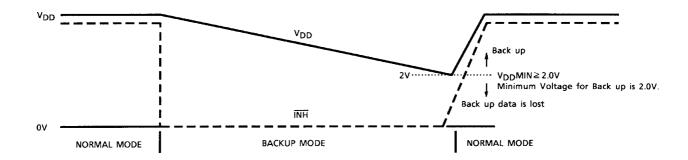
When power on, volume level is set to initial value (46dB) by setting  $\overline{\text{INT}}$  pin to "L" level for a while.



Adjust condenser value which is set  $\overline{INT}$  pin to the period while  $\overline{INT}$  pin is "L" level is longer than 1 ms when power on.

#### (2) Backup operation

Internal operation is all stopped when  $\overline{INH}$  pin is "L" level, and prohibit input and output. Volume data is remains while Backup mode with low current consumption.



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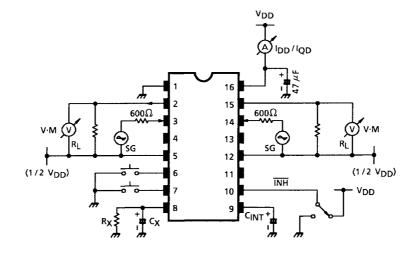
# **Maximum Ratings (Ta = 25°C)**

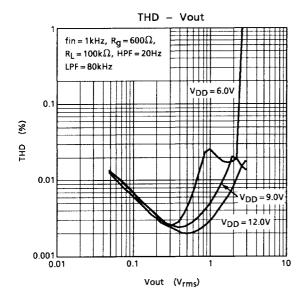
Characteristics	Symbol	Rating	Unit	
Supply voltage	$V_{DD}$	-0.3~15	V	
Input voltage	V <sub>IN</sub>	$-0.3 \sim V_{DD} + 0.3$	V	
Power dissipation	P <sub>D</sub>	300	mW	
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Storage temperature	T <sub>stg</sub>	-55~150	°C	

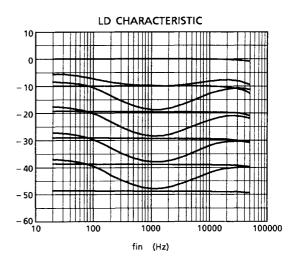
# Electrical Characteristics (unless otherwise specified, Ta = 25°C, $V_{DD} = 9$ V)

Characteristics		Symbol	Test Circuit	Test C	Condition	Min	Тур.	Max	Unit
Operating supply voltage		V <sub>DD</sub>	_	Ta = -40~85°C		4.5	9.0	12	V
Operating supply current		I <sub>DD</sub>	1	No load, f <sub>OSC</sub> = 20 Hz		_	0.3	1.0	mA
Backup voltage		$V_{QD}$	_	INH = "L"		2.0	~	12	V
Backup current		I <sub>QD</sub>	1			_	0.01	1.0	μА
Input voltage	"H" level	VIH		Allianataia		V <sub>DD</sub> × 0.7	~	V <sub>DD</sub>	V
	"L" level	V <sub>IL</sub>		All input pin		0	~	V <sub>DD</sub> × 0.3	
lanut aurrant	"H" level	I <sub>IH</sub>		INH input pin	$V_{IH} = V_{DD}$	-1	_	1	μА
Input current	"L" level	I <sub>IL</sub>			V <sub>IL</sub> = 0 V	-1	_	1	
Pull up resistor		R <sub>UP</sub>	_	UP , DN , INT input pin		23	47	71	kΩ
Volume resistor	Volume resistor		_	Between IN → GND resistor		31	44	58	kΩ
Analog switch ON resistor		R <sub>ON</sub>	_	Analog switch ON resistor		_	500	800	Ω
Attenuation error		ΔATT	_	_		_	0	±2.0	dB
Balance between left and right		$\Delta R_{VR}$	_	Volume resistor error between left and right		_	0	±3.0	%
Total harmonic distortion		THD	1	f <sub>IN</sub> = 1 kHz	0dB	_	0.01	_	%
Maximum attenuation		ATT <sub>MAX</sub>		$V_{IN} = 1 V_{rms}$	∞dB	_	100	_	dB
Cross talk		С·Т		$R_L = 100 \text{ k}\Omega$	OdD	_	100	_	dB
Output noise voltage		V <sub>N</sub>		$R_g = 600 \Omega$	0dB	_	2.0	_	$\mu V_{rms}$
OSC frequency		fosc	1	$C_X = 2.2 \mu F$ , $RX = 33 k\Omega$		_	20	_	Hz

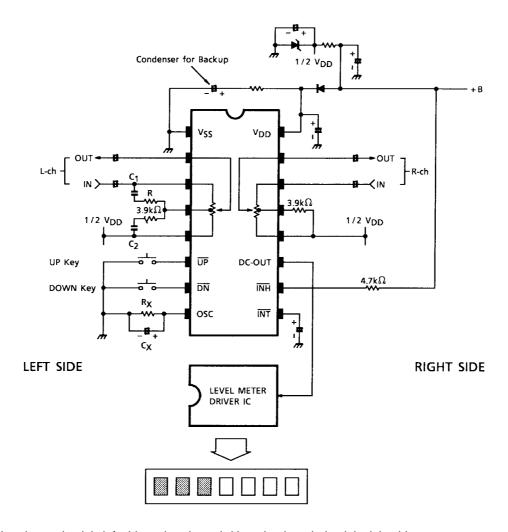
#### **Test Circuit 1**







# **Example of Application Circuit**

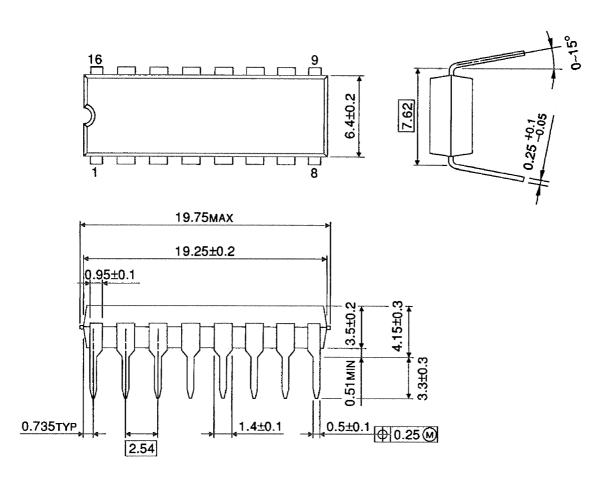


Note 2: Loudness circuit is left side, only volume (without loudness) circuit is right side.

$$C_1 = 1500 \; pF \quad C_2 = 0.1 \; \mu F \quad R = 8.2 \; k \Omega \label{eq:constraint}$$

# **Package Dimensions**

DIP16-P-300-2.54A Unit: mm



Weight: 1.0 g (typ.)

0.8±0.2

# **Package Dimensions**

SOP16-P-300-1.27

Unit:mm

16
9
0.705TYP

10.8MAX
10.3±0.2

10.8MAX
10.3±0.2

10.8MAX

Weight: 0.16 g (typ.)

ZZ 0.1

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Handbook" etc..

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