TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WBD126FK

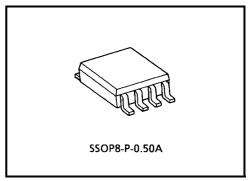
Dual Bus Switch with Level Shift

The TC7WBD126FK is a low on-resistance, high-speed CMOS 2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

The internal diode which adds to power supply line is enable to realize the shift of signal level from 5 V to 3.3 V. (Note 1)

All inputs are equipped with protector circuits to protect the device from static discharge.



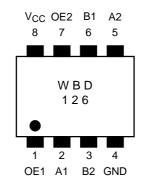
Weight: 0.01 g (typ.)

Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation: tpd = 0.25 ns (max)
- Ultra-low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- Electro-static discharge (ESD) performance: ± 200 V or more (JEITA) ± 2000 V or more (MIL)
- TTL level input (control input)
- Package: US8

Note 1: In case that over-shoot noise is detected, this device should be used with clamp diode to prevent the next stage device from over-stress.

Pin Assignment (top view)

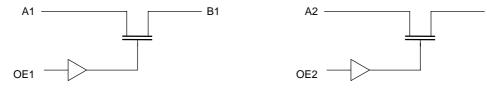




Truth Table

Inputs	Function		
OE	Function		
L	Disconnect		
Н	A port = B port		

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	Vs	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	P _D	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V



Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Character	istics	Symbol	Test Condition		V _{CC} (V)	Min	Typ. (Note 2)	Max	Unit	
Input voltage	"H" level	V_{IH}	_		4.5~5.5	2.0	_	_	V	
input voltage	"L" level	V _{IL}	_		4.5~5.5	_	_	0.8	v	
High-level output	voltage	V _{OH}	Figure 4		_	_	_	_	_	
Input leakage curi	rent	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μА	
Power off leakage	current	I _{OFF}	A, B, OE = 0~5.5 V		0	_	_	±1.0	μΑ	
Off-state leakage (switch off)	current	I _{SZ}	A, B = 0~5.5 V, OE = GND		4.5~5.5	_	_	±1.0	μА	
ON resistance			V _{IS} = 0 V	$I_{IS} = 30 \text{ mA}$	4.5	_	5	7		
ON resistance	(Note 3)	R _{ON}	R _{ON}	VIS - U V	I _{IS} = 64 mA	4.5	_	5	7	Ω
(Note 3)		V _{IS} = 2.4 V, I _{IS} = 15 mA		4.5	_	35	50			
	laa		$V_{IN} = V_{CC}$ or GND $I_{OUT} = 0$	Switch ON	5.5	_	_	1.5	mA	
Quiescent supply current		Ü		Switch OFF	5.5		_	10	μА	
		Δlcc	V _{IN} = 3.4 V (one input)	(Note 4)	5.5	_	_	2.5	mA	

Note 2: Typical values are at $V_{CC} = 5 \text{ V}$ and $Ta = 25^{\circ}\text{C}$.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

Note 4: Quiescent supply current at $V_{CC} = 3.4 \text{ V}$ will be sum of I_{CC} and ΔI_{CC} .

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note 5)	4.5	_	0.25	ns
Output enable time	t _{pZL} t _{pZH}	Figure 1, Figure 3	4.5		4.5	ns
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3	4.5	_	5.5	ns

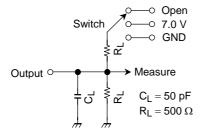
Note 5: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Note 6)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = GND (Note 6)	5.0	10	pF

Note 6: This parameter is guaranteed by design.

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	7.0 V
t _{pHZ} , t _{pZH}	Open

Figure 1

AC Waveform

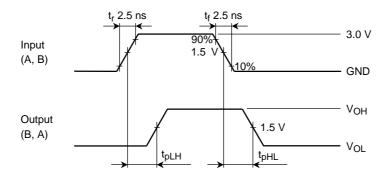


Figure 2 t_{pLH}, t_{pHL}

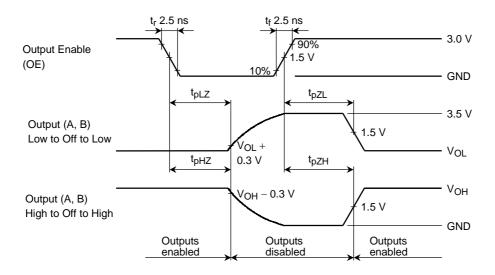
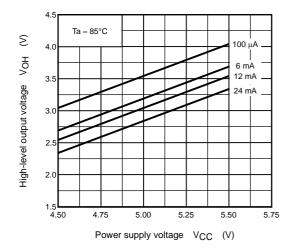
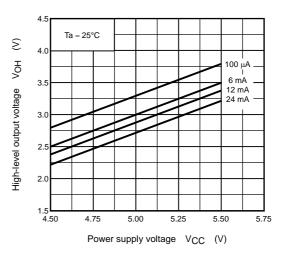


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

V_{OH} – V_{CC} Characteristics (typ.)





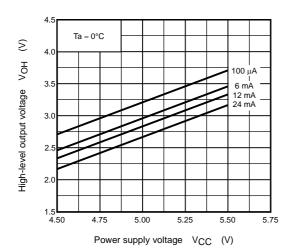
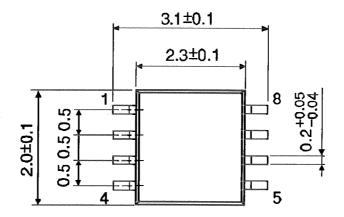


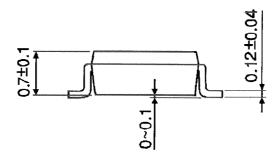
Figure 4

Package Dimensions

SSOP8-P-0.50A







Weight: 0.01 g (typ.)

TOSHIBA TC7WBD126FK

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2001-12-04