TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ07AFE

NON-Inverter (Open Drain)

Features

High output drive: ±24 mA (min)

at $V_{CC} = 3 V$

Super high speed operation: t_{pZL} 2.3 ns (typ.)

at $V_{CC} = 5 \text{ V}, 50 \text{ pF}$

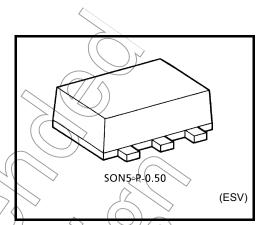
Operation voltage range: V_{CC} (opr.) = 1.65~5.5 V

• 5.5-V tolerant inputs

• 5.5-V power down protection outputs

 Matches the performance of TC74LCX series when operated at 2.2 NV/co.

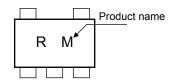
3.3 -V V_{CC}

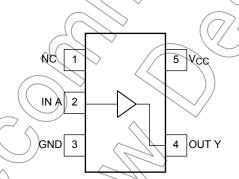


Weight: 0.003 g (typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Ratings (Ta = 25°C)

		\ \ \ \ \ / / / / /	
Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5~6	V
DC input voltage	VIN	-0.5~6	V
DC output voltage	Vout	-0.5~6 (Note 1)	V
Input diode current	/lik	-20	mA
Output diode current	tok	-20 (Note 2)	mA
DC output current	1601	50	mA
DC V _{CC} /ground current	lcc)	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	TL	260	°C

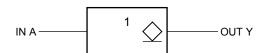
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: I_{OUT} absolute maximum rating must be observed

Note 2: V_{OUT} < GND

Logic Diagram



Truth Table

Α	Υ
L	L
Н	Z

Z: High impedance

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	1.65~5.5 (Note 3)	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~VcC	$\langle v \rangle$
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	d _t /d _v	$0 \sim 20 \text{ (VCC} = 1.8 \text{ V}, 2.5 \text{ V} \pm 0.2 \text{ V})$ $0 \sim 10 \text{ (VCC} = 3.3 \text{ V} \pm 0.3 \text{ V})$ $0 \sim 5 \text{ (VCC} = 5.5 \text{ V} \pm 0.5 \text{ V})$	ns/y

Note 3: Data retention only

2

Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
Characteris	ucs	Symbol	rest Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High level	V _{IH}	_		1.65~1.95	0.75 × V _{CC}	_ <	X	0.75 × V _{CC}		V	
	VIН			2.3~5.5	0.7 × V _{CC}			0.7 × V _{CC}			
input voltago	Low level	V/··			1.65~1.95		\bigcirc	0.25 × Vcc	_	0.25 × V _{CC}	V
Low level	V _{IL}			2.3~5.5	-(-0.3 × V _{CC}	_	0.3 × V _{CC}		
Z-state output leak	age current	lLKG	VIN = VIH	~5.5 V	1.65~5.5			±5		±10	μΑ
		/ level V _{OL}	$V_{IN} = V_{IL} {I_{OL}}$ I_{OL} I_{OL}		1.65	7	\triangleright_0	0.1	4	\$	>
				100 1	2.3	_\\	0	0.1		> 0.1	
				$I_{OL} = 100 \mu A$	3.0)+	0 🔷	0.1)/	0.1	
Output voltage Low level	Lowlovel				4.5	_	0	0.1	40)	0.1	
	Low level			I _{OL} = 8 mA	2.3	_	0.1	0.3	> _	0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	_ (0,22 <	0.55	_	0.55	
				lol ≠ 32 mA	4.5		0.22	0.55	_	0.55	
Input leakage curre	ent	I _{IN}	V _{IN} = 5.5 \	/ or GND	0~5.5	_	_	±1	_	±10	μΑ
Power off leakage	current	loff	V _{IN} or V _Ø (JT = 5.5 V	0.0)]	1	_	10	μΑ
Quiescent supply c	urrent	Icc	V _{IN} = 5.5	VorGND	5.5			2		20	μА



AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -4	Unit	
Characteristics Symbo		rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time		C_L = 50 pF, R_L = 500 Ω	1.8 ± 0.15	1.8	5.5	9.5	1.8	10.5	
	t _{pZL}		2.5 ± 0.2	1.2	3.7 <	5.8	1.2	6.4	- ns
			3.3 ± 0.3	0.8	2.9	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	2.3	(3.5	9.5	3.9	
	t _{pLZ}	C_L = 50 pF, R_L = 500 Ω	1.8 ± 0.15	1.8	4.3	9.5	1.8	10.5	
			2.5 ± 0.2	1,2	2.8//	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	21	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	1.4	3.5	0.5	3.9	
Input capacitance	C _{IN}		0~5.5		1				pF
Output capacitance	C _{OUT}		0~5.5 〈		>8		*	$/\!\!\!/$	pF
Power dissipation capacitance	C _{PD}	(Note 4)	3.3 5.5		20	- (-	pF

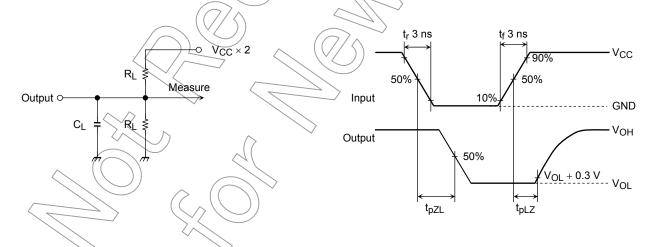
Note4: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

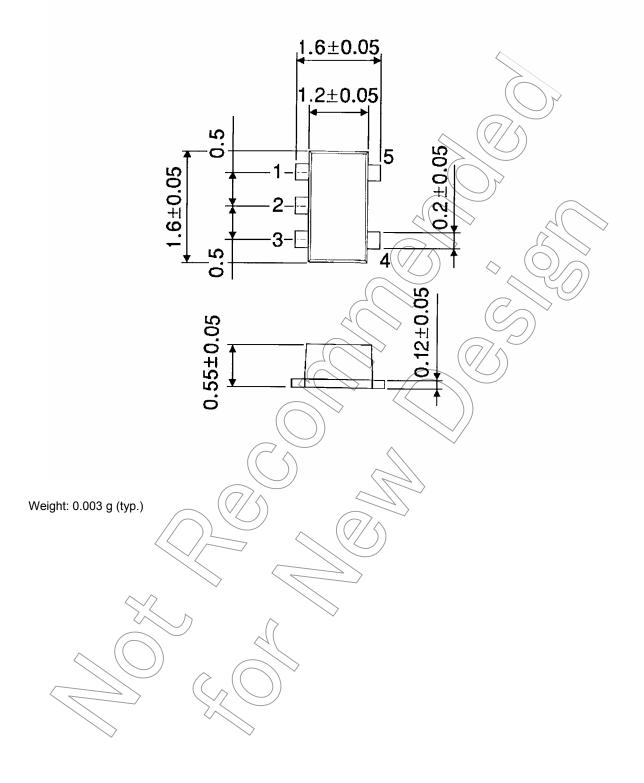
AC Characteristics Measurement Circuit

AC Waveforms



Package Dimensions

SON5-P-0.50 Unit: mm



5 2007-11-01



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20070701-EN GENERAL

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6 2007-11-01