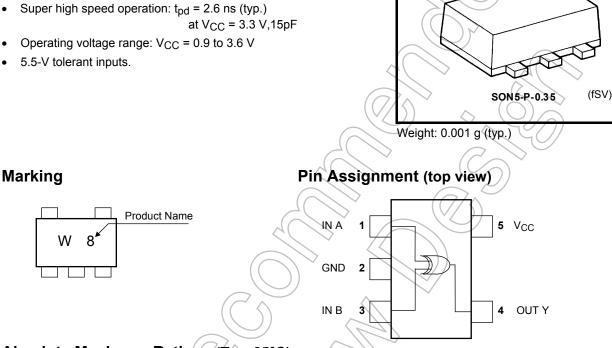
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG86AFS

2-Input EXCLUSIVE OR Gate

Features

- High output current: ±8 mA (min) at V_{CC} = 3.0 V
- Super high speed operation: tpd = 2.6 ns (typ.)
- Operating voltage range: V_{CC} = 0.9 to 3.6 V
- 5.5-V tolerant inputs.



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc 🦯	-0.5 to 4.6	V
DC input voltage	VIN	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	IK	-20	mA
Output diode current	IOK	±20 (Note 1)	mA
DC output current	TOUT	±25	mA
DC V _{CC} /ground current	(Icc)	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

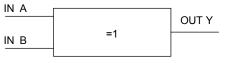
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 2005-01

TOSHIBA

IEC Logic Symbol



Truth Table

IN A IN B =1	OUT Y	L L L H H L	Y L H H L
Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	0.9 to 3.6	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V (O)
Output Current	IOH/IOL	±8.0 (Note 2) ±4.0 (Note 3) ±3.0 (Note 4) ±1.7 (Note 5) ±0.3 (Note 6) ±0.02 (Note 7)	mA
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 8)	ns/V

Note 2: $V_{CC} = 3.0$ to 3.6 V Note 3: $V_{CC} = 2.3$ to 2.7 V Note 4: $V_{CC} = 1.65$ to 1.95 V Note 5: $V_{CC} = 1.4$ to 1.6 V Note 6: $V_{CC} = 1.1$ to 1.3 V

Note 7: $V_{CC} = 0.9 V$ Note 8: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
				0.9	V _{CC}	_	X	V _{CC}	_	V
High-level				1.1 to 1.3	$V_{CC} \times 0.7$		<u> </u>	Vcc × 0.7		
	VIH			1.4 to 1.6	V _{CC} × 0.65	-((776	V _{CC} × 0.65		
input voltage				1.65 to 1.95	V _{CC} × 0.65		\mathcal{Q}	V _{CC} × 0.65		
				2.3 to 2.7	1.7	(-)	2-	1.7	_	
				3.0 to 3.6	2.0)		2.0	_	
				0.9	4	\rightarrow	GND	A	GND	
Low-level V _{IL} input voltage				75	>	V _{CC} × 0.3		V _{CC} × 0.3	V	
				\sum	_	Vcc × 0.35	Z L	V _{CC} × 0.35		
			1.65 to 1.95		-(V _{CC} × 0.35	<u> </u>	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$		
			G	2.3 to 2.7	—	$\overline{\Box}$	0.7	_	0.7	
			2	3.0 to 3.6	((¥	0.8	_	0.8	
			I _{OH} =-0.02 mA	0.9	0.75	$\backslash -$	_	0.75	—	
High-level VoH		V _{IN} = V _{IH or} VIL	I _{OH} = -0.3 mA	1.1 to 1.3	Vcc × 0.75)}	—	V _{CC} × 0.75		
	V _{ОН}		1 _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	/	_	V _{CC} × 0.75		V
output voltage			1 _{OH} -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
	\frown		I _{OH} = -4.0 mA	2.3 to 2.7	2.0			2.0	—	
		I _{OH} = -8.0 mA	3.0 to 3.6	2.48			2.48	—		
Low-level VoL VIN VIH		l _{OL} = 0.02 mA	0.9	_	—	0.1	—	0.1		
	\triangleright	I _{OL} = 0.3 mA	1,1 to 1.3	_	—	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		V _{CC} × 0.25		
	V _{IN} = VIH or VIL	$V_{IN} = V_{IL}$ $I_{OL} = 1.7 \text{ mA}$	1.4 to 1.6	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		V _{CC} × 0.25	V	
			l _{OL} = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
$\sim (())$)		l _{OL} = 4.0 mA	2.3 to 2.7	—	—	0.4	_	0.4	
		> (C	1 _{OL} = 8.0 mA	3.0 to 3.6	—	—	0.4	—	0.4	
Input leakage current	IIN	$V_{IN} = 0$ to 5.5V		0 to 3.6	—	—	±0.1	—	±1.0	μA
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		3.6	—	—	1.0	—	10.0	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40	Unit	
Sym	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	19.2		_	_	
			1.1 to 1.3		9.8	19.5	1.0	36.3	
			1.4 to 1.6	_	5.7	9.0	1.0	10.6	ns
			1.65 to 1.95		4.4	6.6	1.0	7.1	
			2.3 to 2.7	_	3.0	4.1	1.0	4.7	
			3.0 to 3.6		2.4	3.3	1.0	3.9	
		C _L = 15 pF, R _L = 1 MΩ	0.9	_	21.5	\sum	—	—	
			1.1 to 1.3	_ (10.9	22.8	1.0	39.4	
	t _{PLH}		1.4 to 1.6		6.2	9.9	1.0	11.9	
	t _{PHL}		1.65 to 1.95	\mathcal{A}	4.8	7.3	1.0	7.5	
			2.3 to 2.7	\geq	3.2	4.7	1.0	5.3	
			3.0 to 3.6	$\langle - \rangle$	2.6	3.6	1.0	4.1	
		C _L = 30 pF,	0.9	\sum	30.6		~4/	7 –	
			1.1 to 1.3	$\geq -$	15.0	31.4	1.0	59.4	
			1.4 to 1.6	_	8.1	13.9	1.0	16.9	
		$R_L = 1 M\Omega$	1.65 to 1.95		6.0	9.8	1.0	10.2	
			2.3 to 2.7	1	4.1	6.0	1.0	6.5	
			3.0 to 3.6		3.2	4.7	1.0	5.1	
Input capacitance	C _{IN}		3.6	_	3		_	—	pF
Power dissipation capacitance	C _{PD}	(Note 9)	0.9 to 3.6	A	6		—	—	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

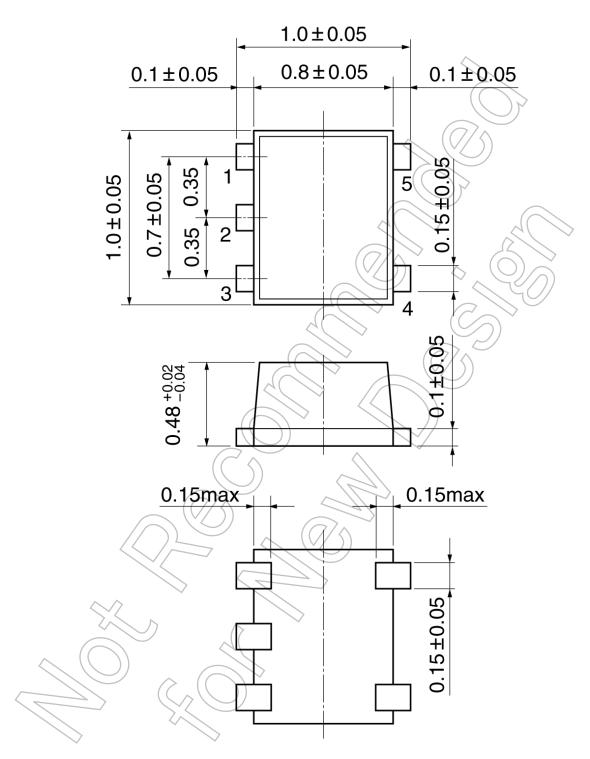
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.35

Unit: mm



Weight: 0.001 g (typ.)

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