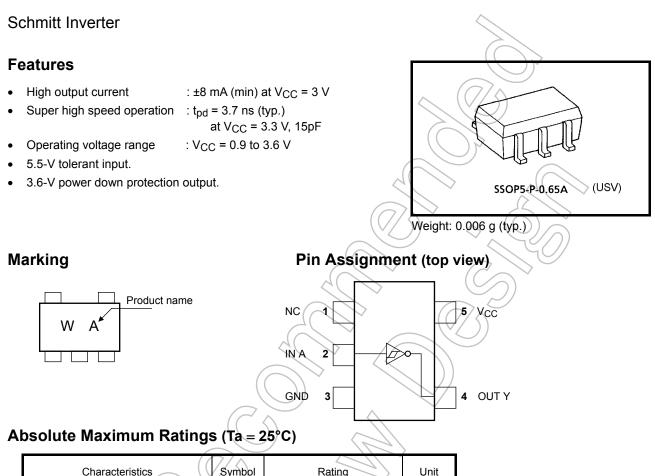
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG14FU



Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	V
DC input voltage	VIN _	-0.5 to 7.0	V
DC output voltage	Vout	-0.5 to 4.6 (Note 1)	V
	VOUI	-0.5 to V _{CC} + 0.5 (Note 2)	v
Input diode current	IIK 🔿	-20	mA
Output diode current	IOK	-20 (Note 3)	mA
DC output current	HOUT	±25	mA
DC V _{CC} /ground current		±50	mA
Power dissipation	PD	200	mW
Storage temperature	Tstg	−65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:
$$V_{CC} = 0V$$

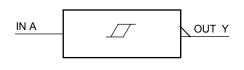
Note 3: V_{OUT} < GND

Start of commercial production 2005-08

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

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IEC Logic Symbol



А	Y
L	Н
Н	L

Truth Table

Operating Ranges

perating Ranges			(
Characteristics	Symbol	Rating	\mathcal{N}	Unit	
Supply voltage	V _{CC}	0.9 to 3.6		V	
Input voltage	V _{IN}	0 to 5.5		v	\frown
Output voltage	V _{OUT}	0 to 3.6	(Note 4)	V	$\bigwedge \bigcirc$
ouput voltage	•001	0 to V _{CC}	(Note 5)		\sim
		± 8.0	(Note 6)	\diamond ((
		±4.0	(Note 7)		40)
Output Current	I _{OH} /I _{OL}	± 3.0	(Note 8)	mA	
	10H/10L	<(<u>±1</u> ,7	(Note 9)	()	
		± 0.3	(Note 10)		
		± 0.02	(Note 11)	$\bigcirc)$	
Operating temperature	T _{opr}	-40 to 85	\frown	°C	
Note 4: $V_{CC} = 0V$	C				
Note 5: High or Low state.))			
Note 6: $V_{CC} = 3.0$ to 3.6 V	$C \wedge$		Ť		
Note 7: $V_{CC} = 2.3$ to 2.7 V		$\langle \rangle$			
Note 8: $V_{CC} = 1.65$ to 1.95 V	776		7		
Note 9: $V_{CC} = 1.4$ to 1.6 V	$\langle O \rangle$	$\overline{\Omega}$			
Note 10: V _{CC} = 1.1 to 1.3 V		$\langle \langle \rangle \rangle$			
Note 11: V _{CC} = 0.9 V					
	$\langle \langle \rangle$				
~ 2					
	\bigwedge	\checkmark			
	40				
	(())				

Electrical Characteristics

DC Characteristics

Characteristics		Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Linit
		Symbol	162	Test Condition		Min	Тур.	Max	Min	Max	- Unit
					0.9	_	4	0.73	_	0.80	
					1.1	_		0.86		0.93	
	Positive	.,			1.4			1.07)/(1.12	
	threshold voltage	VP		_	1.65		+0	1.23	_	1.25	
					2.3	4		1.66		1.68	
Threshold					3.0	- (2.14	_	2.15	V
voltage					0.9	0.18	\square		0.07	_	v
					1.1	0.26		_	0.18		
	Negative	N			1.4	0.36			0.31	\geq	
	threshold voltage	V _N		_	1.65	0.45	_	-6	0.41	> _	
					2.3	0.69			0.64) —	
					3.0	0.96	_		0.91	_	
				_	0.9	0.20	—((0.38	0.15	0.53	
					11	0.25		0.41	0.21	0.53	
Hysteresis volta		V _H		40	1.4	0.35	(\mathcal{A})	0.48	0.34	0.57	v
	ige	vн		$-\langle \langle \rangle$	1.65	0.42))	0.56	0.40	0.60	v
					2.3	0.60	\rightarrow	0.74	0.61	0.76	
					3.0	0.79	//	0.93	0.80	0.94	
				I _{OH} =-0.02 mA	0.9	0.75		_	0.75		
				I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75	—	—	V _{CC} × 0.75		
	High level	VOH	VIN=VIL	I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
				I _{OH} = ~3.0 mA	1.65 to 1.95	V _{CC} -0.45	_		V _{CC} -0.45	_	
		\sim		I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
Output voltage				I _{OH} = -8.0 mA	-3.0 to 3.6	2.48			2.48	_	V
	$\langle \rangle$			I _{OL} = 0.02 mA	0.9			0.1		0.1	
		D		I _{OL} = 0.3 mA	1.1 to 1.3			V _{CC} × 0.25		V _{CC} × 0.25	
Low level V	Low level	w level V _{OL}	VIN=VIH	lo⊾⇒1.7 mA	1.4 to 1.6			V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
		\mathcal{N}	I _{OL} = 3.0 mA	1.65 to 1.95			0.45		0.45		
			$\langle \rangle$	I _{OL} = 4.0 mA	2.3 to 2.7			0.4		0.4	
			\rightarrow	I _{OL} = 8.0 mA	3.0 to 3.6			0.4		0.4	
Input leakage c	urrent	I _{IN}	V _{IN} = 0 to	5.5 V	0 to 3.6			±0.1		±1.0	μA
Power off leaka	ige current	I _{OFF}	V _{IN} = 0 to V _{OUT} = 0		0		_	1.0	_	10.0	μΑ
Quiescent supp	ly current	ICC		_C or GND	3.6		_	1.0	_	10.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Canditian		Ta = 25°C		$Ta = -40$ to $85^{\circ}C$		1.1.0.14	
Characteristics S		Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		C _L = 10 pF,	0.9	_	27.3	_	—	_	
			1.1 to 1.3	_	13.0	22.6	1.0	35.9	
			1.4 to 1.6	_	7.5	10.5	1.0	11.3	
		$R_L = 1 M\Omega$	1.65 to 1.95	—	6.0	7.8	1.0	8.2	
			2.3 to 2.7	—	4.3	5.4	1.0	5.8	
			3.0 to 3.6	-0	3.5	4.4	1.0	4.6	
	t _{pLH} t _{pHL}	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	29.5	\mathcal{T}	—	_	ns
			1.1 to 1.3	_ (14.3	25.1	1.0	41.8	
			1.4 to 1.6		8.0	11.5	1.0	12.6	
			1.65 to 1.95	Æ	6.3	8.4	1.0	8,7	
			2.3 to 2.7	\geq	4.6	5.7	2 1.0	6.1	
			3.0 to 3.6	$(/ \rightarrow)$	3.7	4.6)1.0	5.0	
			0.9		40.5	(A)	~~~)	/ _	
			1.1 to 1.3	$\geq -$	19.6	35.7	1.0	58.1	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.4 to 1.6	—	10.7	15.8	1.0	17.6	
			1.65 to 1.95	—	7.8	10.7	1.0	11.7	
			2.3 to 2.7	_	5.4))6.9	1.0	8.1	
			3.0 to 3.6	$\langle - \rangle$	4.3	5.2	1.0	6.1	
Input capacitance	C _{IN}		3.6		3	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 12)	0.9 to 3.6		//7	—	—	—	pF

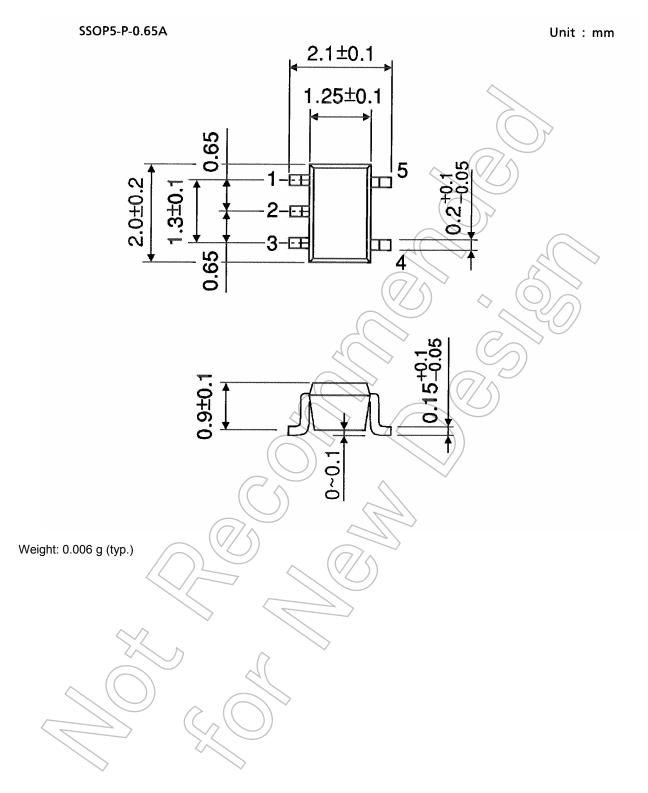
Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

TOSHIBA

Package Dimensions



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