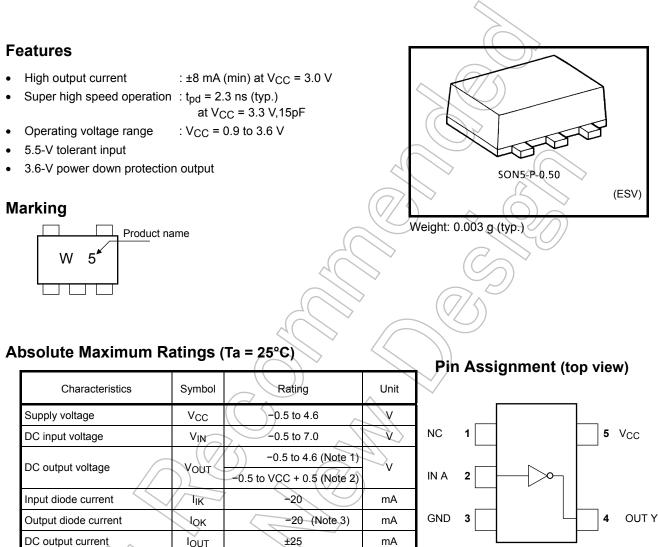
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG04FE

Inverter



Note; Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

±25

150

-65 to 150

±50

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

mΑ

mΑ

mW

°C

Note 1: $V_{CC} = 0V$

DC V_{CC}/ground current

Power dissipation

Storage temperature

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

IOUT

Icc

PD

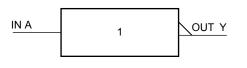
T_{stg}

Note 3: V_{OUT} < GND

TOSHIBA

IEC Logic Symbol

Truth Table



А	Y
L	Н
Н	L

Operating Ranges

berating Ranges			($\overline{\partial}$	
Characteristics	Symbol	Rating	\mathcal{N}	Unit	
Supply voltage	V _{CC}	0.9 to 3.6		V	
Input voltage	V _{IN}	0 to 5.5		V v	\frown
Output voltage	V _{OUT}	0 to 3.6	(Note 4)	v	$\langle \rangle$
Output voltage	V001	0 to V _{CC}	(Note 5)	v	
		±8.0	(Note 6)	\diamond ((
		±4.0	(Note 7)	\sim	~~ <i>()</i> /
Output Current	I _{OH} /I _{OL}	±3.0	(Note 8)	mA	
Output Current	'OH/'OL	2 17	(Note 9)	(
		±0.3	(Note 10)		
		±0.02	(Note 11)	()	
Operating temperature	T _{opr}	-40 to 85	\sim	°C	
Input rise and fall time	dt/dv	0 to 10	(Note 12)	ns/V	
Note 4: $V_{CC} = 0V$))	\bigvee		-
Note 5: High or Low state.	$C \wedge$		~		
Note 6: $V_{CC} = 3.0$ to 3.6 V	(\bigcirc)				
Note 7: $V_{CC} = 2.3$ to 2.7 V	77^				
Note 8: $V_{CC} = 1.65$ to 1.95 V	$\langle \bigcirc \rangle$				
Note 9: $V_{CC} = 1.4$ to 1.6 V	\sim	$\langle (// 5) \rangle$			
Note 10: $V_{CC} = 1.1$ to 1.3 V					
Note 11: V _{CC} = 0.9 V					
Note 12: V_{IN} = 0.8 to 2.0 V, V_{CC} =	3.0 V				
		\searrow			

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	I Test Condition			Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit		
				0.9	V _{CC}	_	K	V _{CC}	_	
				1.1 to 1.3	V _{CC} × 0.7			V _{CC} ×0.7		
High-level input VIH	VIH		_	1.4 to 1.6	V _{CC} × 0.65	-6		V _{CC} × 0.65	_	v
				1.65 to 1.95	V _{CC} × 0.65		\mathcal{D}	V _{CC} × 0.65	_	
				2.3 to 2.7	1.7	(-)	> _	1.7	—	
				3.0 to 3.6	2.0)	-	2.0	_	
				0.9	4	\searrow	GND	\mathcal{A}	GND	-
				1.1 to 1.3	75	~ _ <	V _{CC} × 0.3		V _{CC} × 0.3	
Low-level input VIL		_	1.4 to 1.6	\mathcal{D}	_	V _{CC} × 0.35	L)	$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	V	
voltage						-((V _{CC} × 0.35	>_		$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$
		G		—	$\overline{\Box}$	0.7		0.7		
				3.0 to 3.6	())0.8		0.8	
			I _{OH} =-0.02 mA	0.9	0.75	\sum	_	0.75	_	
			I _{OH} <i>= −</i> 0.3 mA	1.1 to 1.3	Vcc × 0.75)}		V _{CC} × 0.75	_	
High-level output	V _{ОН}	VIN = VIL	10H = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	/_	_	V _{CC} × 0.75	—	V
voltage			1 _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45			V _{CC} -0.45	Ι	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0			2.0	—	
			1 _{OH} = -8.0 mA	3.0 to 3.6	2.48			2.48	—	
	$\langle \rangle \rangle$		I _{OL} = 0.02 mA	0.9			0.1	_	0.1	
		\supset	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V _{CC} × 0.25	—	V _{CC} × 0.25	
Low-level output	VOL	V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
			I _{OL} = 3.0 mA	1.65 to 1.95		_	0.45	_	0.45	
$\sim (())$)		I _{OL} = 4.0 mA	2.3 to 2.7			0.4	—	0.4	
	6	> ((I _{OL} = 8.0 mA	3.0 to 3.6			0.4	—	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to	5.5 V	0 to 3.6			±0.1		±1.0	μA
Power off leakage current	IOFF	V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0	_		1.0	_	10.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		3.6	—		1.0	—	10.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Cumbol	Test Canditian		Ta = 25°C		Ta = -40	a = -40 to 85°C		
Characteristics Sym	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	18.6	_	_	—	
			1.1 to 1.3		8.7	18.4	1.0	34.2	
			1.4 to 1.6		4.9	8.5	1.0	10.0	ns
			1.65 to 1.95		3.8	6.2	1,0	6.7	
			2.3 to 2.7	_	2.6	3.9	1.0	4.4	
	tргн tрнг		3.0 to 3.6	- <	2.1	3.1	1.0	3.7	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	21.0)	—	_	
			1.1 to 1.3	_	9.8	21.5	1.0	37.1	
			1.4 to 1.6	70	5.4	9.3	1.0	11.2	
			1.65 to 1.95	F	4.2	6.9	1.0	7,1	
			2.3 to 2.7		2.8	4.4	21.0	5.0	
			3.0 to 3.6		2.3	3.4((1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		31.2	\sim	~{/)	/ _	
			1.1 to 1.3	\rightarrow	13.8	29.6	1.0	56.0	
			1.4 to 1.6	_	7.4	13.1)	1.0	15.9	
			1.65 to 1,95	_	5.6	9.2	1.0	9.6	
			2.3 to 2.7		3.7	5.7	1.0	6.1	
			3.0 to 3.6		2.9	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	X	3		—	_	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 to 3.6		6	_	—	—	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

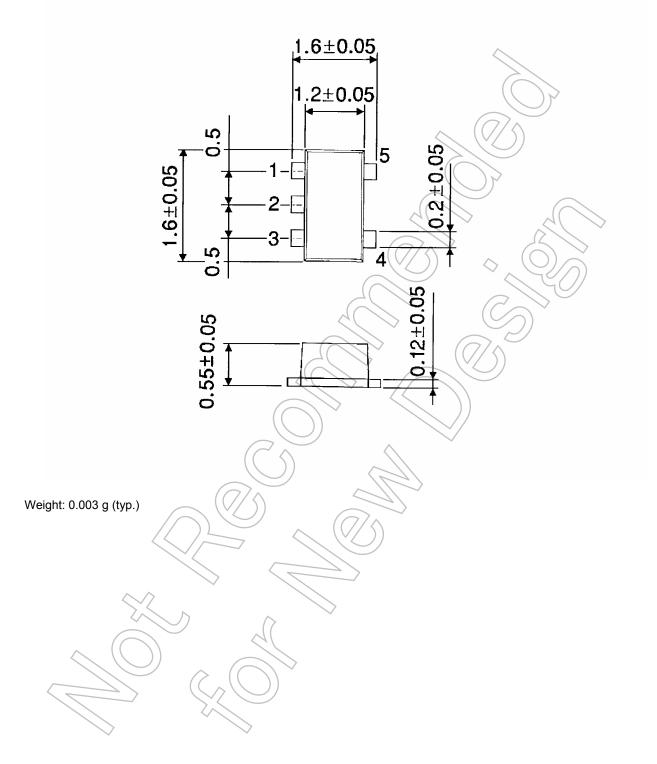
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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