TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

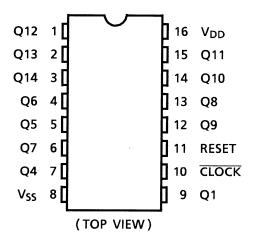
TC4020BP, TC4020BF

TC4020B 14 Stage Ripple-Carry Binary Counter/Dividers

TC4020B is 14 stage ripple carry binary counter having asynchronous clear function. The counter advances its counting stage by falling edge of $\overline{\text{CLOCK}}$ input. When RESET input is placed "H", all the circuits are reset regardless of $\overline{\text{CLOCK}}$ input making all the outputs (Q1, Q4 to Q14) to be "L".

This is most suitable for frequency dividers, control circuits and timing circuits.

Pin Assignment

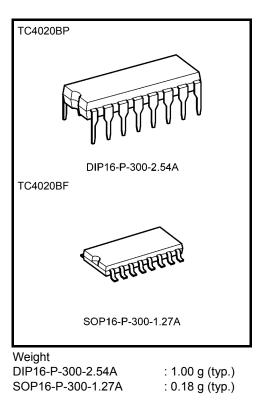


Truth Table

$\overline{CLOCK} \Delta$	RESET	Output State			
* H All Outputs = "L"					
	L No Change				
	L	Advance to Next State			

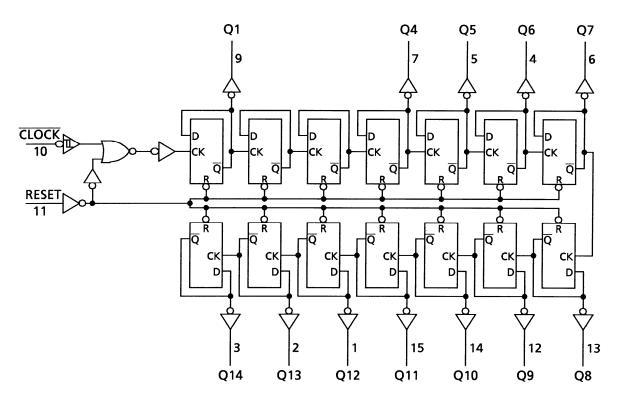
 Δ : Level change

*: Don't care



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Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V _{DD}	V_{SS} – 0.5 to V_{SS} + 20	V
Input voltage	V _{IN}	$V_{\mbox{\scriptsize SS}} - 0.5$ to $V_{\mbox{\scriptsize DD}} + 0.5$	V
Output voltage	V _{OUT}	$V_{\mbox{\scriptsize SS}} - 0.5$ to $V_{\mbox{\scriptsize DD}} + 0.5$	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOP)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	–65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V _{DD}	—	3	_	18	V
Input voltage	V _{IN}	—	0		V _{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 V$)

Characteristics Symbol		Svm-	Test Condition		-40°C		25°C			85°C			
		-		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
			I _{OUT} < 1 μΑ	5	4.95	_	4.95	5.00	_	4.95			
High-level voltage	High-level output	VOH	$ V_{IOUT} < V_{\mu A}$ $V_{IN} = V_{SS}, V_{DD}$	10	9.95	—	9.95	10.00	—	9.95	—	V	
Ŭ			VIN – VSS, VDD	15	14.95	_	14.95	15.00	_	14.95	_		
			I _{OUT} < 1 μΑ	5	—	0.05	—	0.00	0.05	—	0.05		
Low-level voltage	output	V _{OL}	$V_{IN} = V_{SS}, V_{DD}$	10	—	0.05	—	0.00	0.05		0.05	V	
			VIN - VSS, VDD	15	—	0.05	—	0.00	0.05		0.05		
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	—	-0.42	—	mA	
			V _{OH} = 2.5 V	5	-2.50	—	-2.10	-4.0	—	-1.70	—		
Output hig	h current	Iон	V _{OH} = 9.5 V	10	-1.50	—	-1.30	-2.2	—	-1.10	—		
			V _{OH} = 13.5 V	15	-4.00	—	-3.40	-9.0	—	-2.80	—		
			$V_{IN} = V_{SS}, V_{DD}$										
		I _{OL}	$V_{OL} = 0.4 V$	5	0.61	_	0.51	1.2	_	0.42	—	mA	
Output low	vcurrent		$V_{OL} = 0.5 V$	10	1.50	—	1.30	3.2	—	1.10	—		
outpution	vourient		V _{OL} = 1.5 V	15	4.00	—	3.40	12.0	—	2.80	—		
			$V_{IN} = V_{SS}, V_{DD}$										
		V _{IH}	$V_{OUT} = 0.5 V, 4.5 V$	5	3.5	_	3.5	2.75	_	3.5	_	V	
Input high	voltage		V _{OUT} = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_		
input nigh	voltage		$V_{OUT} = 1.5 V, 13.5 V$	15	11.0	_	11.0	8.25	_	11.0	—		
			$ I_{OUT} < 1 \ \mu A$										
			$V_{OUT} = 0.5 V, 4.5 V$	5	—	1.5	—	2.25	1.5		1.5		
Input low voltage		VIL	V _{OUT} = 1.0 V, 9.0 V	10	—	3.0	—	4.50	3.0		3.0	v	
			$V_{OUT} = 1.5 V, 13.5 V$	15	—	4.0	—	6.75	4.0	—	4.0		
			$ I_{OUT} < 1 \ \mu A$										
Input	"H" level	Ι _{ΙΗ}	V _{IH} = 18 V	18	_	0.1	_	10 ⁻⁵	0.1	—	1.0	μA	
current	"L" level	١ _{١L}	$V_{IL} = 0 V$	18	_	-0.1	_	-10 ⁻⁵	-0.1	—	-1.0	μη	
				5	_	5	_	0.005	5		150		
Quiescent supply current		IDD	V _{IN} = V _{SS} , V _{DD} (Note)	10	—	10	—	0.010	10		300	μA	
				15	—	20	—	0.015	20		600		

Note: All valid input combinations.

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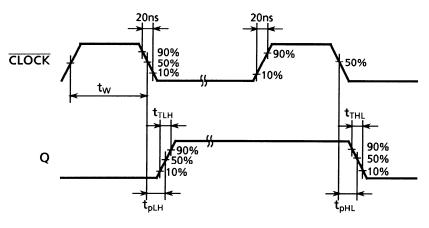
Dynamic Electrical Characteristics (Ta = 25° C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test Condition	Min	Turn	Max	Unit	
Characteristics	Зушрог		V _{DD} (V)	IVII(1	Тур.	wax	Unit
Output transition time			5	_	70	200	
(low to high)	t _{TLH}	—	10		35	100	ns
(low to high)			15	—	30	80	
Output transition time			5	_	70	200	
Output transition time (high to low)	t _{THL}	—	10		35	100	ns
(ilight to low)			15		30	80	
Propagation delay time			5		160	360	
(CLOCK -Q1)	t _{pLH}	—	10	—	80	160	ns
			15		65	130	
Propagation delay time			5		160	360	
(CLOCK -Q1)	t _{pHL}	—	10	—	80	160	ns
			15		65	130	
Propagation delay time			5	_	1000	2000	
(CLOCK -Q14)	t _{pLH}	—	10		500	1000	ns
(CEOCK -Q14)			15		400	800	
Propagation delay time			5		1000	2000	
(CLOCK -Q14)	t _{pHL}	—	10		500	1000	ns
			15	—	400	800	
Propagation delay time			5	—	150	280	
(RESET-Q)	t _{pHL}	—	10	—	70	120	ns
			15	—	50	100	
			5	3.5	10		
Max clock frequency	f _{CL}	—	10	8.0	20	—	MHz
			15	12.0	25		
Min clock pulse width			5	—	50	140	
(RESET)	t _W	—	10	—	20	60	ns
			15	_	15	40	
			5	—	100	200	
Min pulse width	t _W	—	10	—	40	80	ns
			15		30	60	
Min removal time			5	—	_	350	
(RESET- CLOCK)	t _{rem}	—	10	—	—	150	ns
			15			100	
Max clock input rise time	t _{rCL}		5				
Max clock input fall time		—	10	No limit			μS
	чог		15				
Input capacitance	C _{IN}	—			5	7.5	pF

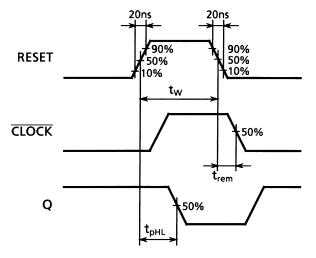
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Operating Supply Current Test Circuit

Waveform 1



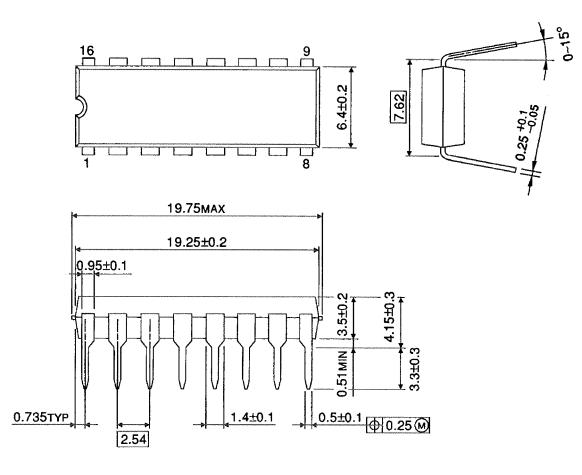
Waveform 2



Package Dimensions

DIP16-P-300-2.54A

Unit : mm



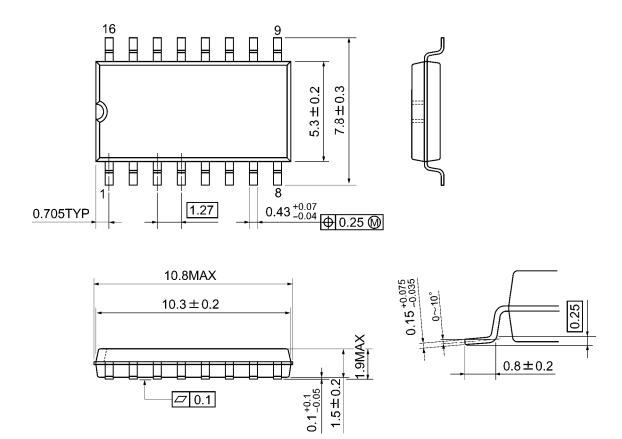
Weight: 1.00 g (typ.)



Package Dimensions

SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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